## 506396257 12/10/2020 PATENT ASSIGNMENT COVER SHEET

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UBMISSION TYPE:		NEW ASSIGNMENT		
NATURE OF CONVEYANCE:		ASSIGNMENT		
CONVEYING PARTY D	ΑΤΑ			
		Name	Execution Date	
YOICHI TAKANO			11/24/2020	
SHINICHIRO MAKI			11/24/2020	
RECEIVING PARTY DA	ATA			
Name:	MITSUM	I ELECTRIC CO., LTD.		
Street Address:	2-11-2, T	SURUMAKI		
City:	TAMA-SH	TAMA-SHI, TOKYO		
State/Country:	JAPAN			
Postal Code:	206-8567	206-8567		
Property Type	4-	Number		
Property Type		Number		
	17	<b>Number</b> 7117705		
Application Number:				
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Application Number: CORRESPONDENCE E Fax Number: Correspondence will b using a fax number, if Phone: Email: Correspondent Name: Address Line 1: Address Line 2: Address Line 2: Address Line 4: ATTORNEY DOCKET NU	DATA (2 pe sent to t provided; 21 ss H 63 SI N UMBER:	7117705 212)319-5101 the e-mail address first; if that is unsur- if that is unsuccessful, it will be sent 123194900 swindell@HHPATENT.COM OLTZ, HOLTZ & VOLEK PC 30 NINTH AVENUE UITE 1010 EW YORK, NEW YORK 10036-3744 20331 SAMUEL SWINDELL		
Application Number: CORRESPONDENCE E Fax Number: Correspondence will b using a fax number, if Phone: Email: Correspondent Name: Address Line 1: Address Line 1: Address Line 2: Address Line 4: ATTORNEY DOCKET NUME IAME OF SUBMITTER: SIGNATURE:	DATA (2 pe sent to t provided; 21 ss H 63 SI N UMBER:	212)319-5101 the e-mail address first; if that is unsul- if that is unsuccessful, it will be sent of 123194900 swindell@HHPATENT.COM OLTZ, HOLTZ & VOLEK PC 30 NINTH AVENUE UITE 1010 EW YORK, NEW YORK 10036-3744 20331 SAMUEL SWINDELL /Samuel Swindell/	via US Mail.	

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Combined Declaration and Assignment						
Title of Invention	SEMICONDUCTOR IN SYSTEM	TEGRATED CIRCUIT FOR REGULA	TOR, AND FAN MOTOR			
As the below named inventor, I hereby declare that:						
This declara		The attached application, or				
	[ ] United	<ul> <li>United States application or PCT international application number filed on</li> </ul>				
The above-identified application was made or authorized to be made by me.						
I believe that I am the original inventor or an original joint inventor of a claimed invention in the application.						
In consideration of value received, I sell and assign to <u>MITSUMI ELECTRIC CO., LTD.</u> , having a business address at <u>2-11-2</u> , <u>Tsurumaki, Tama-shi, Tokyo, 206-8567</u> , <u>JAPAN</u> , its successors, assigns or nominees, hereinafter referred to as "Assignee", my entire right, title and interest in and to said invention, and in and to all applications for patent and patents for said invention, in all countries of the world, including all divisions, reissues, continuations, substitutions and extensions thereof and all rights arising under or pursuant to any and all international agreements, treaties or laws relating to the protection of intellectual property, including rights of priority, resulting from the filing of any of said applications.						
I authorize said Assignee to file and prosecute an application or applications for patent for said invention as the applicant, and I request any official whose duty it is to issue patents, to issue any patent on said invention to said Assignee.						
I agree that on request and without further consideration, but at the expense of said Assignee, I will communicate to said Assignee or its representatives all facts known to me respecting said invention and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuing, reissue, or other applications, make all rightful oaths and declarations, and generally do everything possible to aid said Assignee to obtain and enforce proper patent protection for said invention in all countries.						
I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.						
LEGAL NAME OF INVENTOR						
Yo	ichi TAKANO	Joichi Infam	November 24, 2020			
Invent	or (printed name)	Signature	Date			

Holtz, Holtz & Volek PC 630 Ninth Avenue, Suite 1010, New York, NY 10036-3744 Tel. (212) 319-4900 Fax. (212) 319-5101

Combined Declaration and Assignment					
SEMICONDUCTOR IN SYSTEM	TEGRATED CIRCUIT FOR REGULA	ATOR, AND FAN MOTOR			
w named inventor, I her	eby declare that:				
	tached application, or				
[ ] United					
The above-identified application was made or authorized to be made by me.					
I believe that I am the original inventor or an original joint inventor of a claimed invention in the application.					
In consideration of value received, I sell and assign to <u>MITSUMI ELECTRIC CO., LTD.</u> , having a business address at <u>2-11-2</u> , <u>Tsurumaki, Tama-shi, Tokyo, 206-8567</u> , <u>JAPAN</u> , its successors, assigns or nominees, hereinafter referred to as "Assignee", my entire right, title and interest in and to said invention, and in and to all applications for patent and patents for said invention, in all countries of the world, including all divisions, reissues, continuations, substitutions and extensions thereof and all rights arising under or pursuant to any and all international agreements, treaties or laws relating to the protection of intellectual property, including rights of priority, resulting from the filing of any of said applications.					
I authorize said Assignee to file and prosecute an application or applications for patent for said invention as the applicant, and I request any official whose duty it is to issue patents, to issue any patent on said invention to said Assignee.					
I agree that on request and without further consideration, but at the expense of said Assignee, I will communicate to said Assignee or its representatives all facts known to me respecting said invention and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuing, reissue, or other applications, make all rightful oaths and declarations, and generally do everything possible to aid said Assignee to obtain and enforce proper patent protection for said invention in all countries.					
I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.					
LEGAL NAME OF INVENTOR					
inichiro MAKI	Spinichiro Maki	November 24, 2020			
or (printed name)	Signature	Date			
	SEMICONDUCTOR IN SYSTEM w named inventor, I her ation [X] The at o: [] United numbe identified application wa at I am the original inver ation of value received, idress at <u>2-11-2. Tsurur</u> nereinafter referred to a to all applications for pa divisions, reissues, con rsuant to any and all int property, including right said Assignee to file and cant, and I request any said Assignee. on request and without te to said Assignee. on request and without te to said Assignee or if y legal proceeding, sign s, make all rightful oaths o obtain and enforce pro- knowledge that any will 001 by fine or imprison	SEMICONDUCTOR INTEGRATED CIRCUIT FOR REGULA         SYSTEM         w named inventor, I hereby declare that:         ation       [X] The attached application, or         o:       [] United States application or PCT internation number			

Holtz, Holtz & Volek PC 630 Ninth Avenue, Suite 1010, New York, NY 10036-3744 Tel. (212) 319-4900 Fax. (212) 319-5101