

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
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EPAS ID: PAT6456974

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
ELI HARARI	12/19/2019
VINOD PURAYATH	01/06/2020
WU-YI HENRY CHIEN	12/20/2019
JIE ZHOU	12/19/2019
RECEIVING PARTY DATA	
Name:	SUNRISE MEMORY CORPORATION
Street Address:	46831 LAKEVIEW BLVD.
City:	FREMONT
State/Country:	CALIFORNIA
Postal Code:	94538
PROPERTY NUMBERS Total: 2	
Property Type	Number
PCT Number:	US2020065670
Application Number:	17125477
CORRESPONDENCE DATA	
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<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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Email:	patents@vlplawgroup.com
Correspondent Name:	VLP LAW GROUP, LLP
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Address Line 4:	PALO ALTO, CALIFORNIA 94301
ATTORNEY DOCKET NUMBER:	SRM-025 US & WO
NAME OF SUBMITTER:	ANGELA WENDEL
SIGNATURE:	/angela wendel/
DATE SIGNED:	12/17/2020
Total Attachments: 4	
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PATENT

REEL: 054685 FRAME: 0601


ASSIGNMENT

I, Eli HARARI, residing at Saratoga, California, am an inventor of the subject matter described and claimed in an application for United States Letters Patent entitled "PROCESS FOR PREPARING A CHANNEL REGION OF A THIN-FILM TRANSISTOR IN A 3-DIMENSIONAL THIN-FILM TRANSISTOR ARRAY," (hereinafter referred to as "Application") provisional application number 62/950,390, filed December 19, 2019;

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I hereby assign and transfer, and hereby agree to assign and transfer, to Sunrise Memory Corporation, (hereinafter referred to as "Assignee"), a corporation organized under the laws of Delaware, with an address of 46831 Lakeview Blvd., Fremont, CA 94538, the entire right, title and interest in and to any and all inventions and improvements that are disclosed in the Application (hereinafter referred to, individually and collectively, as "Invention") and to the Application and all patents which may be granted therefrom, and all related applications (e.g., provisional applications, divisional applications, reissues, reexaminations, continuation applications, continuation-in-part applications, extensions thereof, design applications, applications in foreign countries, international applications, national phase applications, and the like). I hereby authorize and request the Commissioner of Patents and Trademarks to issue to Assignee all patents, for the Invention, or patents resulting therefrom.

I also hereby assign and transfer, and hereby agree to assign and transfer, to Assignee all right, title and interest to any and all applications for patent for the Invention throughout the world, including all rights of priority resulting from the filing of the Application or any related application, including but not limited to: the right to file applications and obtain patents, utility models, industrial models and designs for the Invention throughout the world, the right to claim priority from the Application or any related application when filing applications and obtaining patents, utility models, industrial models and designs for the Invention throughout the world.

I further agree to execute any and all powers of attorney, applications, assignments, declarations, affidavits, and any other papers in connection therewith, and generally do everything possible and necessary to perfect all rights, title and interest hereby assigned and transferred, and hereby agreed to be assigned and transferred, in and to Assignee.


Eli HARARI

12.19.19
Date

For and on behalf of Sunrise Memory Corporation


Signature

12/20/19
Date

Name: NORA KEDEM
Title: Operations


ASSIGNMENT

I, Vinod Purayath, residing at Sedona, Arizona, am an inventor of the subject matter described and claimed in an application for United States Letters Patent entitled "PROCESS FOR PREPARING A CHANNEL REGION OF A THIN-FILM TRANSISTOR IN A 3-DIMENSIONAL THIN-FILM TRANSISTOR ARRAY," (hereinafter referred to as "Application") provisional application number 62/950,390, filed December 19, 2019;

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Vinod PURAYATH

01/06/2020

Date

For and on behalf of Sunrise Memory Corporation



Signature

01/06/2020

Date

Name: NOAM KEDEN
Title: Operations

ASSIGNMENT

I, Wu-Yi Henry CHIEN, residing at San Jose, California, am an inventor of the subject matter described and claimed in an application for United States Letters Patent entitled "PROCESS FOR PREPARING A CHANNEL REGION OF A THIN-FILM TRANSISTOR IN A 3-DIMENSIONAL THIN-FILM TRANSISTOR ARRAY," (hereinafter referred to as "Application") provisional application number 62/950,390, filed December 19, 2019;

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Wu-Yi Henry Chien
Wu-Yi Henry CHIEN

12/20/2019
Date

For and on behalf of Sunrise Memory Corporation

Noam Kedem
Signature

12/20/2019
Date

Name: NOAM KEDEM
Title: Operator

ASSIGNMENT

I, Jie ZHOU, residing at San Jose, California, am an inventor of the subject matter described and claimed in an application for United States Letters Patent entitled "PROCESS FOR PREPARING A CHANNEL REGION OF A THIN-FILM TRANSISTOR IN A 3-DIMENSIONAL THIN-FILM TRANSISTOR ARRAY," (hereinafter referred to as "Application") provisional application number 62/950,390, filed December 19, 2019;

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Jie Zhou
Jie ZHOU

12/19/2019
Date

For and on behalf of Sunrise Memory Corporation

Noam Kedem
Signature

12/20/2019
Date

Name: NOAM KEDM
Title: Operations