

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

EPAS ID: PAT6466221

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
SASIKANTH MANIPATRUNI	01/31/2020
YUAN-SHENG FANG	01/29/2020
ROBERT MENEZES	01/30/2020
RAJEEV KUMAR DOKANIA	01/29/2020
GAURAV THAREJA	01/28/2020
RAMAMOORTHY RAMESH	01/31/2020
AMRITA MATHURIYA	01/30/2020
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	KEPLER COMPUTING INC.
<b>Street Address:</b>	180 STEUART STREET #192524
<b>City:</b>	SAN FRANCISCO
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	94105
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	17129824
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	5039688233
<b>Email:</b>	kcampbell@ghmip.com
<b>Correspondent Name:</b>	GREEN, HOWARD, & MUGHAL LLP
<b>Address Line 1:</b>	5 CENTERPOINTE DR.
<b>Address Line 2:</b>	SUITE 400
<b>Address Line 4:</b>	LAKE OSWEGO, OREGON 97035
<b>ATTORNEY DOCKET NUMBER:</b>	12.P012US
<b>NAME OF SUBMITTER:</b>	KATHERINE R. CAMPBELL
<b>SIGNATURE:</b>	/Katherine R. Campbell/
<b>DATE SIGNED:</b>	12/23/2020

PATENT

**Total Attachments: 14**

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**ASSIGNMENT**

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

**Sasikanth Manipatruni, Yuan-Sheng Fang, Robert Menezes  
Rajeev Kumar Dokania, Gaurav Thareja, Ramamoorthy Ramesh**

hereby sell, assign, and transfer to:

**Kepler Computing Inc.**

a corporation of Delaware, having a principal place of business at 135 Dore Street, San Francisco, CA, 94103, USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

**LOW POWER FERROELECTRIC BASED MAJORITY LOGIC GATE MULTIPLIER**

**(I hereby authorize and request any attorney having appropriate authority from the assignee to insert on the designated lines below, the filing date and application number of said application when known.)**

which was filed on December 21, 2020 as

United States Application Number 17/129,824 and

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below,

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

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<p>DocuSigned by: <i>Sasikanth Manipatruni</i> 52D4CE762AC646D</p> <hr/>	<p>1/31/2020</p> <hr/>
<p>Sasikanth Manipatruni</p>	<p>Date signed</p>
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<p>Yuan-Sheng Fang</p>	<p>Date signed</p>
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<p>Robert Menezes</p>	<p>Date signed</p>
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<p>Rajeev Kumar Dokania</p>	<p>Date signed</p>
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<p>Ramamoorthy Ramesh</p>	<p>Date signed</p>

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_____ Sasikanth Manipatruni	_____ Date signed
 _____ Yuan-Sheng Fang	_____ 1/29/2020 _____ Date signed
_____ Robert Menezes	_____ Date signed
_____ Rajeev Kumar Dokania	_____ Date signed
_____ Gaurav Thareja	_____ Date signed
_____ Ramamoorthy Ramesh	_____ Date signed

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Date signed

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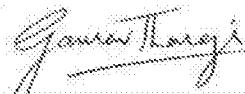
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Gaurav Thareja

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28 Jan 2020

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Date signed

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Ramamoorthy Ramesh

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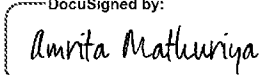
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