

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
MING-XUN WANG	12/25/2020
CHIH-HAO CHEN	12/25/2020
JI-JR LUO	12/25/2020
RECEIVING PARTY DATA	
Name:	ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.
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Property Type	Number
Application Number:	17149689
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NAME OF SUBMITTER:	CHING-YI LU
SIGNATURE:	/Ching-Yi Lu/
DATE SIGNED:	01/14/2021
Total Attachments: 2	
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ASSIGNMENT

Title of Invention: **POST OVER-ERASE CORRECTION METHOD WITH AUTO-ADJUSTING VERIFICATION AND LEAKAGE DEGREE DETECTION**

Whereas, the undersigned inventor(s) has/have made certain inventions, improvements, and discoveries (herein referred to as the "Invention") disclosed in the above-identified patent application;

Whereas, **ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.**, having a place of business at **No. 23, Industry E. Road IV, Hsinchu Science Park, Hsinchu 30077, Taiwan** (herein referred to as "**ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.**"), desires to acquire, and each undersigned inventor desires to grant to **ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.**, the entire worldwide right, title, and interest in and to the Invention and in and to any and all patent applications and patents directed thereto;

Now, therefore, for good and valuable consideration, the receipt and sufficiency thereof being hereby acknowledged, each undersigned inventor ("ASSIGNOR") hereby sells or has sold, assigns or has assigned, and otherwise transfers or has transferred to **ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.** (the "ASSIGNEE"), its successors, legal representatives, and assigns, the entire worldwide right, title, and interest in and to the Invention, the above-identified United States patent application, and any and all other patent applications and patents for the Invention which may be applied for or granted therefor in the United States and in all foreign countries and jurisdictions, including all divisions, continuations, reissues, reexaminations, renewals, extensions, counterparts, substitutes, and extensions thereof, and all rights of priority resulting from the filing of such applications and granting of such patents. In addition, each undersigned inventor hereby authorizes and requests the Director of the United States Patent and Trademark Office to issue any United States Patent, and foreign patent authorities to issue any foreign patent, granted for the Invention, to **ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.**, its successors, legal representatives, and assigns, the entire worldwide right, title, and interest in and to the same to be held and enjoyed by **ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.**, its successors, legal representatives, and assigns to the full end of the terms for which any and all such patents may be granted, as fully and entirely as would have been held and enjoyed by the undersigned had this Assignment not been made; and each undersigned inventor agrees to execute any and all documents and instruments and perform all lawful acts reasonably related to recording this Assignment or perfecting title to the Invention and all related patents and applications, in **ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.**, its successors, legal representatives, and assigns, whenever requested by **ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.**, its successors, legal representatives, or assigns.

Each undersigned inventor acknowledges their prior and ongoing obligations to sell, assign, and transfer the rights under this Assignment to **ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.** and is unaware of any reason why they may not have the full and unencumbered right to sell, assign, and transfer the rights hereby sold, assigned, and transferred, and has not executed, and will not execute, any document or instrument in conflict herewith. Each undersigned inventor also hereby grants **ELITE SEMICONDUCTOR MICROELECTRONICS TECHNOLOGY INC.**, its successors, legal representatives, and assigns, the right to insert in this Assignment any further identification (including, but not limited to, patent Application Number) which may be necessary or desirable for recordation of this Assignment.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.

(1) Legal Name of Inventor: WANG, MING-XUN

Signature: WANG, MING-XUN Date: Dec. 25, 2020

(2) Legal Name of Inventor: CHEN, CHIH-HAO

Signature: Chen, chih-Hao Date: Dec. 25, 2020

(3) Legal Name of Inventor: LUO, JI-JR

Signature: Luo, JI-JR Date: Dec. 25, 2020