

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT6503522

|                                                                                                                                                                                                 |                                          |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------|
| <b>SUBMISSION TYPE:</b>                                                                                                                                                                         | NEW ASSIGNMENT                           |
| <b>NATURE OF CONVEYANCE:</b>                                                                                                                                                                    | ASSIGNMENT                               |
| <b>CONVEYING PARTY DATA</b>                                                                                                                                                                     |                                          |
| <b>Name</b>                                                                                                                                                                                     | <b>Execution Date</b>                    |
| HIROYUKI TEZUKA                                                                                                                                                                                 | 12/09/2020                               |
| MASAMI KURODA                                                                                                                                                                                   | 12/10/2020                               |
| <b>RECEIVING PARTY DATA</b>                                                                                                                                                                     |                                          |
| <b>Name:</b>                                                                                                                                                                                    | SONY SEMICONDUCTOR SOLUTIONS CORPORATION |
| <b>Street Address:</b>                                                                                                                                                                          | 4-14-1 ASAHI-CHO                         |
| <b>Internal Address:</b>                                                                                                                                                                        | ATSUGI-SHI                               |
| <b>City:</b>                                                                                                                                                                                    | KANAGAWA                                 |
| <b>State/Country:</b>                                                                                                                                                                           | JAPAN                                    |
| <b>Postal Code:</b>                                                                                                                                                                             | 2430014                                  |
| <b>PROPERTY NUMBERS Total: 1</b>                                                                                                                                                                |                                          |
| <b>Property Type</b>                                                                                                                                                                            | <b>Number</b>                            |
| Application Number:                                                                                                                                                                             | 17250417                                 |
| <b>CORRESPONDENCE DATA</b>                                                                                                                                                                      |                                          |
| <b>Fax Number:</b>                                                                                                                                                                              | (312)803-4960                            |
| <i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i> |                                          |
| <b>Email:</b>                                                                                                                                                                                   | docketing@chiplawgroup.com               |
| <b>Correspondent Name:</b>                                                                                                                                                                      | CHIP LAW GROUP                           |
| <b>Address Line 1:</b>                                                                                                                                                                          | 505 N LAKESHORE DR                       |
| <b>Address Line 2:</b>                                                                                                                                                                          | SUITE 250                                |
| <b>Address Line 4:</b>                                                                                                                                                                          | CHICAGO, ILLINOIS 60611                  |
| <b>ATTORNEY DOCKET NUMBER:</b>                                                                                                                                                                  | SYP328497US01                            |
| <b>NAME OF SUBMITTER:</b>                                                                                                                                                                       | PRAMOD CHINTALAPOODI                     |
| <b>SIGNATURE:</b>                                                                                                                                                                               | /Pramod Chintalapoodi/                   |
| <b>DATE SIGNED:</b>                                                                                                                                                                             | 01/19/2021                               |
| <b>Total Attachments: 2</b>                                                                                                                                                                     |                                          |
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| source=SYP328497US01_USFP#page2.tif                                                                                                                                                             |                                          |

**DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN  
APPLICATION DATA SHEET (37 CFR 1.76) AND ASSIGNMENT FOR SINGLE ASSIGNEE**

|                           |                |
|---------------------------|----------------|
| <b>Title of Invention</b> | MEMORY CIRCUIT |
| <b>Docket Number</b>      | SYP328497US01  |

As the below named inventor, I hereby declare that:

This declaration is directed to: ☐ The attached application, or  
☒ United States application or PCT international application number  
PCT/JP2019/029118 filed on 25 July 2019.

The above-identified application was made or authorized to be made by me.

I believe that I am the original inventor or an original joint inventor of a claimed invention in the application.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.

WHEREAS, Sony Semiconductor Solutions Corporation

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hereinafter referred to as "ASSIGNEE") having a place of business at:  
4-14-1 Asahicho, Atsugi-shi, Kanagawa 243-0014, Japan

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desires to acquire the entire right, title and interest in said invention and the above-identified United States patent application;

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, I hereby assign to the ASSIGNEE, the entire right, title and interest in said invention and in the above-identified United States patent application and in all divisions, continuations and continuations-in-part of said application, and reissues, extensions and renewals of Letters Patent granted thereon, and in all corresponding patent applications filed in countries foreign to the United States ("foreign countries") and corresponding international patent applications, and in all Letters Patents issuing on any such patent applications in the United States and foreign countries;

I hereby assign to the ASSIGNEE the right to file patent applications in foreign countries on said invention in its own name and the right to claim priority to the above-identified United States patent application under the terms of the International Convention and any other relevant treaties;

I hereby authorize and request the United States Patent & Trademark Office and officials in patent offices in foreign countries to issue any and all of said Letters Patent to the ASSIGNEE as the assignee of my entire right, title and interest in and to the same, for the sole use and behoof of the ASSIGNEE, its successors, assigns, and legal representatives, to the full end of the term for which said Letters Patent may be granted; and

Further, I agree that, without further consideration, I will communicate to the ASSIGNEE any facts known to me respecting said invention, and testify in any legal proceeding, sign all lawful papers, execute all divisional, continuation, continuation-in-part, substitute, renewal and reissue applications, execute all necessary assignment papers to cause said Letters Patent to be issued to the ASSIGNEE, make all rightful oaths, and, perform all lawful acts to aid the ASSIGNEE, its successors and assigns, to obtain and enforce Letters Patent for said invention in the United States and foreign countries.

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**LEGAL NAME OF INVENTOR**

Inventor: HIROYUKI TEZUKA Date: 2020/12/9

Signature: /Hiroyuki Tezuka/

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**LEGAL NAME OF INVENTOR**

Inventor: MASAMI KURODA Date: Dec 10, 2020

Signature: / Masami Kuroda /