# PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT6529463

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

#### **CONVEYING PARTY DATA**

Name	Execution Date
NXP B.V.	01/15/2021

## **RECEIVING PARTY DATA**

Name:	FUTURE LINK SYSTEMS, LLC
Street Address:	3945 FREEDOM CIRCLE
Internal Address:	SUITE 900
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95054

#### **PROPERTY NUMBERS Total: 22**

Property Type	Number
Patent Number:	8639949
Patent Number:	7092400
Patent Number:	8780705
Patent Number:	10412046
Patent Number:	9483209
Patent Number:	9847934
Patent Number:	9940270
Patent Number:	10033607
Patent Number:	10003677
Patent Number:	8238333
Patent Number:	8358589
Patent Number:	8638800
Patent Number:	8359346
Patent Number:	8694740
Patent Number:	8775699
Patent Number:	8837322
Patent Number:	9621483
Patent Number:	9952995
Patent Number:	9674032

PATENT REEL: 055115 FRAME: 0491

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Property Type	Number
Patent Number:	9438537
Patent Number:	9817784
Patent Number:	9851941

#### CORRESPONDENCE DATA

#### Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

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**Email:** montereydocketing@longitudelicensing.ie

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Address Line 2: FIRST FLOOR, BLACKTHORN EXCHANGE

Address Line 4: DUBLIN, IRELAND D18 P3Y9

ATTORNEY DOCKET NUMBER:	NXP TO FLS 2020 INTAKE
NAME OF SUBMITTER:	RONAN O'BYRNE
SIGNATURE:	/Ronan O'Byrne/
DATE SIGNED:	02/02/2021

#### **Total Attachments: 7**

source=2020-12-22 - Patent Assignment - NXP to Future Link Systems (EXECUTED)#page1.tif source=2020-12-22 - Patent Assignment - NXP to Future Link Systems (EXECUTED)#page2.tif source=2020-12-22 - Patent Assignment - NXP to Future Link Systems (EXECUTED)#page3.tif source=2020-12-22 - Patent Assignment - NXP to Future Link Systems (EXECUTED)#page4.tif source=2020-12-22 - Patent Assignment - NXP to Future Link Systems (EXECUTED)#page5.tif source=2020-12-22 - Patent Assignment - NXP to Future Link Systems (EXECUTED)#page6.tif source=2020-12-22 - Patent Assignment - NXP to Future Link Systems (EXECUTED)#page7.tif

PATENT REEL: 055115 FRAME: 0492

#### ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, NXP B.V., a Netherlands corporation ("Assignor"), does hereby assign, transfer, and convey unto Future Link Systems, LLC, a Delaware Corporation ("Assignee"), all rights, title, and interest that exist as of December 22, 2020 and may exist in the future in and to any and all of the following (collectively, the "Patent Rights"):

- (a) the patent applications and patents listed in the attached Schedule A.1 (the "Patents");
- (b) all patents, divisionals, continuations, continuations-in-part, reissues, reexaminations and extensions thereof and all pending applications therefor, and all foreign counterparts thereof (including statutory invention registrations) and all pending applications therefor, and any patents resulting therefrom for all Patents in (a) above.
- (c) all items in any of the foregoing categories (a) and (b), whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;
- (d) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (c), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;
- (e) all claims for damages, accounting and information, and other secondary claims arising out of the past and current infringement of the Patents and/or any item in any of the foregoing categories (b) through (d) by third parties which have accrued with Assignor or to which Assignor otherwise holds title; and
- (f) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (e).

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignce to the entire interest therein.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at	Austin, Texas
on <u>January 15, 2021</u>	

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#### Schedule 2

ASSIGNOR: NXP B.V.

Name: Jennifer Wuamett Title: General Councel, NXP B.V. (Signature MUST be attested) ATTESTATION OF SIGNATURE (PURSUANT TO 28 U.S.C. § 1746) The undersigned witnessed the signature of Jennifer Wuamett to the above Assignment of Patent Rights on behalf of NXP B.V. and makes the following statements: 1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so, 2. Jennifer Wuamett is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on January 15 2021 to execute the above Assignment of Patent Rights on behalf of NXP B.V. 3. The Annual Subscribed to the above Assignment of Patent Rights on behalf of NXP B.V. I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

Assignce hereby declares the acceptance of the assignment of Patent Rights.

ASSIGNEE: Future Link Systems, LLC

Name: Richard Misia

Title: General Manager FLS

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NXP Ref.	Country	Patent Number	Application No.	File Date	Title
008055US1	us	8639949	12/668961	7/16/2008	DEVICE WITH A SECURE VIRTUAL MACHINE
008055CN1	CN	200880025189.2	200880025189.2	7/16/2008	DEVICE WITH A SECURE VIRTUAL MACHINE
008055DE1	DE	602008035334.6	08789328.5	7/16/2008	DEVICE WITH A SECURE VIRTUAL MACHINE
008055EP1	EP		07112858.1	7/20/2007	DEVICE WITH A SECURE VIRTUAL MACHINE
008055EP2	EP	2183695	08789328.5	7/16/2008	DEVICE WITH A SECURE VIRTUAL MACHINE
008055FR1	FR	2183695	08789328.5	7/16/2008	DEVICE WITH A SECURE VIRTUAL MACHINE
008055GB1	GB	2183695	08789328.5	7/16/2008	DEVICE WITH A SECURE VIRTUAL MACHINE
008055WO02	РСТ		IB2008/052859	7/16/2008	DEVICE WITH A SECURE VIRTUAL MACHINE
DE010079DE	DE	50212947.6	02100294.4	3/26/2002	METHOD OF TRANSMITTING DATA THROUGH A DATA BUS
DE010079DEN	DE		10115118.7	3/27/2001	METHOD OF TRANSMITTING DATA THROUGH A DATA BUS
DE010079EP	ÉP	1246043	02100294.4	3/26/2002	METHOD OF TRANSMITTING DATA THROUGH A DATA BUS
DE010079FR	FR	1246043	02100294.4	3/26/2002	METHOD OF TRANSMITTING DATA THROUGH A DATA BUS
DE010079GB	GB	1246043	02100294.4	3/26/2002	METHOD OF TRANSMITTING DATA THROUGH A DATA BUS
					METHOD OF TRANSMITTING DATA THROUGH A
DE010079JP	JP		2002-89082	3/27/2002	DATA BUS METHOD OF TRANSMITTING
DE010079US	US	7092400	10/104913	3/22/2002	DATA THROUGH A DATA BUS APPARATUS,
CP41954ES-US02	US	8780705	13/258807	4/30/2009	COMMUNICATIONS SYSTEM AND METHOD FOR OPTIMIZING DATA PACKET FLOW

APPARÂTUS, COMMUNICAT SYSTEM AND METHOD FOR OPTIMIZING D. PACKET FLOW A METHOD OF A DEVICE FOR UPDATING A MULTIPLE-PROCESSING ENTITY PACKE MANAGEMENT SYSTEM, AND ASSOCIATED COMPUTER PROGRAM PRODUCT INTERFACE SYSTEM AND DN30941EH-US01 US 9483209 14/492601 9/22/2014 METHOD	ATA V , AND I
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UN30941EH-US01 US 9483209 14/492601 9/22/2014 METHOD	
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REORDERING FLOW-BASED	IN
DN30982TA-US01 US 9847934 14/481277 9/9/2014 NETWORKS	
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DN31070NH-US01 US 9940270 14/838601 8/28/2015 INTERCONNEC	т .
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DN31123ES-US02 US 10033607 14/972955 12/17/2015 AND METHOD	
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DN31123ES-R001 RO A201500729 10/13/2015 AND METHOD	VI
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APPLICATION	1
VERIFICATION	ATA
DN31270ES-US01 US 10003677 15/011604 1/31/2016 PROCESSOR	
DN31270ES-US01 US 10003677 15/011604 1/31/2016 PROCESSOR METHOD FOR	
TRANSMITTING	.
DATA AND A	·
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NC10036EH-US02 US 8238333 12/302227 5/29/2006 CAPABILITIES	1
NC10036EH-US02 US 8238333 12/302227 5/29/2006 CAPABILITIES	
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					BUFFER MODULE,
					RECEIVER, DEVICE
					AND BUFFERING
					METHOD USING
NC45321ES-US02	US	8358589	12/865140	2/8/2008	WINDOWS
					BUFFER MODULE,
	1			Į.	RECEIVER, DEVICE
			4		AND BUFFERING
1				1	METHOD USING
NC45321ES-DE05	DE	602008007785.3	08709969.3	2/8/2008	WINDOWS
					BUFFER MODULE
		1			RECEIVER, DEVICE
	1	-		1	AND BUFFERING
i.		1	1		METHOD USING
NC45321ES-EP03	EP	EP2250775	08709969.3	2/8/2008	WINDOWS
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			ì		BUFFER MODULE,
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NO45304EC EDO4	ED	0050775	00700000	01010000	METHOD USING
NC45321ES-FR04	FR	2250775	08709969.3	2/8/2008	WINDOWS
					BUFFER MODULE,
	ì		}		RECEIVER, DEVICE
	1			i.	AND BUFFERING
					METHOD USING
NC45321ES-GB06	GB	2250775	087099693	2/8/2008	WINDOWS
	1				BUFFER MODULE,
					RECEIVER, DEVICE
					AND BUFFERING
			PCT/IB2008/050		METHOD USING
NC45321ES-WO01	PCT	Sanaa san	463	2/8/2008	WINDOWS
	1				TECHNIQUE FOR
					GENERATING
					HASH-TUPLE
		}			INDEPENDENT OF
					PRECEDENCE
					ORDER OF APPLIED
NM45620TC-US01	us	8638800	12/418259	4/3/2009	RULES
	1	1	( and TT ) Manual C	7/3/2008	HASH FUNCTION
					0
NM45632HH-US01	us	8359346	10/610757	44/5/0000	FOR HARDWARE
19191-10001	03	0338340	12/612757	11/5/2009	IMPLEMENTATIONS
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				1	SYSTEM AND
				1	METHOD FOR
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NM45677EC-US02	US	8694740	13/508091	11/6/2009	COUNTERS
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	1			j.	COUNTERS ARRAY
	1			1	SYSTEM AND
				4	METHOD FOR
			PCT/IB2009/054		UPDATING
NM45677EC-WO01	PCT		941	11/6/2009	COUNTERS
					READ STACKING
					FOR DATA
		٠.		ŀ	PROCESSOR
NM45873HH-US01	us	8775699	13/038054	3/1/2011	INTERFACE
		0110000	(3)030034	1 31.1/2011	LINICREACE

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NM45939HH-US01	US	8837322	13/164009	6/20/2011	METHOD AND APPARATUS FOR SNOOP-AND-LEARN INTELLIGENCE IN DATA PLANE
NIMACA AAFELLUGGO	110			4	ETHERCAT PACKET FORWARDING WITH DISTRIBUTED
NM46141EH-US02	US	9621483	14/409322	7/2/2012	CLOCKING
NM46141EH-WO01	РСТ		PCT/IB2012/053 360	7/2/2012	ETHERCAT PACKET FORWARDING WITH DISTRIBUTED CLOCKING
					REDUNDANT PACKET FORWARDING
NM46146EH-US02	US	9952995	14/441188	11/27/2012	SYSTEM
NM46146EH-WO01	РСТ		PCT/IB2012/056 766	11/27/2012	REDUNDANT PACKET FORWARDING SYSTEM
				The state of the s	REAL-TIME DISTRIBUTED NETWORK MODULE, REAL- TIME DISTRIBUTED NETWORK AND METHOD
NM46148ES-US02	US	9674032	14/348254	11/4/2011	THEREFOR
NM46148ES-CN03	CN	201180074651.X	201180074651.X	11/4/2011	REAL-TIME DISTRIBUTED NETWORK MODULE, REAL- TIME DISTRIBUTED NETWORK AND METHOD THEREFOR
NM46148ES-DE05	DE	602011064576.5	11874891.2	11/4/2011	REAL-TIME DISTRIBUTED NETWORK MODULE, REAL- TIME DISTRIBUTED NETWORK AND METHOD THEREFOR
NM46148ES-EP04	EP	2774336	11874891.2	11/4/2011	REAL-TIME DISTRIBUTED NETWORK MODULE, REAL- TIME DISTRIBUTED NETWORK AND METHOD THEREFOR

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NM46148ES-WO01	PCT		PCT/lB2011/054 925	11/4/2011	REAL-TIME DISTRIBUTED NETWORK MODULE, REAL- TIME DISTRIBUTED NETWORK AND METHOD THEREFOR A Method For Cut
NM46219EH-US02	US	9438537	14/406954	7/3/2012	Through Forwarding Data Packets Between Electronic Communication Devices
NM46219EH-WO01	PCT		PCT/IB2012/053 383	7/3/2012	A Method For Cut Through Forwarding Data Packets Between Electronic Communication Devices
NS30195EH-US02	US	9817784	14/778126	3/22/2013	MULTI-PORT TRANSMITTER DEVICE FOR TRANSMITTING AT LEAST PARTLY REDUNDANT DATA, AN ASSOCIATED CONTROL SYSTEM, AN ASSOCIATED METHOD AND AN ASSOCIATED COMPUTER PROGRAM PRODUCT
NS30195EH-WO01	PCT		PCT/IB2013/052 306	3/22/2013	MULTI-PORT TRANSMITTER DEVICE FOR TRANSMITTING AT LEAST PARTLY REDUNDANT DATA, AN ASSOCIATED CONTROL SYSTEM, AN ASSOCIATED METHOD AND AN ASSOCIATED COMPUTER PROGRAM PRODUCT
NS30307EH-US01	US	9851941	14/307864	6/18/2014	METHOD AND APPARATUS FOR HANDLING INCOMING DATA FRAMES

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PATENT REEL: 055115 FRAME: 0499