

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT6530080

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
TIANHONG YAN	01/29/2021
SCOTT BRAD HERNER	01/29/2021
JIE ZHOU	01/29/2021
WU-YI HENRY CHIEN	01/28/2021
ELI HARARI	01/29/2021
RECEIVING PARTY DATA	
Name:	SUNRISE MEMORY CORPORATION
Street Address:	46831 LAKEVIEW BLVD.
City:	FREMONT
State/Country:	CALIFORNIA
Postal Code:	94538
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17161504
CORRESPONDENCE DATA	
Fax Number:	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	4086205501
Email:	patents@vlplawgroup.com
Correspondent Name:	VLP LAW GROUP, LLP
Address Line 1:	555 BRYANT STREET SUITE 820
Address Line 4:	PALO ALTO, CALIFORNIA 94301
ATTORNEY DOCKET NUMBER:	SRM-024 US X1
NAME OF SUBMITTER:	ANGELA WENDEL
SIGNATURE:	/angela wendel/
DATE SIGNED:	02/02/2021
Total Attachments: 5	
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ATTORNEY DOCKET NO.
SRM-024 US X1

ASSIGNMENT

ASSIGNMENT

I, Tianhong Yan, am an inventor of the subject matter described and claimed in a provisional patent application entitled "VERTICAL THIN-FILM TRANSISTOR AND APPLICATION AS BIT-LINE CONNECTOR FOR 3-DIMENSIONAL MEMORY ARRAYS," (hereinafter referred to as "Application") application serial number 17/161,504, filed January 28, 2021;

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I hereby assign and transfer, and hereby agree to assign and transfer, to Sunrise Memory Corporation, (hereinafter referred to as "Assignee"), a corporation organized under the laws of Delaware, with an address of 46831 Lakeview Blvd., Fremont, California 94538, the entire right, title and interest in and to any and all inventions and improvements that are disclosed in the Application (hereinafter referred to, individually and collectively, as "Invention") and to the Application and all patents which may be granted therefrom, and all related applications (e.g., provisional applications, divisional applications, reissues, reexaminations, continuation applications, continuation-in-part applications, extensions thereof, design applications, applications in foreign countries, international applications, national phase applications, and the like). I hereby authorize and request the Commissioner of Patents and Trademarks to issue to Assignee all patents, for the Invention, or patents resulting therefrom.

I also hereby assign and transfer, and hereby agree to assign and transfer, to Assignee all right, title and interest to any and all applications for patent for the Invention throughout the world, including all rights of priority resulting from the filing of the Application or any related application, including but not limited to: the right to file applications and obtain patents, utility models, industrial models and designs for the Invention throughout the world, the right to claim priority from the Application or any related application when filing applications and obtaining patents, utility models, industrial models and designs for the Invention throughout the world.

I further agree to execute any and all powers of attorney, applications, assignments, declarations, affidavits, and any other papers in connection therewith, and generally do everything possible and necessary to perfect all rights, title and interest hereby assigned and transferred, and hereby agreed to be assigned and transferred, in and to Assignee.

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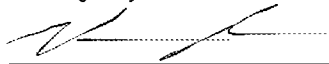
Tianhong Yan

January 29, 2021

Date

For and on behalf of Sunrise Memory Corporation

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Signature

January 28, 2021

Date

Name: Noam Kedem

Title: Operations

ATTORNEY DOCKET NO.
SRM-024 US X1

ASSIGNMENT


ASSIGNMENT

I, Scott Brad Herner, am an inventor of the subject matter described and claimed in a provisional patent application entitled "VERTICAL THIN-FILM TRANSISTOR AND APPLICATION AS BIT-LINE CONNECTOR FOR 3-DIMENSIONAL MEMORY ARRAYS," (hereinafter referred to as "Application") application serial number 17/161,504, filed January 28, 2021;

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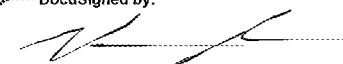
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Scott Brad Herner

January 29, 2021

Date

For and on behalf of Sunrise Memory Corporation

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Signature

January 28, 2021

Date

Name: Noam Kedem
Title: Operations

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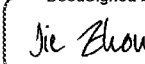
I, Jie Zhou, am an inventor of the subject matter described and claimed in a provisional patent application entitled "VERTICAL THIN-FILM TRANSISTOR AND APPLICATION AS BIT-LINE CONNECTOR FOR 3-DIMENSIONAL MEMORY ARRAYS," (hereinafter referred to as "Application") application serial number 17/161,504, filed January 28, 2021;

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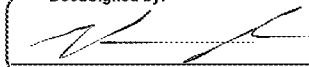

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Jie Zhou

January 29, 2021

Date

For and on behalf of Sunrise Memory Corporation

DocuSigned by:


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Signature

January 28, 2021

Date

Name: Noam Kedem
Title: Operations

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I, Wu-Yi Henry Chien, am an inventor of the subject matter described and claimed in a provisional patent application entitled "VERTICAL THIN-FILM TRANSISTOR AND APPLICATION AS BIT-LINE CONNECTOR FOR 3-DIMENSIONAL MEMORY ARRAYS," (hereinafter referred to as "Application") application serial number 17/161,504, filed January 28, 2021;

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Wu-Yi Henry Chien

January 28, 2021

Date

For and on behalf of Sunrise Memory Corporation

DocuSigned by:



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Signature

January 28, 2021

Date

Name: Noam Kedem

Title: Operations

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SRM-024 US X1

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I, Eli Harari, am an inventor of the subject matter described and claimed in a provisional patent application entitled "VERTICAL THIN-FILM TRANSISTOR AND APPLICATION AS BIT-LINE CONNECTOR FOR 3-DIMENSIONAL MEMORY ARRAYS," (hereinafter referred to as "Application") application serial number 17/161,504, filed January 28, 2021;

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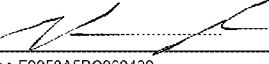
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Eli Harari

January 29, 2021

Date

For and on behalf of Sunrise Memory Corporation

DocuSigned by:

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Signature

January 28, 2021

Date

Name: Noam Kedem
Title: Operations

PATENT

RECORDED: 02/02/2021

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