

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT6519959

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	
CONVEYING PARTY DATA		
	Name	Execution Date
	CONEXANT SYSTEMS, LLC	09/01/2017
RECEIVING PARTY DATA		
Name:	SYNAPTICS INCORPORATED	
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State/Country:	CALIFORNIA	
Postal Code:	95131	
PROPERTY NUMBERS Total: 1		
	Property Type	Number
	Application Number:	15853645
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ATTORNEY DOCKET NUMBER:	70548.633US01	
NAME OF SUBMITTER:	SIDNEY BUCHANAN	
SIGNATURE:	/SIDNEY BUCHANAN/	
DATE SIGNED:	01/27/2021	
Total Attachments: 15		
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INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT

THIS INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT ("Agreement"), dated September 1, 2017, is entered by and between Conexant Systems, LLC, a Delaware limited liability company with an office at 1901 Main Street, Suite 300, Irvine, CA 92614 ("Assignor") and Synaptics Incorporated, a Delaware corporation with an office at 1251 McKay Drive, San Jose, CA 95131 ("Assignee" and collectively with Assignee, the "Parties").

WHEREAS Assignor desires to transfer, assign, and deliver to Assignee, and Assignee desires to receive, all of Assignor's right, title and interest in or to any and all Intellectual Property.

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, and intending to be legally bound, the Parties agree as follows:

1. Definition of Intellectual Property. As used herein, "Intellectual Property" means all worldwide intellectual property rights, including all rights in: (a) patents, utility models, industrial design registrations, applications for any of the foregoing and statutory invention registrations, including all provisionals, divisionals, continuations, continuations-in-part, reissues, reexaminations, renewals and extensions, all inventions disclosed in any of the foregoing and improvements thereto and all rights to claim priority throughout the world (collectively, "Patents"), (b) trademarks, service marks, certification marks, trade dress and trade names, all applications and registrations for any of the foregoing, renewals and extensions thereof and all goodwill associated therewith (collectively, "Trademarks"), (c) copyrights, and mask work rights, including copyrights in Software and data collections, all applications and all registrations for any of the foregoing, renewals and extensions thereof and all moral rights associated therewith, (d) internet domain names, (e) trade secrets and proprietary information, including confidential and proprietary information and know-how, inventions (whether or not patentable), invention disclosures, algorithms, designs, drawings, prototypes, business methods, processes, discoveries, ideas, formulae, manufacturing techniques, specifications, and engineering data, (f) all moral and economic rights of authors or inventors, however denominated, and (g) all rights arising from the foregoing and any similar or equivalent rights to any of the foregoing.

2. Transfer of Intellectual Property. Assignor hereby irrevocably grants, conveys, assigns, and transfers to Assignee, its successor and assigns, Assignor's entire worldwide right, title and interest in and to (a) any and all Intellectual Property owned or controlled by Assignor as of the date of this Agreement, including, but not limited to, all Intellectual Property listed on the Intellectual Property Schedules attached hereto, any related or corresponding United States provisional or non-provisional application embodying the inventions described in the Patents or any other United States application claiming priority to a provisional application under 35 U.S.C. § 119(e) or converted therefrom, or any application claiming the benefit of a non-provisional application under 35 U.S.C. §120, and all continuations, continuation-in-parts, divisionals, provisionals, reissues and reexaminations thereof and the subject matter of any and all claims which may be obtained in every such patent, as well as all corresponding foreign patents and foreign patent applications in which the same exist along with the all rights of priority, the right to file applications for patents based thereon, to prosecute such applications and receive patents thereon in Assignee's own name, including any other counterparts thereto worldwide, patents of addition, utility models, inventors' certificates, industrial property protection, patent rights and right to claim priority thereto in any country, and all extensions and renewals thereof existing now or in the future, including, without limitation, all applications for patents, utility models and designs that may hereafter be filed for in any country or countries other than the United States,

together with the right to file such applications under the Patent Laws of the United States, the International Convention for the Protection of Industrial Property, or any other international agreement or the domestic laws of the country in which any such application is filed, as may be applicable, and all forms of industrial property protection, including without limitation, patents, utility models, inventors' certificates and designs, which may be granted in any country or countries foreign to the United States and all extensions, renewals and reissues therefor, the same to be held and enjoyed by Assignee, its successors and assigns; and any and all other rights and interests arising out of, in connection with or in relation to the Patents, and all rights to causes of action and remedies related thereto (including, without limitation, all rights to profits and damages by reason of past, present or future infringement or other violation by any party or parties, with the right to sue, prosecute and collect the same for its own use and benefit and for the use and benefit of its successors, assigns or other legal representatives); (b) any and all goodwill associated with the use of, and symbolized by, any and all Trademarks included in such Intellectual Property; and (c) any and all causes of action that may have accrued to Assignor in connection with such Intellectual Property. Assignor authorizes and requests the Commissioner of Patents and Trademarks of the United States, and the corresponding entities or agencies in any applicable foreign jurisdictions, whose duty is to issue patents or other evidence or forms of industrial property protection on applications as aforesaid, to issue the same to Assignee and to record Assignee as owner of the patents and trademarks, as assignee of the entire right, title and interest in, to and under the same, for the sole use and enjoyment of Assignee, its successors, assigns or other legal representatives.

3. Further Assurances. Each Party shall execute and deliver, or cause to be executed and delivered, such other documents and instruments, reasonably satisfactory to the other Party, as may be reasonably necessary or desirable to carry out or implement any provision of this Agreement and to take all reasonable actions designed to vest in Assignee the Intellectual Property, to perfect, record or enforce any interests or rights of Assignee relating to the Intellectual Property, and to provide to Assignee the full use, enjoyment and possession of the Intellectual Property. In addition, each Party shall provide, from time to time, such additional information and assistance as the other Party may reasonably require to carry out the terms of this Agreement. Assignor further hereby covenants and agrees to execute all applications and any additional assignment or other documents, sign all lawful papers, and make all rightful oaths, as reasonably necessary to aid the Assignee or its successors, assigns or other legal representatives to obtain and enforce proper protection for the Intellectual Property in all jurisdictions and to record Assignee as owner of the Intellectual Property, as assignee of the entire right, title and interest in, to and under the same, for the sole use and enjoyment of Assignee, its successors, assigns or other legal representatives.

4. Entire Agreement. This Agreement, together with the Intellectual Property Schedules attached hereto, constitutes the entire agreement and understanding of the Parties in respect of the subject matter hereof and supersedes all prior understandings, agreements or representations by or among the Parties, written or oral, to the extent they relate in any way to the subject matter hereof. No representations, warranties, covenants, understandings, agreements, oral or otherwise, relating to the transactions contemplated by this Agreement exist between the parties except as expressly set forth in this Agreement.

5. No Third-Party Beneficiaries. Nothing herein expressed or implied is intended or shall be construed to confer upon or give any Person, other than Assignee and Assignor and their respective successors and assigns, any rights, remedies, obligations, or liabilities under or by reason of this Agreement.

7. Binding Effect. All of the terms, agreements, covenants, representations, warranties and conditions of this Agreement are binding upon, inure to the benefit of and are enforceable by, the Parties and their respective successors and assigns.

8. Counterparts. This Agreement may be executed in one or more counterparts, each of which will be deemed to be an original but all of which together will constitute one and the same document. Any signature to this Agreement delivered via facsimile, PDF format or other electronic means shall be deemed an original for all purposes.

9. Governing Law. This Agreement will be governed by and construed in accordance with the laws of the State of Delaware, without giving effect to principles or rules of choice of laws or conflict of laws to the extent such principles or rules would require or permit the application of Laws of another jurisdiction.

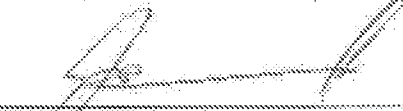
10. Severability. If any term, provision, or clause hereof, or of any other agreement or document which is required by this Agreement, is held to be invalid, such invalidity shall not affect or render invalid any other provision or clause hereof or thereof, all of which shall remain in full force and effect. If any provision of this Agreement is so broad as to be unenforceable, such provision shall be interpreted to be only as broad as is enforceable under applicable law. The parties further agree that if any provision contained herein is, to any extent, held invalid, or unenforceable in any respect under the Laws governing this Agreement, they shall take any actions necessary to render the remaining provisions of this Agreement valid and enforceable to the fullest extent permitted by Law and, to the extent necessary, shall negotiate in good faith to amend or otherwise modify this Agreement to replace any provision contained herein that is held invalid or unenforceable with a valid and enforceable provision giving effect to the original intent of the parties.

[Signature Pages Follow]

IN WITNESS WHEREOF, the Parties have caused this Intellectual Property Assignment Agreement to be executed as of the date stated in the introductory paragraph of this Agreement.

ASSIGNOR:

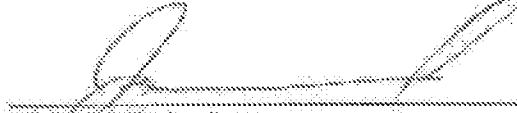
CONEXANT SYSTEMS, LLC



John McFarland
Secretary

ASSIGNEE:

SYNAPTICS INCORPORATED



John McFarland
SVP, General Counsel & Secretary

[Signature Page to Intellectual Property Assignment Agreement]

INTELLECTUAL PROPERTY SCHEDULES

IP Schedule A: United States Patents and Patent Applications

IP Schedule B: International Patents

IP Schedule C: Trademark Registrations and Applications

IP SCHEDULE A

United States Patents and Patent Applications

Application #	Filed Date	Issue Date	Patent #	Title
08/672530	06/11/1997	05/05/2001	6229812	Scheduling Techniques For Data Cells In A Data Switch
08/878577	06/11/1997	06/28/1999	5938510	Second Order Filter With Low Sensitivity To Component Values And A Method For Draining The Same
08/012547	08/18/1997	07/13/1999	5922053	Fast Ethernet Combination Chaining Of Auto-Negotiations For Multiple Physical Layer Capability
08/528148	09/05/1997	11/30/1999	5986104	System For Coding Signals
08/025552	09/08/1997	05/23/2000	6067378	Axis Dimensional Block Modulation Code For A PAM-S Constellation 4
08/948842	10/01/1997	03/07/2000	6024396	System And Method For Concatenating Reed-Solomon And Trellis Codes
08/242977	10/02/1997	05/09/2000	6061391	Error Indicator, And Data Modulator-Demodulator And Indicating Method Of Light Emitter Therein
08/956553	10/23/1997	08/28/2001	6282246	Frequency Modulation Method And Modem Unit Employing Such Method
08/550088	10/28/1997	05/30/2000	6089883	Asynchronous Transfer Mode Switching Architecture Having Connection Buffers
08/952548	10/31/1997	08/01/2000	5997783	MMSE Equalization For DMT Systems With Cross Talk
08/964178	11/06/1997	05/21/2001	6283124	CPE Alert Signal Tone Detector
08/942014	12/17/1997	08/07/2001	6272484	A Non-Content Frequency Shift Keying Detection Scheme
08/005845	01/17/1998	06/05/2001	6243425	Adaptive Precoding System And Method For Equalizing Communication Signal
08/005228	01/20/1998	02/06/2000	6025483	Method And Apparatus For Synchronizing A Data Communication System To A Periodic Digital Impairment
08/014155	01/27/1998	12/04/2001	6327866	System And Method For External Timing Using A Complex Rotator
08/089744	03/16/1998	01/09/2001	6169801	Digital Isolation Apparatus And Method
08/045275	03/20/1998	09/07/1999	5949218	Methods And Apparatus For Managing The Charging And Discharging Of A Lithium Battery
08/080228	04/13/1998	08/06/2001	6188723	Asynchronous Transfer Mode Traffic Shapers
08/082501	04/17/1998	02/19/2002	6349088	Method And Apparatus For Forming A Virtual Circuit
08/075451	05/08/1998	10/03/2000	6128373	Programmable Data Access Arrangement Having Combined Command Signals
08/075722	05/11/1998	08/08/2000	6101217	System And Method For Demodulating Digital Information From An Odd Constellation
08/079905	05/13/1998	08/08/2000	6100834	Recursive Multi-Bit ADC With Predictor
08/087216	05/29/1998	08/08/2000	6109337	Method And System For Combining Multiple Images Into A Single Higher-Quality Image
08/096883	06/11/1998	08/15/2000	6104793	System And Method For Performing Telephone Line-In/Out Detection, Extension Pick-Up Detection, Extension Pick-Up Detection, And Remotely Hang-Up Detection In A Modem
08/094715	06/15/1998	03/14/2000	6037749	Battery Charger And Power Source Employing An Environmental Energy Extractor And A Method Related Therein
08/121476	07/23/1998	03/20/2001	6214280	Multiplex Generation Of RF For Wireless Transmission Of Video
08/121475	07/23/1998	03/13/2001	6202165	Photonic Clock Distribution Method And Apparatus For Electronic Systems
08/143584	08/11/1998	11/26/2000	6188385	Methods And Apparatus For A High Efficiency Charge Pump
08/148057	08/02/1998	04/29/2000	6035024	Adaptive Method For Y-C Separation In Video Signals
08/152952	08/18/1998	08/15/2000	6104230	Electronic Inductor Circuit Using Cascode Transistor
08/161389	09/29/1998	05/14/2001	6349135	Method And Apparatus For Deriving Power From A Clock Signal Coupled Through Capacitors
08/162403	09/29/1998	07/02/2002	6418890	Timing Recovery For A High Speed Digital Data Communication System Based On Adaptive Equalizer Impulse Response Characteristics
08/164139	09/30/1998	04/30/2002	6381286	Method And Apparatus For Identifying The Encoding Type Of A Central Office COBEC
08/168256	09/30/1998	07/17/2001	6283129	Dynamic Microcode For Embedded Processors
08/168776	09/30/1998	07/16/2002	6424757	Method And Apparatus For Controlling The Programming And Erasing Of Flash Memory
08/168630	09/30/1998	08/13/2002	6434293	Method And Apparatus For Canceling Periodic Interference Signals In A Digital Data Communication System
08/164921	10/01/1998	02/20/2001	6182498	System And Method For Generating Error Checking Data In A Communications System
08/164329	10/01/1998	08/27/2002	6423278	System And Method For Data Alignment In A Communication System
08/164888	10/01/1998	02/18/2003	6535522	Multiple Protocol Interface And Method For Use In A Communications System
08/164860	10/01/1998	05/16/2004	6788589	Circuit And Method For Performing Partial Parallel Data Transfer In A Communications System
08/164909	10/01/1998	07/30/2002	6427178	System And Method For Protocol Conversion In A Communications System
08/155783	11/02/1998	12/19/2000	6163645	Apparatus And Method For Power Management Of Embedded Subsystems
08/167083	10/06/1998	05/26/2000	6064267	Current Mirror Utilizing Amplifier To Match Operating Voltages Of Input And Output Transconductance Devices
08/170749	10/13/1998	03/20/2001	6205110	System And Method For Bit Loading With Optimal Margin Assignment
08/170753	10/13/1998	11/26/2000	6184037	System And Method For Testing Distortion In Transformer

IP SCHEDULE A

United States Patents and Patent Applications

Application #	Filed Date	Issue Date	Patent #	Title
09/175886	10/20/1998	05/15/2001	6233594	Distortion Filter For Oversampling Analog-To-Digital Converter
09/178637	10/27/1998	02/20/2001	6192277	Method For Detecting Nonlinear Distortion Using Moment Invariants
09/192516	11/16/1998	09/10/2002	6449664	Two Dimensional Direct memory Access In Image Processing Systems
09/193323	11/16/1998	02/26/2002	6351530	Modem Having A Digital High Voltage Isolation Barrier
09/192517	11/16/1998	09/11/2001	6288794	Variable Sample Rate Converter Adaptable To Rational Ratios
09/192827	11/16/1998	11/06/2001	6814475	Method And Apparatus For Monitoring, Controlling And Configuring Local Communication Devices
09/193804	11/17/1998	12/11/2001	6830597	Method And Apparatus For Monitoring, Controlling And Configuring Remote Communication Devices
09/208029	11/01/1998	10/01/2002	6455678	System And Method For Providing Near Optimal Bit Loading In A Discrete Multi-Tone Modulation System
09/208235	12/10/1998	04/18/2000	6052037	Modulation Method And Apparatus
09/209801	12/11/1998	02/04/2003	6516804	HDLC Digital Data Transmission Protocol Controller
09/209897	12/11/1998	10/14/2000	6136225	Protocol And Bus Link System Between Components Of A Micro-Controller
09/213981	12/17/1998	09/07/1999	5849319	Method And Apparatus For Training Linear Equalizers In A PCM Modem
09/219914	12/25/1998	02/20/2001	6192109	Apparatus And Method For Improved DSL Communication
09/224752	01/04/1999	06/13/2000	6075845	System And Method For Performing Telephone Line-In-Use Detection, Extension Pick-Up Detection, And Remote Hang-Up Detection In A Modem
09/226406	01/06/1999	06/11/2001	6247092	Control Of Command Transmission And Reception In A Data Transmission Device And Data Transmission Device Therefor
09/227146	01/25/1999	05/14/2002	6280033	System And Method For Performing Signal Acceleration On An A-Link BUS
09/233024	02/03/1999	06/25/2002	6412627	Direct Memory Access Controller Having On-Board Arbitration Circuitry
09/244612	03/04/1999	01/16/2001	6175369	Comb Filtered Signal Separation
09/247266	02/10/1999	07/02/2002	6455338	System For Writing A Data Value At A Starting Address To A Number Of Consecutive Locations Equal To A Segment Length Modifier
09/247400	02/10/1999	08/17/2003	6433365	Direct Memory Access Controller Having Decode Circuit For Compact Instruction Format
09/250458	02/16/1999	02/26/2002	6351845	Information Service For Thermal And Passive Mode Systems
09/251096	02/17/1999	10/01/2002	6455430	System And Method For Implementing Multi-Level Resolution Conversion Using Modified Linear Interpolation
09/261981	03/04/1999	01/08/2001	6172556	Feedback Controlled Low Voltage Current Sink/Source
09/265480	03/09/1999	10/09/2001	6300824	Analog Offset Cancellation Method And Circuit And Amplifier Using Such Circuit
09/274395	03/23/1999	05/07/2002	6355756	Selection Of Functions Within An Integrated Circuit
09/281826	04/01/1999	10/21/2003	6636228	Flexible Array Image Data Processing For Ink Jet Nozzle Data Control In Digital Image Printing Systems
09/282949	04/01/1999	09/10/2002	6449957	Image Data Addressing For Bit Rotation Block Within Digital Image Processor
09/282956	04/01/1999	03/24/2002	6455380	Integrated Bit Rotation And Row Alignment Within Digital Image Printing Systems
09/290884	04/13/1999	03/04/2007	7265873	Image Processor That Performs Edge Enhancement Using A Dynamic Threshold
09/294131	04/15/1999	04/09/2001	6211715	Power Control Circuit For A Driver
09/295962	04/21/1999	07/23/2002	6424541	Electronic Device Attachment Methods And Apparatus For Forming An Assembly
09/296009	05/10/1999	02/27/2001	6185706	System And Method For Providing Soft Audio And Soft Modem Copy Protection For Hardware Interfaces And Software Code
09/312218	05/14/1999	11/11/2003	6647114	Telephone Line Interface Circuit With Virtual Impedance
09/312412	05/14/1999	05/04/2004	6731749	Telephone Line Interface Circuit With Intelligent Line Current And Voltage Control
09/317277	05/24/1999	04/10/2001	6215353	Stable Voltage Reference Circuit
09/335975	06/18/1999	04/09/2002	6370671	Configurable Decoder And Method For Decoding A Reed-Solomon Codeword
09/336626	06/18/1999	06/25/2002	6462980	Galois Field Computation System And Method
09/336630	06/18/1999	07/07/2002	6415413	Configurable Reed-Solomon Controller And Method
09/339968	06/23/1999	04/29/2003	6556822	Seamless Transition Between Communication Characteristics Of Communication Devices
09/343873	06/30/1999	09/03/2002	6445734	System And Method Of Validating Equalizer Training
09/340235	06/30/1999	12/26/2000	6157257	Low Power Folding Amplifier
09/349674	07/01/1999	04/09/2002	6370259	Standalone Telephone Answering Machine With Store And Forward Facsimile
09/350252	07/23/1999	06/05/2001	6263414	Method And Apparatus For Data Transmission
09/341801	07/27/1999	09/11/2000	6118769	Reference Voltage Stabilization System And Method For Fixing Reference Voltages Independent Of Sampling Rate
09/363713	07/27/1999	01/30/2001	6181122	System And Method For Starting Voltage And Current Controlled Elements
09/379833	08/08/1999	05/03/2004	6771393	Transmission Rate Control For Continuous Tone Facsimile
09/372546	08/11/1999	04/16/2002	6372118	High-Value Integrated Circuit Resistor

IP SCHEDULE A

United States Patents and Patent Applications

Application #	Filed Date	Issue Date	Patent #	Title
09/382298	06/26/1999	06/18/2002	6407631	System And Method For Providing Automatic Compensation Of IC Design Parameters That Vary As A Result Of Natural Process Variation
09/379998	08/24/1999	12/08/2003	6490687	System For Storing And Using A Unique Serial Number For A Device For Communication Within A Computer System
09/392238	09/09/1999	09/12/2000	6118266	Low Voltage References With Power Supply Rejection Ratio
08/723149	09/26/1999	02/08/1999	5870438	Fast Desynchronization System For High Speed Data Transmission
09/407465	09/26/1999	08/21/2003	6278387	Audio Encoder And Decoder Utilizing Time Scaling For Variable Playback
09/406864	08/26/1999	09/17/2002	6432424	Method And Apparatus For Multiple Channel Signal Processing
09/409820	09/30/1999	03/13/2003	6532511	Asynchronous Controlled Multi-Channel DMA Controller
09/409823	09/30/1999	07/08/2003	6593938	DCT Domain Conversion Of A Higher Definition Signal To Lower Definition Signal
09/409368	09/30/1999	03/19/2003	6359972	Line In Use Detection
09/409819	09/30/1999	03/16/2004	6707497	Microprocessor Extensions For Two-Dimensional Graphics Processing
09/408885	09/30/1999	05/14/2002	6308467	High Voltage Tolerant Output Driver For Sustained Tri-State Signal Lines
09/410862	10/12/1999	08/01/2004	6765371	Signaling Mechanism For Modern Connection Making And Reconnecting
09/432341	11/02/1999	08/13/2002	6434633	Method And Apparatus For Facilitating Ac-Link Communications Between A Controller And A Slow Peripheral Of A Codec
09/432834	11/02/1999	03/04/2003	6529975	Method And Apparatus For Addressing And Controlling Expansion Devices Through An Ac-Link And A Codec
09/439328	11/17/1999	06/16/2005	6896243	Apparatus And Methods For Down Conversion Video De-Interlacing
09/442983	11/28/1999	06/22/2001	6235273	High Efficiency Power Amplifier
09/467299	12/20/1999	10/17/2000	6134168	High Speed Sensing Of Dual Port Static Ram Cell
09/476849	01/03/2000	06/17/2004	6651164	System For Adjusting Clock Frequency Based Upon Amount Of Unread Data Stored In Sequential Memory When Reading A New Line Of Data Within A Field Of Data
09/476856	01/03/2000	08/20/2002	6438158	Method And Apparatus For Implementing Enhanced Multiple Modulus Conversion Techniques In A Signal Point Mapping Context
09/480215	01/10/2000	02/17/2004	6693901	System And Method Of Video Frame Memory Reduction Of Video Decoders
09/504992	02/15/2000	01/27/2004	6694236	System Of And Method For Efficiently Performing Computations Through Extended Booth Encoding Of The Operands Thereof
09/507306	02/18/2000	08/24/2004	6782155	Apparatus And Methods For Adaptive Digital Video Quantization
09/516873	03/01/2000	09/30/2008	6624788	Single Clock Cycle CRC Engine
09/517979	03/03/2000	07/31/2003	6268831	High Performance Switched Capacitor Filter For Oversampling Sigma-Delta Signal To Analog Converters
09/523637	03/13/2000	03/23/2004	6711207	System And Method For Reduced Power Consumption In Digital Subscriber Line Modems
09/534873	03/24/2000	02/11/2003	6512339	Method Of Regulating Power Transfer Across An Isolation Barrier
09/543201	04/05/2000	10/23/2001	6207408	Method And Apparatus For Powering Down A Line Driver
09/551253	04/14/2000	03/09/2004	6704999	Quick Connect Parameter Exchange
09/573040	05/17/2000	10/09/2001	6301341	System And Method For Performing Telephone Line-In-Use Detection, Extension Pick-Up Detection, And Remote Hang-Up Detection In A Modem
09/575486	05/19/2000	07/13/2004	6783470	System And Method For Dynamically Amplifying A Delayed Analog Signal Based On Amplitude Information Obtained From Its Digital Representation
09/575579	05/25/2000	06/27/2006	7086178	System And Method To Interfaze A Local Area Network With A Wide Area Network
09/578880	05/26/2000	10/02/2001	6297759	Digital To Analog Converter With High-Speed Output
09/588830	06/07/2000	03/20/2005	6874041	Automatic Configuration Of Communication Device Input Or Output Terminal
09/588992	06/08/2000	11/22/2006	6968020	System And Method Of The Frequency Synthesis To Avoid Gaps And VCO Pulling In Direct Broadcast Satellite Systems
09/593707	06/13/2000	05/04/2004	6731726	Communication On Hold
09/611254	07/07/2000	11/04/2003	6643859	Calibrating For Errors In A Plurality Of Signal Sources Such As Current Or Voltage Sources
09/616087	07/14/2000	07/30/2002	6477011	Modem Data Access Arrangement Without Diode Bridge
09/618330	07/18/2000	03/30/2004	6714643	Telephone Line Interface Circuit Without Hookswitch Relay
09/620871	07/21/2000	11/11/2003	6617109	Network Telephony
09/626887	07/26/2000	11/11/2003	6647100	Universal Serial Bus Data Pump Command Interpreter
09/626435	07/27/2000	12/13/2005	6975525	Slotted Synchronous Frequency Division Multiplexing For Multi-Drop Networks
09/632029	08/02/2000	04/25/2006	7035323	System And Method For Combining Multiple Digital Subscriber Line Transceivers
09/630845	08/02/2000	09/21/2004	6795547	Full Duplex Speaker-Phone Providing Increased Loop Stability
09/641406	08/17/2000	04/27/2004	6728401	Red Eye Removal Using Color Image Processing
09/643567	08/22/2000	02/14/2004	6687973	High Speed Mr. Address Search Engine
09/663280	09/15/2000	03/05/2002	6353347	Power Control Circuit For A Line Driver

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09/067954	09/21/2000	09/11/2003	6518604	Diode With Variable Width Metal Stripes For Improved Protection Against Electrostatic Discharge (ESD) Current Failure
09/072166	09/27/2000	11/04/2003	6643109	Fully Synthesizable And Highly Area Efficient Very Large Scale Integration (VLSI) Electrostatic Discharge (ESD) Protection Circuit
09/078989	10/04/2000	05/26/2004	6741363	Power Line Networking Apparatus And Method
09/083434	10/05/2000	04/16/2002	6523377	Power Supply With Digital Data Coupling For Power-Line Networking
09/083627	12/05/2000	02/25/2003	6520309	Method And Apparatus For Hybrid Smart Center Loop For Clock Data Recovery
09/0732458	12/06/2000	11/22/2005	6967996	System And Method For Optimizing Digital Subscriber Line Performance Between Transceivers From Multiple Manufacturers
09/0734848	12/11/2000	10/29/2002	6673870	System And Method For Providing Soft Audio And Soft Modem Copy Protection For Hardware Interfaces And Software Code
09/0734523	12/11/2000	07/30/2003	6472319	Apparatus And Method For Power Management Of Embedded Subsystems
09/0747470	12/20/2000	11/05/2002	6476675	Double-Cascode Two-Stage Operational Amplifier
09/0746755	12/20/2000	03/05/2002	6533381	Fully Differential Two-Stage Operational Amplifier With Gain Boosting
09/0748665	12/21/2000	02/05/2002	6548851	K-Replacement For Color Digital Image Printing
00/250353	12/28/2000	03/22/2011	7912155	Demodulator Circuit
09/075797	01/09/2001	02/21/2008	7002916	Asynchronous Transfer Mode Traffic Shapers
09/075870	01/09/2001	05/15/2003	6880295	Control Channel Implementation In A Packet Switched Communications Network
09/0758130	01/10/2001	06/04/2002	6406782	Method Of Frequency Domain Filtering Employing A Fast To Analytic Transform
09/076741	01/19/2001	06/29/2006	6757396	System And Method For Sealing A Sound System
09/077692	02/05/2001	04/05/2004	6718195	Systems And Methods For A Partial Sum Digital To Filter
09/0775970	02/05/2001	12/18/2002	6665021	System And Process For Filtering Single Tone Signals
09/080054	02/08/2001	02/11/2006	7008878	Scheduling Techniques For Data Cells In A Data Switch
09/085690	02/16/2001	08/20/2002	6437720	Clock Independent Charge Transfer Scheme For Switched-Capacitor Digital-To-Analog Converter
09/092152	02/22/2001	01/24/2006	6990526	Unified Algorithms For Frame Scheduling And Buffer Management In Differentiated Services Networks
09/094124	02/26/2001	10/11/2005	6951424	Credit Based Pacing Scheme For Heterogeneous Speed Frame Forwarding
09/088682	03/15/2001	11/28/2006	7142514	Bandwidth Sharing Using Emulated Weighted Fair Queuing
09/0916621	03/23/2001	12/16/2003	6664841	Method And Apparatus For Conditioning An Analog Signal
09/0824951	04/03/2001	10/10/2006	7120583	Single Switch Image For A Stack Of Switches
09/088800	05/04/2001	07/08/2002	6638678	Method And Apparatus For Synchronizing A Data Communications System To A Periodic Digital Impairment
09/085184	05/21/2001	04/12/2005	6879625	System And Method For Providing Cancellation Of Interference In A Repeater Configuration With Remote Loop Powering
09/083689	05/21/2001	05/21/2006	7016400	Circuit Board Capacitor Structure For Forming A High Voltage Isolation Barrier
09/086259	05/23/2001	01/16/2013	8354334	Data Network Node Having Enhanced Security Features
09/079141	06/13/2001	04/27/2004	6728982	Method And System For Predictive Model Layout Generation With Reduced Design Cycle
09/089040	07/03/2001	09/28/2004	6748270	Non Real-Time Facsimile System For A Computing Device
09/092880	07/29/2001	06/13/2006	7051970	Multiple Symbol Rate Burst Equalizer Training
09/092363	07/31/2001	09/21/2006	7016371	Multi-Mode Adaptive Filter
09/085933	08/23/2001	02/14/2006	6925416	Buffer Management For Support Of Quality-Of-Service Guarantees And Data Flow Control In Data Switching
09/041883	08/28/2001	07/01/2003	6528902	Method And System For Predictive Layout Generation For Inductors With Reduced Design Cycle
09/093829	09/24/2001	09/27/2008	6959461	Modems Utilizing Low Density Parity Check Codes
09/088821	09/26/2001	07/15/2006	7082138	Internal Communication Protocol For Data Switching Equipment
09/089323	10/02/2001	10/04/2005	6852458	Demapping System And Method
09/089702	10/03/2001	08/14/2007	7257134	Method Of Pacing The Synchronization Of Routing Information In A Data Switching Environment
09/071400	10/05/2001	07/01/2003	6586992	System And Method For Increasing Power Range In A Power Amplifier Using A Single Voltage Supply
10/024976	10/24/2001	01/04/2005	6828857	Method And System For Predictive Multi-Component Circuit Layout Generation With Reduced Design Cycle
10/016184	11/03/2001	02/28/2005	7058623	Circuit For Reducing Voltage Peak In Interfacing With A Telephone Line
10/085364	11/08/2001	03/18/2004	6634854	Pin Grid Array Package With Controlled Impedance Pins
09/097882	11/20/2001	08/17/2006	7068592	System And Method For Increasing Payload Capacity By Clustering Unloaded Bins In A Data Transmission System
10/034082	12/29/2001	02/17/2004	6602894	Error Correction And Compression Parameter Exchange During Modem Training Phase

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09/683702	02/11/2002	11/01/2006	6961779	System And Method For Robust Parsing Of Multiple-Frames Protocol Messages
09/683783	02/12/2002	10/04/2006	6953430	System And Method For Interlacing A Data Link Protocol Engine And A Physical Layer
10/096889	03/04/2002	05/09/2006	7042875	System And Method For High-Speed Switching
10/089324	04/09/2002	04/17/2007	7206123	Interfacing 622.08 MHz Line Interface To A 77.76 MHz Sonet Framer
10/064981	05/11/2002	03/13/2006	7342927	Systems And Methods For Transferring Various Data Types Across An ATM Network
10/053085	03/11/2002	08/28/2007	7263109	Clock Skew Compensation For A Jitter Buffer
10/063036	03/13/2002	12/23/2009	6657501	Integrated Circuit Capable Of Operating In Multiple Orientations
10/099582	03/15/2002	05/23/2004	6711245	Method And Apparatus For Processing Call Progress Of A Serial Communication Device
10/137601	04/29/2002	02/12/2008	7330544	Digital Isolation Barrier As Interface Bus For Modems
10/064340	07/02/2002	09/05/2006	7105008	Communications System Using Rings Architecture
10/066449	07/15/2002	08/25/2006	7418516	System And Method For Improved Synchronous Data Access
10/198848	07/18/2002	04/25/2006	6636512	Double-Cascade Two-Stage Operational Amplifier
10/131124	08/12/2002	12/06/2006	6602246	Two-Dimensional Memory Access In Image Processing Systems
10/230354	08/28/2002	08/06/2010	6604470	Key Interface For Secure Object Manipulation
10/284856	10/31/2002	11/16/2010	7335205	High Availability Ethernet Backplane Architecture
10/284634	10/31/2002	10/12/2006	6802840	Solid State Inducting Device
10/308317	11/21/2002	07/17/2007	7246366	Method And Apparatus For Firmware Restoration In Modems
10/219304	12/13/2002	07/09/2006	7097869	Scheduling Methods For Combined Unicast And Multicast Queuing
10/321500	12/19/2002	04/18/2006	7031414	Combined Feedforward Filter For A Decision Feedback Equalizer
10/250552	12/20/2002	08/21/2007	7260060	Apparatus For Link Failure Detection On High Availability Ethernet Backplane
10/228618	12/20/2002	08/02/2006	6925827	Method And Apparatus For Power Routing In An Integrated Circuit
10/349051	01/13/2003	09/27/2011	8027478	System And Method For Improved Data Protection In Books
10/352754	01/28/2003	01/10/2006	6985506	Point Of Sale modem For High-Speed Communications
10/418748	04/17/2003	08/14/2007	7257185	Method And Apparatus For Preamble Detection And Time Synchronization Estimation In OFDM Communication Systems
10/422076	04/21/2003	05/25/2010	7724637	Method And Apparatus For Controlled Spectrum Multi-Carrier Modulation
10/438875	05/07/2003	04/23/2009	7522638	Method And System For Preventing Data Loss In A Real-Time Computer System
10/680163	06/19/2003	09/18/2007	7257162	Hypothetical Reference Decoder For Compressed Image And Video
10/607344	06/25/2003	01/11/2005	6842509	Signaling Mechanism For Modem Connection Holding And Reconnecting
10/611402	06/30/2003	04/13/2011	7929013	System For Data Encryption And Decryption Of Digital data Entering And Leaving Memory
10/618309	07/08/2003	06/09/2010	7753875	System And Method For Packet Transmission From Fragmented Buffer
10/515613	07/08/2003	11/20/2007	7295750	Method And System For Optimizing Utopia Cnv Pulling Activation
10/680771	07/25/2003	05/13/2008	7372809	Reduced Symbol Rate Handshake Signaling In ADSL Systems
10/637174	08/08/2003	08/09/2006	6927488	Ballasting Mosfets Using Staggered And Segmented Diffusion Regions
10/640102	08/12/2003	07/26/2006	6824667	Quick Connect Parameter Exchange
10/642300	08/18/2003	09/27/2011	8028296	Systems And Methods For Managing Shared Resources In A Computer System
10/655668	09/05/2003	05/12/2009	7537670	Hypothetical Reference Decoder With Low Start-Up Delays For Compressed Image And Video
10/676093	09/30/2003	01/01/2006	7315519	TCP Acceleration System
10/697819	10/31/2003	03/01/2011	7881463	Method And Apparatus For Conveying Bidirectional Data, Power And Timing Signals Using A Single Transformer
10/714597	11/18/2003	09/18/2007	7272372	System And Method For Selectable Mask For DRSL
10/735678	12/10/2003	05/11/2010	7718686	System And Method For Scheduling Thread Execution
10/798254	12/17/2003	13/14/2006	7135366	Method For Fabricating A Lateral Metal-Insulator-Metal Capacitor
10/746821	12/23/2003	08/29/2006	7098961	Horizontal SYNC Pulse Detection For Video Signal Encoding
10/746235	12/29/2003	01/05/2010	7648413	System And Method For Providing Quality Of Service In Asynchronous Transfer Mode-Cell Transmission
10/749570	12/31/2003	08/08/2006	7088275	Variable Clock Rate Analog-To-Digital Converter
10/748571	12/31/2003	06/27/2006	7088202	Architecture For An Algorithmic Analog-To-Digital Converter
10/753613	12/31/2003	07/22/2006	7403790	DC Offset Correction For Direct-Conversion Receiver
10/776123	02/11/2004	01/23/2007	7107232	Comb Filter Design For Separating Luminance And Chrominance In Video Signals
10/775075	02/11/2004	06/05/2007	7227585	Luminance And Chrominance Separation System
10/881226	06/30/2004	07/26/2010	7780719	Combined Pipelined Classification And Address Search Method And Apparatus For Switching Environments
10/801873	07/29/2004	07/05/2011	7974272	Remote Control Of A Switching Node In A Stack Of Switching Nodes

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10/949861	09/24/2004	05/20/2008	7376075	Circular Constellations With Coherent Gain/Differential Phase And Pilots
10/990841	11/18/2004	08/29/2010	7746506	ESD Protection For Integrated Circuits Having Ultra Thin Gate Oxides
11/031891	01/07/2005	07/20/2010	7759067	General Purpose Pin Mapping For A General Purpose Application Specific Integrated Circuit (ASIC)
11/032458	01/10/2005	11/04/2008	7447373	Compensation For Exposure Time Variation When Scanning With CCD Sensors
11/032478	01/10/2005	05/19/2009	7536605	CIS (Contact Image Sensor) RYB (Red/Luminance/Blue) Sampling
11/032616	01/10/2005	12/18/2007	7309117	Multi-valued Raster Elements As A Technique To Reduce The Memory Requirements And Computation Complexity Of Advanced Ink Jet Systems
11/032615	01/10/2005	11/14/2008	7453600	Text Region Identification And Enhancement During Color Copy
11/032578	01/10/2005	05/11/2010	7716512	Real Time Clock Content Validation
11/032683	01/10/2005	10/18/2008	7441850	Generic Ink Jet Head Fire Logic
11/032609	02/10/2005	07/22/2008	7401880	Combination Of A/D Inputs With Quadrature Detection To Give Higher Resolution Positioning
11/032604	01/10/2005	07/20/2007	7180823	Flexible SRAM Clocking (AS-DLL)
11/032456	01/10/2005	01/27/2009	7464070	Selective Memory Block Remapping
11/032635	01/10/2005	02/23/2010	7667870	White Region Identification To Accelerate Image Processing For Copies
11/107308	04/15/2005	09/09/2010	7875127	Mosfet Having Increased Sheet-Bulk Conduction Uniformity
11/117262	04/28/2005	06/10/2006	7386040	System And Method For Optimizing Digital Subscriber Line Performance Between Transceivers From Multiple Manufacturers
11/174869	06/30/2005	07/21/2009	7564928	System And Method Of The Frequency Synthesis To Avoid Gaps And VCO Pulling In Direct Broadcast Satellites Systems
11/173129	06/30/2005	08/18/2008	7590674	Method And Apparatus For Generating A Random Bit Stream
11/224328	09/13/2005	05/08/2007	7216267	Systems And Methods For Multistage Signal Detection In MIMO Transmissions And Iterative Detection Of Pseudos OFDM
11/205406	12/19/2005	09/18/2012	8270002	Printing Digital Images With Rotation
11/223357	12/30/2005	07/06/2010	7750976	Automatic Video Detector
11/496023	05/01/2006	12/06/2011	8072547	Comb Filter That Utilizes Host Memory
11/496009	05/19/2006	01/26/2016	9287068	Modern Call Forwarding And Three-Way Calling Features For VoIP Applications
11/437311	05/12/2006	09/15/2009	7589268	Spread Spectrum Clock Generation
11/512806	06/30/2006	09/08/2009	7588644	Color Conversion System And Method
11/514628	08/30/2006	08/15/2009	7590248	Head Related Transfer Function Filter Generation
11/524790	09/25/2006	08/07/2007	7254134	Systems For Transferring Various Data Types Across An ATM Network
11/817054	12/26/2006	08/17/2010	7778618	Systems And Methods For Reducing Broadband Image Noise Through A Programmable Bandwidth RF Variable Gain Attenuator
11/847784	12/29/2006	06/01/2010	7730360	CDC-Compliant Embedded USB Controller Communication Device And System With Custom Features Support
11/709068	03/06/2007	01/25/2011	7870977	Image Processor And Method Of Image Rotation
11/731563	03/26/2007	01/06/2009	7474057	Decoding Video Signals To Separate Luma And Chroma Components
11/848618	08/06/2007	02/24/2009	7496188	System And Method For Selectable Mask For LBSL
11/853354	09/11/2007	04/20/2010	7701301	Systems For Implementing A Temperature And Process Compensated Two Stage Ring Oscillator
11/871915	10/12/2007	06/02/2011	7990902	Facilitating Transmissions In A Plurality Of Protocols
11/930721	10/31/2007	12/27/2011	8085698	Flagger Acknowledgement
11/931172	10/31/2007	04/10/2013	8358139	Systems And Methods For Management Of Wireless Clients
11/947946	11/30/2007	02/12/2014	8654773	Systems And Methods For Coexistence Of WLAN And Bluetooth Networks
11/954879	12/12/2007	09/30/2014	8848815	Systems And Methods For Retrieving Buffered Data From An Access Point
12/017172	02/26/2008	04/20/2010	7701374	Method And Apparatus For Automatic Optimal Sampling Phase Detection
12/086391	08/11/2008	04/24/2012	8165154	Systems And Methods For Reliable Broadcast And Multicast Transmission Over Wireless Local Area Network
12/047210	05/12/2008	07/31/2012	8233414	Systems And Methods For Indicating Buffered Data At An Access Point Using An Embedded Traffic Indication Map
12/047921	05/14/2008	06/01/2012	8170002	Systems And Methods For Indicating Buffered Data At An Access Point With Efficient Beacon Handling
12/046946	05/12/2008	01/03/2012	8086908	Systems And Methods For Indicating Buffered Data At An Access Point Using A Traffic Indication Map Broadcast
12/051875	03/19/2008	03/24/2013	8985135	Flexible Audience Follower For Film In Video
12/106253	04/18/2008	12/06/2011	8072593	Systems And Methods For Communicating To A Disassociated Station In A Protected Network

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12/108759	04/24/2008	02/07/2012	8111677	Systems And Methods Of Combined Bluetooth And WLAN Signaling
12/110888	04/28/2008	12/18/2011	8077047	Systems And Methods For Utilizing A Scanning Threshold To Reduce Power Consumption
12/128345	05/23/2008	12/18/2012	8395663	Signal Acquisition Systems And Method
12/157436	06/08/2008	05/04/2013	7710299	System And Method Providing Channel Multiplexing For Analog-To-Digital Conversion
12/186450	06/10/2008	04/02/2013	8411847	Acoustic Echo Canceller
12/138923	06/13/2008	03/15/2011	7907887	Low Power Receiving
12/143540	06/30/2008	08/29/2011	7915950	Method And Algorithm Of High Precision On-Chip Global Biasing Using Integrated Resistor Calibration Circuits
12/149133	06/28/2008	08/09/2010	7783252	Method And Apparatus For Delay And Combining Circuitry
12/165590	06/30/2008	03/18/2014	8656415	Method And System For Removal Of Clicks And Noise In A Redirected Audio Stream
12/178179	07/23/2008	07/29/2011	7983306	Method And Apparatus For Channelizing Sampled Broadband Signal
12/286491	11/06/2008	12/14/2010	7852868	Method And Apparatus For Establishing Circuit Connections Over Local Area Networks With Frequency Selective Impairments
12/388685	12/18/2008	12/04/2012	8325540	Power Management Controller For Drivers
12/346793	12/22/2008	09/25/2012	8274772	Current Detection And Limiting Method And Apparatus
12/346777	12/22/2008	08/14/2012	8283952	Microphone Array Calibration Method And Apparatus
12/358971	01/23/2009	07/03/2012	8215809	Universal Systems And Methods For Determining An Incoming Carrier Frequency And Decoding An Incoming Signal
12/385401	02/03/2009	06/06/2012	8019440	Enhanced Thermal Dissipation Ball Grid Array Package
12/373890	02/13/2009	05/13/2014	8724790	Systems And Methods For Network Facsimile Transmissions
12/390333	02/20/2009	07/15/2011	7982525	Systems And Methods For Driving High Power Stages Using Lower Voltage Processes
12/384180	03/01/2009	04/17/2012	8180153	Hypothetical Reference Decoder With Low Start-Up Delays For Compressed Image And Video
12/469647	05/20/2009	08/21/2012	8500421	Systems And Methods For Low-Latency Encrypted Storage
12/487528	06/18/2009	08/20/2013	8514532	Electrostatic Discharge Protection Circuit
12/488422	06/19/2009	04/05/2011	7920078	Systems And Methods For Variable Rate Conversion
12/488498	06/19/2009	11/22/2012	8061699	Anti-Pop Circuit
12/539836	08/11/2009	01/29/2013	8269082	Systems And Methods For Alignment Of Laser Printers
12/540330	08/12/2009	01/15/2013	8355270	Systems And Methods For Raster-To-Block Converter
12/553004	09/07/2009	05/31/2011	7952508	Noise-Shaped Scrambler For Reduced Out-Of-Band Common-Mode Interference
12/553749	09/08/2009	08/26/2011	7933045	Color Conversion System And Method
12/559054	09/14/2009	03/19/2013	8403073	Method And Apparatus For Generating A Random Bit Stream
12/574663	10/06/2009	10/25/2011	8044090	System And Method For Clock Synchronized Triangular Waveform Generation
12/578477	10/15/2009	04/02/2013	8411877	Tuning And Bias Selection Of High-Pass Filters For Audio Coders
12/579200	10/16/2009	07/02/2013	8677949	2:1 Crossover Equalization In PC Audio Applications
12/623202	11/20/2009	01/01/2013	8344787	Systems And Methods For Offset Cancellation Method For DC-Coupled Audio Drivers
12/684029	01/08/2010	01/21/2014	8634569	Systems And Methods For Echo Cancellation And Echo Suppression
12/696901	01/29/2010	11/22/2013	8664103	White Region Identification To Accelerate Image Processing For Copies
12/713141	02/05/2010	06/04/2013	8485390	Systems And Methods Of Tamper Proof Packaging Of A Semiconductor Device
09/817827	03/25/2010	09/15/2009	0858607	System And Method For Detecting Valid Indicator Signals
12/759325	04/19/2010	08/06/2013	8505012	System And Method For Scheduling Threads Requesting Immediate Cpu Resource In The Indexed Time Slot
12/792892	06/02/2010	10/30/2012	8301828	Systems And Methods For Reliable Multi-Level Cell Flash Storage
12/814425	06/11/2010	11/05/2013	8577068	Systems And Methods For Creating Immersion Surround Sound And Virtual Speakers Effects
12/830882	06/22/2010	08/27/2013	8517487	Systems And Methods For Dielectric Heating Of Ink In Inkjet Printers
12/828718	07/01/2010	09/09/2013	8528635	Grounding Switch Method And Apparatus
12/828640	07/01/2010	11/19/2013	8587287	High-Bandwidth Linear Current Mirror
12/836865	07/05/2010	03/29/2014	8682002	Systems And Methods For Transducer Calibration And Tuning
12/833806	07/09/2010	01/28/2014	8688053	Systems And Methods For Generating Phantom Steps
12/838771	07/20/2010	05/04/2014	8605498	Method And System For Automatically Detecting And Processing Halftone Regions In Scanned Documents
12/851383	08/05/2010	07/02/2013	8477354	Systems And Methods For Color Dithering
12/860791	08/20/2010	07/03/2012	8212608	Apparatus And Method For A Smooth Powerup Of A Reference Signal
12/872758	08/31/2010	12/31/2013	8621194	Processor Non-Flash Boot System And Method
12/878796	09/09/2010	09/30/2013	8847590	Adaptive High Dynamic Range Surface Capacitive Touchscreen Controller

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12/063443	12/08/2010	11/18/2016	9457540	System And Method For Reducing Ribs And Buzz Distortion
13/011569	01/21/2011	08/09/2011	7995867	Image Processor And Method Of Image Rotation
12/051062	02/11/2011	09/24/2013	8542721	System And Method For Television Signal Demodulation And Decoding
12/052170	03/17/2011	08/20/2012	8253610	Systems And Methods For Variable Rate Conversion
13/035780	04/26/2011	10/16/2012	8289358	Systems And Methods For Alignment Of Laser Printers
13/037091	02/28/2011	05/09/2014	8742901	Multi-Sample Reading In Sleep Mode For Passive Infrared Detectors And Other Analog Inputs
13/040121	03/03/2011	08/12/2014	8804865	Systems And Methods For High Pass Filtering With Smart Saturation
13/041089	03/04/2011	04/02/2013	8410828	Systems And Methods For Driving High Power Stages Using Lower Voltage Processes
13/044578	03/09/2011	04/16/2013	8421516	High Speed Level Shifters And Method Of Operation
09/512846	03/21/2011	12/09/2009	6867321	Modems Using Phase Coherence To Adapt For Missing Transmission Samples
13/082300	04/21/2011	03/08/2018	8971542	Systems And Methods For Speaker Bar Sound Enhancement
13/088731	04/25/2011	11/05/2013	8575979	Full Differential Adaptive Bandwidth PLL With Differential Supply Regulation
13/092743	04/27/2011	12/10/2013	8695912	Biphase Mark Code Decoder And Method Of Operation
13/104520	05/10/2011	10/08/2013	8952540	Wafer Level Package With Thermal Pad For Higher Braker Dissipation
13/149725	05/21/2011	05/06/2014	8719741	Asynchronous Process Notification
13/149725	05/21/2011	12/21/2013	8620868	DataSet Hierarchical Inheritance
13/149713	05/21/2011	11/05/2015	8978396	Regular Expression Selection For Configurable Application Data Steering
13/168800	06/21/2011	09/01/2015	9134219	Audio Driver System And Method
13/181370	07/12/2011	08/10/2013	8552145	Method And Apparatus For Channelizing Sampled Broadband Signal
13/183288	07/14/2011	05/27/2015	8983092	Waveform Shaping System To Prevent Electrical And Mechanical Saturation In Loud Speakers
13/184231	07/15/2011	08/16/2015	8963417	Audio Driver System And Method
13/196379	08/02/2011	09/24/2013	8540529	Shielded USB Connector Module With Molded Hood And LED Light Pipe
13/230663	08/13/2011	01/09/2016	9230928	Spot Fused Leadframe And IC Bond Pad Via Array Design For Copper Wire
13/268358	10/07/2011			System And Methods For Reliable Broadcast And Multicast Transmission Over Wireless Local Area Network
13/273122	10/13/2011	07/14/2015	9081667	Apparatus And Method For Abstract Memory Addressing
13/289221	10/24/2011	11/12/2013	8581601	Audio Programmable Gain Amplifier With Automatic Reconfiguration
13/285805	10/31/2011	11/18/2014	8892918	Method And System For Waking On Input/Output Interrupts While Powered Down
13/349526	11/04/2011	02/26/2014	8655943	Low Power Level Shifter With Output Swing Control
13/360012	11/16/2011	05/23/2013	8446210	Anti-Pop Circuit
13/312897	12/06/2011	04/19/2016	9319758	Multi-Standard Headset Support With Integrated Ground Switching
13/312879	12/06/2011	08/05/2014	8797770	System And Method For Capacitive DC-DC Converter With Variable Input And Output Voltages
13/324834	12/13/2011	03/12/2013	8396015	Scanning Threshold
13/348510	01/05/2012	10/13/2015	9160864	Systems And Methods For Stereo Echo Cancellation
13/368306	01/07/2012	09/09/2014	8865848	Systems And Methods Of Combined Bluetooth And WLAN Signaling
13/426469	03/21/2012	03/31/2015	8589324	Systems And Methods For Receiving Data At An Access Point
13/431602	03/27/2012	05/07/2015	9049781	Nonlinear Echo Suppression
13/443849	04/10/2012	09/08/2015	9130662	Systems And Methods For Management Of Wireless Clients
13/525071	05/15/2012	04/30/2013	8432148	Apparatus And Method For A Smooth Powerup Of A Reference Signal
13/633212	06/27/2012	03/11/2014	8570371	Systems And Methods For Indicating Buffered Data At An Access Point Using An Embedded Traffic Indication Map
13/540466	07/02/2012	05/05/2015	9025959	Universal Systems And Methods For Determining An Incoming Carrier Frequency And Decoding An Incoming Signal
13/552523	07/18/2012	10/28/2014	8872676	Systems And Methods For Variable Rate Conversion
13/561583	07/18/2012	10/28/2014	8872678	Systems And Methods For Variable Rate Conversion
13/555893	07/20/2012	08/10/2013	8532147	Residential Gateway For Voice Over Internet Protocol Communications
13/560439	07/27/2012	03/23/2015	8987125	System And Method To Print Documents And Images That Are Not Natively Supported By The Printer
13/597556	09/07/2012	10/08/2013	8554609	Systems And Method For Frequency Based Site-Rite Channel Scanning
13/618443	09/14/2012	06/01/2016	9498889	Analog Frontend For CCD/CMOS Sensor
13/672228	11/08/2012	03/26/2014	8681399	Systems And Methods For Improving The Scan-Line Throughput Of An Image Scanner Using A Modulated Light Source
13/683889	11/04/2012	11/01/2016	9435568	Power Management Controller For Drivers
13/719491	12/19/2012	07/06/2016	9386894	Low-Power Programmable Oscillator And Ramp Generator

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Application #	Filed Date	Issue Date	Patent #	Title
13/758053	01/18/2013	05/16/2017	9654856	System And Method For Dynamic Range Compensation Of Distortion
13/755575	01/15/2013	05/05/2013	9021803	Low-Power Current Comparator For Switched-Mode Regulator
13/765087	02/13/2013	03/21/2017	9602027	Speaker And Room Virtualization Using Headphones
13/778923	02/22/2013	10/13/2015	9160580	Adaptive Equalizer Utilizing Eye Diagram
13/788074	03/01/2013	10/18/2016	9473963	Integrated Motion Detection Using Changes In Acoustic Echo Path
13/790967	03/06/2013			System And Method For Isolating A Power-Saving Block In A Signal Circuit During Low-Power Modes
13/840302	05/15/2013	4/19/2016	9312791	Reduced-Delay Subband Signal Processing System And Method
13/261006	03/27/2013			Copper Sphere Array Package
13/950895	04/03/2013			Methods To Reduce Current Spikes In Capacitive DC-DC Converters Employing Gain-Hopping
13/860477	04/20/2013			Class-D Amplifier With Pulse Density Modulation Output Feedback For Higher Performance Acoustic Echo Canceller
13/587280	05/09/2013	05/13/2016	8773584	Low-Power Dual-Voltage-Mode Receiver
13/918073	06/27/2013	07/15/2014	8773816	Low-Power All-Digital Delay-Locked Loop Insensitive To Reference Clock Duty Cycle And Jitter
14/017037	09/03/2013	12/09/2016	9515846	Grounding Switch Method And Apparatus
14/040329	09/17/2013	08/22/2015	9182851	Semiconductor Package With Corner Pins
14/105765	12/13/2013	08/11/2016	9407992	Estimation Of Reverberation Decay Related Applications
14/164886	12/30/2013	08/15/2016	9418240	Database Hierarchical Indexes
14/160867	01/21/2014	08/23/2016	9426279	Systems And Methods For Echo Cancellation And Echo Suppression
14/183308	02/18/2014	08/28/2015	8989212	Systems And Methods For Coexistence Of Wi-Fi And Bluetooth Networks
14/189989	02/26/2014	03/15/2016	9364043	Low-Power Level Shifter With Output Swing Control
14/197183	03/04/2014			Robust Speech Boundary Detection System And Method
14/197138	03/04/2014	08/01/2016	9276625	Adaptive Biasing Technique For Audio Circuitry
14/134561	07/17/2014			Systems And Methods For Retrieving Buffered Data From An Access Point
14/567662	10/06/2014	05/16/2017	9654854	Selective Audio Source Enhancement
14/727799	06/03/2015			Nonlinear Echo Suppression
14/741353	06/16/2015	08/08/2017	9729127	System And Method For Leakage Current Control For Programmable Gain Amplifiers
14/808134	07/24/2015			Robust Acoustic Echo Cancellation For Loosely Paired Devices Based On Semi-Blind Multichannel Denoising
14/809137	07/24/2015	02/07/2017	9564146	System And Method For Multichannel On-Line Unsupervised Bayesian Spectral Filtering Of Real-World Acoustic Noise
14/842775	09/01/2015			Audio Driver System And Method
15/088073	03/31/2016			System And Method For Suppressing Transient Noise In A Multichannel System
15/132190	04/16/2016			Reduced-Delay Subband Signal Processing System And Method
62/557223	06/06/2016			Gate Boosting Circuit And Method For An Integrated Power Stage
62/517485	06/10/2016			Output Mode Control For Audio Processing
62/419416	11/08/2016			High-Speed Current Comparator Suitable For Nano-Power Circuit Design
15/355401	11/16/2016			Input/Output Mode Control For Audio Processing
62/425610	11/22/2016			Audio Digital-To-Analog Converter With Enhanced Dynamic Range
15/368402	12/7/2016			Semi-Supervised System For Multichannel Source Enhancement Through Configurable Adaptive Transformations And Deep Neural Network
62/438447	12/21/2016			Error Amplifying And Frequency Compensation Circuits And Methods
62/432480	12/22/2016			Method And System For End-User Tuning Of Active Noise Cancelling Audio Devices
62/435848	12/23/2016			Multiple Input Multiple Output (MIMO) Audio Signal Processing For Speech De-Reverberation
62/428860	12/23/2016			Online Denoising Algorithm Based On Weighted Prediction Error For Noisy Time-Varying Environments
62/441892	01/03/2017			Hum Reduction Circuit And Method
62/457663	02/10/2017			Simple Connectionist Temporal Classification (CTC) Network Using Cross-Entropy Cost Function For Binary Classification
62/457678	02/10/2017			An Efficient Connectionist Temporal Classification (CTC) Algorithm For Binary Classification
62/460636	02/17/2017			Systems And Methods For Image Optimization And Enhancement In Electrophotographic Copying
62/465723	03/01/2017			Recurrent Network For Connectionist Temporal Classification (CTC) Using The Segmented Labeled Sequence Data
62/465738	03/01/2017			Connectionist Temporal Classification (CTC) With No Latency Using The Segmented Labeled Sequence Data

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Application #	Filed Date	Issue Date	Patent #	Title
62/471856	08/25/2017			Multichannel Over-Vision Enhancement Using Headsets
62/472100	08/23/2017			Temperature And Power Protection Of Loudspeakers Using Non-Linear Feedback Control
15/476820	03/31/2017			Adaptive Configuration To Achieve Low Noise And Low Distortion In An Analog System
62/479720	03/31/2017			Correction of Current Measurement in an Amplifier
62/487443	04/19/2017			Real Time Single-Channel Speech Enhancement in Noisy And Far-Field Environments
15/885954	08/15/2017			Selective Audio Source Enhancement
62/557223	06/26/2017			Gate Boosting Circuit And Method For An Integrated Pre-Drive Stage

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