

PATENT ASSIGNMENT COVER SHEET

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Stylesheet Version v1.2

EPAS ID: PAT6594507

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
NAVITAS SEMICONDUCTOR	03/09/2021
RECEIVING PARTY DATA	
Name:	NAVITAS SEMICONDUCTOR LIMITED
Street Address:	22 FITZWILLIAM SQUARE SOUTH, SAINT PETER'S
City:	DUBLIN
State/Country:	IRELAND
Postal Code:	D02 FH68
PROPERTY NUMBERS Total: 7	
Property Type	Number
Application Number:	62059008
Application Number:	62038063
Application Number:	62039742
Application Number:	62051160
Application Number:	62052899
Application Number:	62055191
Application Number:	14554871
CORRESPONDENCE DATA	
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NAME OF SUBMITTER:	ERICA M. ROJAS
SIGNATURE:	/Erica M. Rojas/
DATE SIGNED:	03/10/2021

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Total Attachments: 4

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**ASSIGNMENT
(Patents and Patent Applications)**

This Assignment, effective as of the last execution date written below is made by NAVITAS SEMICONDUCTOR, a corporation having a place of business at 3007 Malibu Canyon Road, Malibu, CA 90265, United States of America, hereinafter referred to as "Assignor," to NAVITAS SEMICONDUCTOR LIMITED, a company of Ireland, having a place of business at 22 Fitzwilliam Square South, Saint Peter's, Dublin, D02 FH68, Republic of Ireland, hereinafter referred to as "Assignee,"

WHEREAS, Assignor is currently the sole owner of all rights, title, and interest in certain Patents and Patent Applications (as defined in Appendix A);

WHEREAS, Assignor desires to transfer and Assignee is desirous of acquiring all rights, title, and interest in the Patents and Patent Applications currently owned by Assignor;

NOW THEREFORE, for good and valuable consideration, receipt of which is hereby acknowledged, Assignor agrees as follows:

1. Agrees to assign, transfer, convey, and sell; hereby assigns, transfers, conveys and sells; and has assigned, transferred, conveyed, and sold to Assignee all right, title and interest in and to:
 - (a) the patents and patent applications identified in the supplemental sheet appended hereto:

(hereinafter "the Patents and Patent Applications") and to intellectual property (including, without limitation, any innovation, information, invention, discovery, product, process, work or design) disclosed, embodied, shown, or claimed in the Patents and Patent Applications, implicitly or explicitly;
 - (b) the Patents and Patent Applications, the right to claim priority to the Patent Applications, all applications based in whole or in part upon the above-referenced Patents and Patent Applications, including, without limitation, all applications that are provisional, non-provisional, design, divisional, continuation, continuation-in-part, registration, utility model, industrial design, reissue, renewal, substitute, extension, reexamination, post-grant review, inter partes review, supplemental examination or non-U.S. patent applications or applications for other rights based in whole or in part on the Patents and Patent Applications;
 - (c) all patents (including, without limitation, all U.S. and non-U.S. patents, registrations, utility models, industrial designs, design patents, counterparts, continuations, continuations-in-part, divisionals, reissues, renewals, substitutes, extensions, reexaminations, post-grant reviews, inter partes reviews and supplemental examinations) that are granted or issued upon, or that claim priority to, any and all Patent Applications or that disclose or claim intellectual property described in (a) of this paragraph, in whole or in part; and

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- (d) all claims for damages by reason of past infringement of any rights under (a), (b) or (c) of this paragraph (including provisional rights to reasonable royalties pursuant to 35 U.S.C. §154(d)) and the right to sue for and collect such damages and royalties for Assignee's own use;
2. Agrees to authorize and request the U.S. Patent and Trademark Office or any other U.S. or non-U.S. agency to issue to the Assignee any and all patent(s), or other rights or documents, resulting from the intellectual property, patent applications and patents described in Paragraph 1 of this Assignment.
 3. Agrees to sign all papers and documents, including without limitation, applications, declarations, oaths and petitions, and, at the Assignee's expense, perform any other acts that are necessary in connection with prosecution of patent application(s) or intellectual property described in paragraph 1 of this Assignment and the enforcement of patent(s) or other rights resulting from such patent application(s) or intellectual property.
 4. Agrees that the terms, covenants, and conditions of this Assignment shall inure to the benefit of the Assignee, its successor, assign and other legal representative, and shall be binding upon Assignor, as well as its successor, assign and other legal representative.
 5. Promises and affirms that Assignor has not entered, and will not enter, into any assignment, contract, or understanding that conflicts with this Assignment.

NAVITAS SEMICONDUCTOR
(Assignor)

Date: 3/9/2021

By: 

Name: Daniel M. Kinzer

Title: Chief Technical Officer

NAVITAS SEMICONDUCTOR
LIMITED (Assignee)

Date: 3/9/2021

By: 

Name: Daniel M. Kinzer

Title: Chief Technical Officer

Appendix A

U.S. Application No. 62/059,008 entitled "SOFT SWITCHING SCHEME FOR FLYBACK AND DC TO DC CONVERTERS" filed on October 2, 2014

U.S. Application No. 62/038,063 entitled "GAN OVERVOLTAGE PROTECTION CIRCUIT" filed on August 15, 2014

U.S. Application No. 62/039,742 entitled "POWER TRANSISTOR WITH DISTRIBUTED SCHOTTKY DIODE AND LOW RG" filed on August 20, 2014

U.S. Application No. 62/051,160 entitled "HYBRID HALF-BRIDGE DRIVER USING GAN AND SILICON DEVICES" filed on September 16, 2014

U.S. Application No. 62/052,899 entitled "IMPROVED GAN STRUCTURES" filed on September 19, 2014

U.S. Application No. 62/055,191 entitled "SOFT SWITCHED SINGLE STAGE WIRELESS TRANSFER" filed on September 25, 2014

U.S. Application No. 14/554,871 entitled "ZERO VOLTAGE SOFT SWITCHING SCHEME FOR POWER CONVERTERS" filed on November 26, 2014