

PATENT ASSIGNMENT COVER SHEET

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 Stylesheet Version v1.2

EPAS ID: PAT6603669

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
VASANTH RANGANATHAN	11/26/2020
JOYDEEP RAY	12/18/2020
ABHISHEK R. APPU	01/21/2021
NIKOS KABURLASOS	12/10/2020
LIDONG XU	12/11/2020
SUBRAMANIAM MAIYURAN	12/22/2020
ALTUG KOKER	02/01/2021
NAVEEN MATAM	01/20/2021
JAMES HOLLAND	02/08/2021
BRENT INSKO	01/26/2021
SANJEEV JAHAGIRDAR	01/20/2021
SCOTT JANUS	12/15/2020
DURGAPRASAD BILAGI	12/15/2020
XINMIN TIAN	12/10/2020
RECEIVING PARTY DATA	
Name:	INTEL CORPORATION
Street Address:	2200 MISSION COLLEGE BLVD.
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95054
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17095530
CORRESPONDENCE DATA	
Fax Number:	(720)815-3362
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Email:	intel-para-legals@jwmhlaw.com
Correspondent Name:	JAFFERY WATSON MENDONSA & HAMILTON LLP

Address Line 1:	7501 VILLAGE SQUARE DRIVE, SUITE 206
Address Line 4:	CASTLE PINES, COLORADO 80108

ATTORNEY DOCKET NUMBER:	AC5169-US
NAME OF SUBMITTER:	APRIL PIEPENBURG
SIGNATURE:	/April Piepenburg/
DATE SIGNED:	03/16/2021

Total Attachments: 15

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ASSIGNMENT

In consideration of good and valuable consideration, the receipt of which is hereby acknowledged, we, the undersigned inventor(s):

**Vasanth Ranganathan, Joydeep Ray, Abhishek R. Appu, Nikos Kaburlasos, Lidong Xu,
Subramaniam Maiyuran, Altug Koker, Naveen Matam, James Holland, Brent Insko,
Sanjeev Jahagirdar, Scott Janus, Durgaprasad Bilagi, Xinmin Tian**

hereby sell, assign, and transfer to:

Intel Corporation

a corporation of Delaware, having a principal place of business at 2200 Mission College Boulevard, Santa Clara, California, 95054 USA ("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all other countries, in and to any and all inventions and improvements that are disclosed in the application for the patent entitled:

SYSTEMS AND METHODS FOR ERROR DETECTION AND CONTROL FOR EMBEDDED MEMORY AND COMPUTE ELEMENTS

(I hereby authorize and request any attorney having appropriate authority from the assignee to insert on the designated lines below, the filing date and application number of said application when known.)

which was filed on November 11, 20 as

US Application Number 17/095,530 and
COUNTRY or International Office

which has been executed by the undersigned prior hereto or concurrently herewith on the date(s) indicated below.

and in and to said application and all Additional Applications, and all other patent applications that have been or shall be filed in the United States and all other countries and international filing offices on any of said inventions and improvements; and in and to all original and reissued patents that have been or shall be issued in the United States and all other countries and international filing offices on said inventions and improvements; and in and to all rights of priority resulting from the filing of said applications; as used herein "Additional Applications" includes but is not limited to design, utility, utility model, divisional, continuing, continuation-in-part, substitute, renewal, reissue, and national phase applications on said inventions and improvements;

and agree that said Assignee may apply for and receive a patent or patents for said inventions and improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all Additional Applications, and all other patent applications on any and all said inventions and improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors,

assigns, and legal representatives all facts known to the undersigned relating to said inventions and improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or legal representatives in securing and maintaining proper patent protection for said inventions and improvements and for vesting title to said inventions and improvements, and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

R. Vasanth

Vasanth Ranganathan

11/26/2020

Date signed

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Abhishek R. Appu

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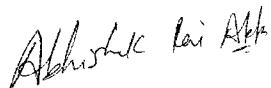
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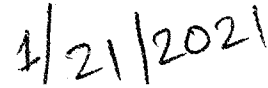
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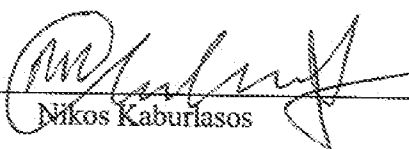
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S. Maiyuran

Subramaniam Maiyuran

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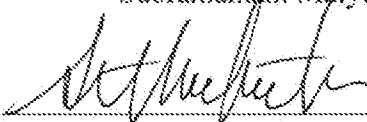
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_____ Abhishek R. Appu	_____ Date signed
_____ Nikos Kaburlasos	_____ Date signed
_____ Lidong Xu	_____ Date signed
_____ Subramaniam Maiyuran	_____ Date signed
 _____ Altug Koker	<u>2/1/2021</u> _____ Date signed
_____ Naveen Matam	_____ Date signed
_____ James Holland	_____ Date signed

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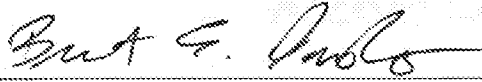
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James Holland



Date signed



Brent Insko

1/26/21

Date signed

Sanjeev Jahagirdar

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Scott Janus

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Durgaprasad Bilagi

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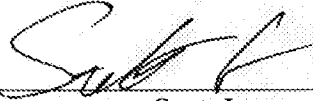
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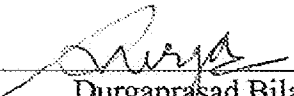
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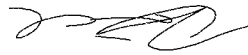
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