506562950 03/18/2021

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT6609729

SUBMISSION TYPE:	NEW ASSIGNMENT	
NATURE OF CONVEYANCE:	ASSIGNMENT	

CONVEYING PARTY DATA

Name	Execution Date
XILINX GMBH	03/18/2021

RECEIVING PARTY DATA

Name:	XILINX, INC.	
Street Address:	2100 LOGIC DRIVE	
City:	SAN JOSE	
State/Country:	CALIFORNIA	
Postal Code:	95124	

PROPERTY NUMBERS Total: 1

Property Type	Number	
Application Number:	17196574	

CORRESPONDENCE DATA

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Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 5618017700

Email: xilinxpatent@iplawpro.com

Correspondent Name: CUENOT, FORSYTHE & KIM, LLC

Address Line 1: 20283 STATE ROAD 7

Address Line 2: STE. 300

Address Line 4: BOCA RATON, FLORIDA 33498

ATTORNEY DOCKET NUMBER:	X-6303 US (8131-0535)	
NAME OF SUBMITTER:	KEVIN T. CUENOT	
SIGNATURE:	/Kevin T. Cuenot/	
DATE SIGNED:	03/18/2021	

Total Attachments: 2

source=X-6303 US Xilinx GmbH Assignment to Xilinx Inc_signed#page1.tif source=X-6303 US Xilinx GmbH Assignment to Xilinx Inc_signed#page2.tif

PATENT 506562950 REEL: 055651 FRAME: 0194

X-6303 US

PATENTASSICSMENT

This Patern Assignment (the "Assignment") is made from Xilinx GmbH, a company organized and existing under the laws of Germany, whose principal place of business is Willy-Brandt-Allee 4, Munich \$1829, Germany, (the "Assignor") to Xilinx, Inc., a Delaware corporation, whose principal place of business is 2100 Logic Drive, San Jose, CA 95124 (the "Assignee").

Assignor desires to assign to Assignee and its successors all of Assignor's right, title and interest in and to the patents and any and all corresponding patent applications, divisional applications, continuing applications, provisionals, retissues, reexams, and extensions thereof related thereto or based thereon, any and all patents issuing thereon, and any and all foreign patents and patent applications corresponding thereto or relying thereon (including corresponding PCT applications and any patents resulting therefrom) for its filing or convention date (the "Patents") set forth in Schedule A which is attached hereto, together with the goodwill of the business pertaining thereto.

In consideration for good and valuable consideration, receipt and sufficiency of which is hereby acknowledged, Assignor does hereby assign, transfer, grant, sell and otherwise convey to Assignee and its successors all of Assignor's rights, title and interest in and to the Invention, including all common law rights therein, applications to register therefor, together with the good will of the business symbolized by the Patents and all claims for damages by reason of past infringements of the Patents with the right to sue for and collect the same for its own use and benefit, and for the use and on behalf of its successors, assigns and other legal representatives. Assignor authorizes the Director of Patents and Trademarks of the United States of America, and the empowered officials of all other governments to issue or transfer all said patents and patent applications to Assignee, as assignee thereof, or otherwise as Assignee may direct.

Dated: 18/03/1021

Assignor: Xilinx GmbH

Name: Brendan Farley
Title: Managing Director

Dated March 15, 2021

Assignee: Xiliax, Inc.

(alia Hagopau

Name: Catia Hagopian

Title: Senior Vice President and General Counsel

SCHEDULE A

Attorney Docket No.	Country	Application No.	Filing Date	Title
X-6303 US (8131-0535)	U.S.	17/196,574	March 9, 2021	DATA PROCESSING ENGINE ARRAY
				ARCHITECTURE WITH MEMORY
				TILES

PATENT REEL: 055651 FRAME: 0196

RECORDED: 03/18/2021