

## PATENT ASSIGNMENT COVER SHEET

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Stylesheet Version v1.2

EPAS ID: PAT6682323

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
SILANNA SEMICONDUCTOR NORTH AMERICA, INC.	09/13/2018
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	SILANNA ASIA PTE LTD
<b>Street Address:</b>	10 COLLYER QUAY
<b>Internal Address:</b>	#10-01 OCEAN FINANCIAL CENTRE
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<b>State/Country:</b>	SINGAPORE
<b>Postal Code:</b>	049315
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
Application Number:	17302060
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<b>ATTORNEY DOCKET NUMBER:</b>	SILSP055C2
<b>NAME OF SUBMITTER:</b>	L. JON LINDSAY
<b>SIGNATURE:</b>	/L. Jon Lindsay/
<b>DATE SIGNED:</b>	04/28/2021
<b>Total Attachments: 1</b>	
source=20210422_SILSP055C2_Assignment_Silanna_Semi_NA_to_Silanna_Asia#page1.tif	

PATENT ASSIGNMENT

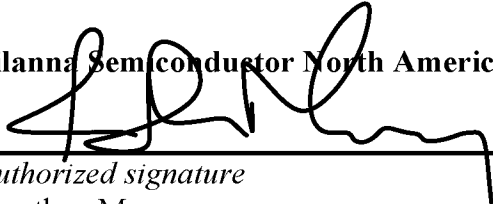
For good and valuable consideration, the receipt of which is hereby acknowledged, **Silanna Semiconductor North America, Inc.** ("Assignor"), having a registered office at 4795 Eastgate Mall, Suite 100, San Diego, CA 92121, USA, hereby assigns to **Silanna Asia Pte Ltd** ("Assignee"), having a registered office at 10 Collyer Quay, #10-01 Ocean Financial Centre, Singapore, 049315, all of its right, title and interest in and to the U.S. patent application listed below and the right to file non-provisionals, divisionals, continuations, continuations-in-part, substitute, continued prosecution, request for continued examination, renewals, reexaminations and reissues, and foreign filing of the patent application listed below, including but not limited to the right to file, prosecute and issue patent applications and patents claiming priority to the patent application listed below for the invention set forth in the patent application listed below and, including without limitation, any and all claims and potential claims of past, present and future infringement of such patent application and the right to sue and collect for past, present and future damages and any other causes of action under such patent application ("Assigned Patent Rights"):

Serial No.	Filing Date	Title	Inventors
Unassigned	Herewith	Laterally Diffused MOSFET with Low Rsp*Qg Product	David Snyder

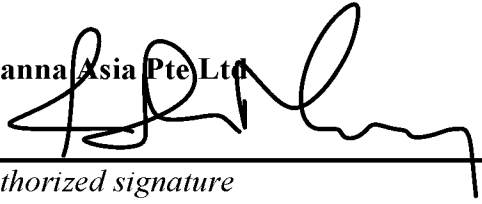
Assignor also hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, letters patents, utility models or certificates of invention which may be granted upon the Assigned Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

IN WITNESS WHEREOF Assignor has executed this Patent Assignment this 13th day of September, 2018

**Silanna Semiconductor North America, Inc.**

  
 \_\_\_\_\_  
 Authorized signature  
 Jonathan Murray  
 \_\_\_\_\_  
 Name (Printed)  
 Treasurer  
 \_\_\_\_\_  
 Title

**Silanna Asia Pte Ltd**

  
 \_\_\_\_\_  
 Authorized signature  
 Jonathan Murray  
 \_\_\_\_\_  
 Name (Printed)  
 Director  
 \_\_\_\_\_  
 Title