

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

EPAS ID: PAT6700018

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
CIRRUS LOGIC INTERNATIONAL SEMICONDUCTOR LTD.	03/29/2015
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	CIRRUS LOGIC, INC.
<b>Street Address:</b>	800 WEST 6TH STREET
<b>City:</b>	AUSTIN
<b>State/Country:</b>	TEXAS
<b>Postal Code:</b>	78701
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	16988796
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(202)420-2201
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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<b>ATTORNEY DOCKET NUMBER:</b>	200735-00362
<b>NAME OF SUBMITTER:</b>	STEPHEN A. SOFFEN
<b>SIGNATURE:</b>	/Stephen A. Soffen/
<b>DATE SIGNED:</b>	05/10/2021
<b>Total Attachments: 11</b>	
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## PATENT ASSIGNMENT

This Patent Assignment (this "*Assignment*"), effective as of 11:00 a.m. CDT, March 29, 2015 (the "*Effective Time*"), is by and between Cirrus Logic International Semiconductor Ltd., a Scottish limited company having its principal place of business at Westfield House, 26 Westfield Road, Edinburgh, EH11 2QB, United Kingdom ("*Assignor*"), and Cirrus Logic, Inc., a Delaware corporation having its principal place of business at 800 West 6th Street, Austin, Texas 78701 ("*Assignee*"). Each of the parties is referred to individually as a "*Party*," and collectively as the "*Parties*."

Assignor and Assignee have entered into a Purchase and Sale Agreement, effective as of the Effective Time, pursuant to which Assignor has agreed to assign to Assignee the Patents (as defined below).

In consideration of the promises and covenants set forth in this Assignment and in the Purchase and Sale Agreement, the Parties agree as follows:

1. "*Patents*" means the patents and patent applications listed on Schedule 1 hereto, and any utility models, supplementary protection certificates, statutory invention registrations, patents and applications for same, and extensions, divisions, continuations, continuations-in-part, reexaminations, and reissues thereof.

2. "*United States*" means the geographic territory of the United States of America, including the 50 States, District of Columbia, Puerto Rico, Guam, Northern Marianas, United States Virgin Islands and American Samoa.

3. Assignor hereby, absolutely and unconditionally, conveys, sells, assigns, transfers, grants and sets over unto the Assignee, all of the Assignor's right, title and interest and benefit in and to the Patents, together with all rights of action, both at law and in equity with respect thereto, including all rights to sue, settle any claim, and collect all damages for any past, present, or future infringement or misappropriation of the Patents, the same to be held and enjoyed by the Assignee, its successors and assigns forever, as fully and entirely as the same could have been held and enjoyed by the Assignor if this assignment had not been made and the Assignee does hereby accept such sale, assignment, transfer, grant, conveyance and set over; *provided*, however, that with respect to any pending patent application within the Patents ("*U.S. Application*"), the assignment in this Assignment will not become effective until the issuance of such U.S. Application as a patent in the United States.

4. To the extent any provision in this Assignment is inconsistent with the Purchase and Sale Agreement, the provisions of the Purchase and Sale Agreement will control.

[*Signature page follows.*]

IN WITNESS WHEREOF, Assignor and Assignee have caused this Assignment to be signed and executed by the undersigned officers duly authorized and effective as of the date set forth above.

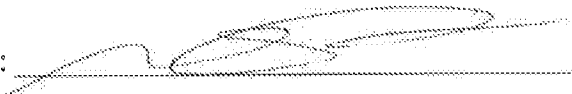
CIRRUS LOGIC INTERNATIONAL  
SEMICONDUCTOR LTD.

By:  \_\_\_\_\_

Name: Pedro Andrade

Title: Director

CIRRUS LOGIC, INC.

By:  \_\_\_\_\_

Name: Gregory Scott Thomas

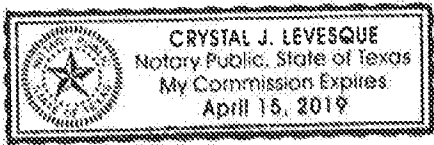
Title: Vice President, General Counsel and  
Secretary

*[Notarization page follows.]*

STATE OF TEXAS )  
 ) §§  
COUNTY OF TRAVIS )

Before me, the undersigned, a notary public in and for said County and State, personally appeared Pedro Andrade, an authorized representative of Cirrus Logic International Semiconductor Ltd., a Scottish limited company, who acknowledged the execution of the foregoing Patent Assignment to be his voluntary act and deed.

WITNESS MY HAND AND SEAL THIS 7<sup>th</sup> DAY OF APRIL, 2015.



Crystal J. Levesque  
Notary Public

Printed: Crystal J. Levesque

My Commission Expires: April 15, 2019

STATE OF TEXAS )  
 ) §§  
COUNTY OF TRAVIS )

Before me, the undersigned, a notary public in and for said County and State, personally appeared Gregory Scott Thomas, an authorized representative of Cirrus Logic, Inc., a Delaware corporation, who acknowledged the execution of the foregoing Patent Assignment to be his voluntary act and deed on behalf of said company.

WITNESS MY HAND AND SEAL THIS 9<sup>th</sup> DAY OF APRIL, 2015.



Karen Klein  
Notary Public

Printed: Karen Klein

My Commission Expires: June 11, 2019

Schedule I  
Patents

HL Ref.	CL Ref.	Country	Catch Word	Status	Priority Date	App. No.	App. Date	Pub. No.	Reg. No.	Reg. Date
P113919US00	P1005US00	US	T/W/M Joint Invention	Granted	13-Feb-97	08/799904	13-Feb-97		5874909	2/23/1999
P113868US00	P1014US00	US	Dynamic Element Matching	Granted	26-Feb-98	09/622178	25-Oct-90		6583742	6/24/2003
P113837US00	P1022US00	US	D-Thump	Granted	10-May-02	10/146114	16-May-02		6600365	7/29/2003
P113852US00	P1016US00	US	DAC Reference Discharge Scheme	Granted	16-May-02	10/153913	24-May-02		6573850	6/3/2003
P113582US00	P1038US00	US	ADC Back End	Granted	10-Sep-02	10/247357	20-Sep-02	US 2004-0046605 A1	6831507	12/14/2004
P113349US00	P1006US00	US	Temp Sensing	Granted	1-Oct-02	10/265414	7-Oct-02	US 2004-0062292 A1	6799689	10/8/2004
P113820US00	P1040US00	US	Audio multiplier	Granted	26-Nov-02	10/347300	21-Jan-03	US 2004-0100319 A1	6828845	12/7/2004
P113853US00	P1072US00	US	PLL CS	Granted	14-Apr-03	10/459606	12-Jun-03	US 2004-0201425 A1	6856202	2/15/2005
P113858US00	P1050US00	US	DAC RCS (Reference Chopper Scheme)	Granted	5-Dec-03	10/770387	4-Feb-04	US 2005-0122245 A1	6952176	10/4/2005
P113755US00	P1048US00	US	Class D BP	Granted	15-Sep-03	10/770407	4-Feb-04	US 2005-0068209 A1	7257164	8/14/2007
P113756US00	P1052US00	US	WLCRC (PWM noise shaping 8698)	Granted	5-Dec-03	10/777763	13-Feb-04	US 2005-0122241 A1	7061415	6/13/2006
P113861US00	P1129US00	US	LNOA	Granted	16-Mar-04	10/837779	4-May-04	US 2005-0206454 A1	7113042	9/26/2006
P113857US00	P1049US00	US	Triple Well	Granted	26-Nov-03	10/841454	10-May-04	US 2005-0110574 A1	7030699	4/18/2006
P113856US00	P1074US00	US	Dual Loop	Granted	17-Dec-03	10/988694	15-Nov-04	US 2005-02281367 A1	7583774	9/1/2009
P112774US00	P1055US00	US	Merged Equalizer (Merged 3D and Graphic Equalizer)	Granted	18-Oct-04	11/002207	3-Dec-04	US 2006-0083361 A1	7466883	12/16/2008
P113498US00	P1054US00	US	Algorithmic ADC	Granted	11-Oct-04	11/094037	28-Mar-05	US 2006-0077086 A1	7106240	9/12/2006
P112621US00	P1056US00	US	PWM Tri-Level	Granted	17-Jan-05	11/110858	21-Apr-05	US 2006-0158359 A1	7205917	4/1/2007
P113822US00	P1057US00	US	Flipping DAC	Granted	19-Apr-05	11/140951	1-Jun-05		7102557	9/5/2006
P112953US00	P1155US00	US	Dual Power Bus #2	Granted	26-Jul-04	11/185718	21-Jul-05	US 2006-0022640 A1	7402986	7/22/2008

P110269US00	P1058US00	US	Class D Feedback Driver	Granted	17-Aug-05	11/225845	14-Sep-05	2007-0040608	7348840	3/25/2008
P113861US01	P1129US01	US	LNOA+ESD Drv 01	Granted	16-Mar-04	11/267130	4-May-04	US 2006-0109055 A1	7301401	11/27/2007
P1102728US00	P1099US00	US	Current Sense Circuit	Granted	27-Oct-05	11/318458	28-Dec-05	2007-0096748	7301547	11/27/2007
P113900US00	P1076US00	US	AH 2.0 Digital Mix P&M	Granted	26-Nov-05	11/318596	28-Dec-05	US 2007-0123192 A1	7885422	2/8/2011
P113901US00	P1072US00	US	AH 1.0 Digital Only Path	Granted	26-Nov-05	11/318600	28-Dec-05	US 2007-0124150 A1	7765019	7/27/2010
P112657US00	P1077US00	US	AH 3.0 Partitionable Bus & Bridge	Granted	26-Nov-05	11/318601	28-Dec-05	US 2007-0124526 A1	7376778	5/20/2008
P1102825US00	P1060US00	US	DLL	Granted	1-Feb-06	11/406323	19-Apr-06	2007-0176656	7295053	11/13/2007
P108181US00	P1031US00	US	MEMS DUAL SL	Granted	20-Mar-06	11/723514	20-Mar-07	US-2007-2007/0284682	7781249	8/24/2010
P113686US00	P1023US00	US	Configurable regulators	Granted	25-Jul-06	11/783397	9-Apr-07	2008-0024108	7573152	8/11/2009
P113817US00	P1078US00	US	Debounce	Granted	11-Apr-06	11/783579	10-Apr-07	2007-0236257	7579894	8/25/2009
P110282US00	P1061US00	US	Slope Compensation	Granted	26-Apr-06	11/785277	16-Apr-07	US-2007-0252567-A1	7605574	10/20/2009
P106983US00	P1015US00	US	Vector Quantiser	Granted	31-Jan-07	11/822565	6-Jul-07	US-2008-0180292-A1	7508331	3/24/2009
P107011US00	P1093US00	US	LED Driver	Granted	14-Jul-05	11/822830	10-Jul-07	US-2008-0048567	7884558	2/8/2011
P107012US00	P1066US00	US	LED Protection	Granted	14-Jul-06	11/822831	10-Jul-07	US-2008-0012508	7884557	2/8/2011
P110270US00	P1026US00	US	Class D Start Up	Granted	14-Jul-06	11/826369	13-Jul-07	US-2008-0024213	8139792	3/20/2012
P106406US00	P1062US00	US	S-Shape PD	Granted	31-Aug-06	11/892641	24-Aug-07	US-2008-0069376	8208658	6/26/2012
P106204US00	P1028US00	US	S-Shape PU	Granted	31-Aug-06	11/892642	24-Aug-07	US-2008-0056513	8204251	6/19/2012
P110277US00	P1012US00	US	4 Switch	Granted	31-Aug-06	11/892885	28-Aug-07	US-2008-0055946	7723965	5/25/2010
P114089US00	P1064US00	US	Bandgap Startup	Granted	6-Oct-06	11/902025	18-Sep-07	US 2008-0224682 A1	7626374	12/1/2009
P108267US00	P1047US00	US	BaBo sensing	Granted	7-Jun-07	11/905822	4-Oct-07	US-2008-0303502	7777457	8/17/2010
P106982US00	P1046US00	US	Skp Shuffler	Granted	18-Dec-06	11/957271	14-Dec-07	US-2008-0143572	7567195	7/28/2009
P110286US00	P1025US00	US	BLC	Granted	5-Dec-06	11/987772	4-Dec-07	US-2008-0129563	7649480	1/19/2010
P110274US00	P1068US00	US	DMCP	Granted	23-Dec-06	12/003545	13-Dec-07	US-2008-0150619	7626445	12/1/2009

P110271US00	P1071US00	US	2FB CAP	Granted	22-Dec-06	12/000546	13-Dec-07	US-2006-0150620	7996742	8/2/2011
P110272US00	P1036US00	US	LSCP	Granted	22-Dec-06	12/000547	13-Dec-07	US-2006-0150621	7,622,984	11/24/2009
P110273US00	P1076US00	US	HFOne Amp	Granted	22-Dec-06	12/000548	13-Dec-07	US-2006-0159567	8369856	1/29/2013
P1102721US00	P1069US00	US	Class G+	Granted	22-Dec-06	12/000549	13-Dec-07	2008-00116979	7714660	5/11/2010
P107221US00	P1017US00	US	WISCE Reg Mem	Granted	30-Jan-07	12/007007	4-Jan-08	US-2006-0216007	8140465	3/20/2012
P108149US00	P1009US00	US	DSD Silence	Granted	30-Mar-07	12/047001	12-Mar-08	US-2008-0240462	8081770	12/20/2011
P108266US00	P1034US00	US	dB LUT	Granted	26-Apr-07	12/108188	23-Apr-08	US-2008-0270441	8280937	10/2/2012
P116550US00	P1169US00	US	SROCR Hard	Granted	20-Mar-08	12/117421	8-May-08	US-2009-0238323	7702943	4/20/2010
P109028USPC	P1099USPC	US	SPQ ROM	Granted	26-Jan-06	12/160986	17-Jan-07	US-2010-0195842-A1	8472636	6/25/2013
P116284US00	P1008US00	US	30 Pin Mate	Granted	29-Aug-07	12/201893	29-Aug-08	2009-0058497	7682661	3/23/2010
P116283US00	P1607US00	US	Linear+SDAC	Granted	6-Sep-07	12/203542	3-Sep-08	US-2009-0066552	7786916	8/31/2010
P108306US00	P1002US00	US	WLR FB	Granted	31-Jul-07	12/219074	15-Jul-08	US-2009-0033382	7667508	2/23/2010
P108319US00	P1019US00	US	Frac Div	Granted	31-Jul-07	12/219075	15-Jul-08	US-2009-0033376	7724048	5/25/2010
P109150US00	P1098US00	US	SPQ Gyration	Granted	13-Nov-07	12/269632	12-Nov-08	US-2009-0123003	8045724	10/25/2011
P107777USPC	P1033USPC	US	MEMS Process	Granted	24-Feb-06	12/280669	23-Feb-07	US-2009-0152655	8497149	7/30/2013
P107775USPC	P1092USPC	US	MEMS SR	Granted	20-Mar-06	12/293537	20-Mar-07	US-2009-0278217	8643129	2/4/2014
P109036USPC	P1106USPC	US	SPQ 1B ADC	Granted	12-Apr-06	12/295709	11-Apr-07	US-2009-0046867	8165312	4/24/2012
P109428US00	P1105US00	US	ALC AnbC	Granted	19-Dec-07	12/338348	18-Dec-08	US-2009-0161889	8315410	11/20/2012
P109412US00	P1102US00	US	BOF DAC	Granted	31-Mar-08	12/414,075	30-Mar-09	2009-0243902	7804434	9/28/2010
P110932US00	P1184US00	US	MEMS E-SCF	Granted	7-May-08	12/436909	7-May-09	2009-0279719	3066623	11/29/2011
P113856US01	P1074US01	US	Dual Loop Cont 01	Granted	17-Dec-03	12/533422	15-Nov-04	2010-0020912	7949083	5/24/2011
P110971US00	P1186US00	US	Sample Rate Converter	Granted	13-Oct-08	12/578366	13-Oct-09	US-2010-0091922	7,948,405	5/24/2011
P109636USPC	P1160US00	US	SPQ ASM	Granted	9-May-07	12/599026	8-May-08	US-2010-0086144	8953814	2/10/2015

PATENT

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P110277US01	P1012US01	US	4 Switch Cont 01	Granted	31-Aug-06	12/615291	28-Aug-07	US-2010-0052638-A1	7994763	8/9/2011
P110288US01	P1025US01	US	BLC Div 01	Granted	5-Dec-06	12/620217	4-Dec-07	US-2010-0060498	7994954	8/9/2011
P111713US00	P1198US00	US	MEMS E-CP Trim	Granted	30-Dec-08	12/649525	30-Dec-09	US-2010-0166228	889726	4/15/2014
P111707US00	P1200US00	US	MEMS E-Poly PIN Diode	Granted	30-Dec-08	12/649541	30-Dec-09	US-2010-0164068	8581356	11/12/2013
P111468US00	P1195US00	US	MEMS E-Test	Granted	30-Dec-08	12/649623	30-Dec-09	US-2010-0219839-A1	8643382	2/4/2014
P111734US00	P1201US00	US	MEMS E-CP Leaky D	Granted	30-Dec-08	12/649879	30-Dec-09	US-2010-0166229	8750539	6/10/2014
P108672USPC	P1045US00	US	CM Macro	Granted	25-Jul-07	12/670607	25-Jan-08	US-2010-0205412-A1	8751025	6/10/2014
P113920USPC	P1090US00	US	1L 2V	Granted	31-Jul-07	12/671364	25-Jul-08	US-2010-0194359	8427121	4/23/2013
P108434USPC	P1119US00	US	V/SVPs & VF	Granted	3-Aug-07	12/671639	4-Aug-08	US-2010-0219888-A1	8687826	4/1/2014
P109742USPC	P1126US00	US	AS-S/V	Granted	3-Aug-07	12/671656	4-Aug-08	2011-0221533	8198941	6/12/2012
P108929USPC	P1096US00	US	MEMS K2 ICP	Granted	17-Aug-07	12/673925	15-Aug-08	US-2011-0089504-A1	8546170	10/1/2013
P108291USPC	P1029US00	US	MEMS BPS	Granted	19-Sep-07	12/678884	18-Sep-08	US-2010-0308425-A1	8841737	9/23/2014
P108143USPC	P1083US00	US	MEMS Fused	Granted	19-Sep-07	12/678903	18-Sep-08	2010-0244162	8482088	7/9/2013
P108926USPC	P1094US00	US	MEMS uBPS	Granted	19-Sep-07	12/678922	18-Sep-08	US-2010-0237447-A1	8198715	6/12/2012
P108181US01	P1031US01	US	MEMS DUAL SL CONT. 01	Granted	20-Mar-06	12/719999	20-Mar-07	US-2010-0155864-A1	7856804	12/28/2010
P110297USPC	P1130US00	US	CPIL	Granted	11-Dec-07	12/744073	8-Dec-08	US-2010-0277152-A1	8264273	9/11/2012
P109795USPC	P1139US00	US	T1 OSIRF	Granted	21-Dec-07	12/745726	12-Dec-08	US-2010-0306297-A1	8645444	2/4/2014
P110272US03	P1069US03	US	Class G+ V/NV CP	Granted	22-Dec-06	12/759900	13-Dec-07	US-2010-0194479-A1	8375506	2/12/2013
P109794USPC	P1183US00	US	T1 GCNL	Granted	21-Dec-07	12/808616	11-Dec-08	US-2010-0266137 A1	8737633	5/27/2014
P109792USPC	P1177US00	US	T1 LRF	Granted	21-Dec-07	12/808931	12-Dec-08	US-2010-0310086-A1	8908876	12/9/2014
P109797USPC	P1181US00	US	T1 FCDP	Granted	21-Dec-07	12/809028	12-Dec-08	US-2010-0322432-A1	8670571	3/11/2014
P109456USPC	P1161US00	US	MEMS E-DPL	Granted	7-May-08	12/812390	7-May-09	US-2010-0315272-A1	8913762	12/16/2014

P113901US01	P1073US01	US	AH 1.1 Adding inside codec	Granted	26-Nov-05	12/833222	28-Dec-05	2010-0317397	8433430	4/30/2013
P112948US00	P1223USFEF00	US	PMIC1 Reg Con ABC	Granted	22-Jul-09	12/841515	22-Jul-10	US-2011-0022859-A1	8612779	12/17/2013
P113612US00	P1237USFEF00	US	PMIC1 BF Current limit	Granted	22-Jul-09	12/841589	22-Jul-10	US-2011-0018515-A1	8519691	8/27/2013
P112948US00	P1226USFEF00	US	PMIC1 NVN ABC	Granted	22-Jul-09	12/841591	22-Jul-10	US-2011-0022826-A1	8607036	12/10/2013
P113611US00	P1236USFEF00	US	PMIC1 BF Preset & SC	Granted	22-Jul-09	12/841604	22-Jul-10	US-2011-0018507-A1	8729880	5/20/2014
P113610US00	P1235USFEF00	US	PMIC1 BF Level shift	Granted	22-Jul-09	12/841637	22-Jul-10	US-2011-0018588-A1	8628155	1/28/2014
P113609US00	P1234USFEF00	US	PMIC1 BF Diode mode	Granted	22-Jul-09	12/841688	22-Jul-10	US-2011-0018516-A1	8513933	8/20/2013
P113608US00	P1233USFEF00	US	PMIC1 BF Emulation	Granted	22-Jul-09	12/841722	22-Jul-10	US-2011-0050185-A1	8541993	9/24/2013
P110272US05	P1069US05	US	Class G+ V=N+1	Granted	22-Dec-06	12/966228	13-Dec-07	US-2011-0103619-A1	8183931	5/22/2012
P110272US06	P1069US06	US	Class G+ Controller	Granted	22-Dec-06	12/966248	13-Dec-07	US-2011-0115563	8279012	10/2/2012
P109266USPC	P1101US00	US	DC CAL	Granted	7-May-08	12/991190	7-May-09	US-2011-0055877-A1	8824703	9/2/2014
P109973USPC	P1131US00	US	T1 RXTX	Granted	27-Jan-08	13/0901386	24-Jan-09	US-2011-0130176-A1	8682250	3/25/2014
P113856US02	P1074US02	US	Dual Loop Cont. 2	Granted	17-Dec-03	13/113168	15-Nov-04	2011-0721487	8537957	9/17/2013
P110271US01	P1071US01	US	ZFB CAP	Granted	22-Dec-06	13/151987	13-Dec-07	2011-0234305	8427851	4/23/2013
P110272US04	P1069US04	US	Class G+ CLCP	Granted	22-Dec-06	13/152770	13-Dec-07	2011-0235827	8660277	2/25/2014
P110277US02	P1012US02	US	4 Switch Cont. 2	Granted	31-Aug-06	13/184101	28-Aug-07	2011-0273151	8159200	4/17/2012
P113759US00	P1243US00	US	NBY PROXDE	Granted	21-Oct-10	13/271926	12-Oct-11	US-2012-0997356-A1	8831238	9/9/2014
P108149US01	P1009US01	US	DSD Silence Codes	Granted	30-Mar-07	13/298109	12-Mar-08	US-2012-0065754	8331581	12/11/2012
P116640US00	P1257US00	US	DC Cal MHI	Granted	16-Dec-10	13/327109	15-Dec-11	US-2010-0154032-A1	8624669	1/7/2014
P115855US01	P1249US01	US	CP Callidus/Thoth/Horus	Granted	23-Dec-10	13/403450	27-Dec-10	2012-0163632	8896604	11/18/2014
P109630USPC01	P1106USPC01	US	SPQ 1B ADC Cont. 01	Granted	12-Apr-06	13/416707	11-Apr-07	US-2012-0170765	8644523	2/4/2014
P109742USPC01	P1126US01	US	AS-S/V Broadr	Granted	3-Aug-07	13/490070	4-Aug-08	US-2012-0242413-A1	8514025	8/20/2013
P110297USPC01	P1130US01	US	CPIL FR&EQ	Granted	11-Dec-07	13/603222	8-Dec-08	US-2012-0326771-A1	8508288	8/13/2013

P118295US00	P1268US00	US	Class D DFF	Granted	11-Nov-11	13/673760	9-Nov-12	US-2013-0120063	8854132	10/7/2014
P118341US00	P1269US00	US	Class D Onset	Granted	18-Nov-11	13/677182	14-Nov-12	US-2013-0127531-A1	8957731	2/17/2015
P113901US02	P10735US01	US	AH 1.1 Adding inside codec	Granted	26-Nov-05	13/269726	28-Dec-05	US-2013-0237285	8843257	9/23/2014
P108299USPC01	P10964US01	US	MEMS x2 ICP Package	Granted	17-Aug-07	13/902344	15-Aug-08	US-2013/0256810-A1	8698256	4/5/2014
P119375US00	P1278US00	US	SS VCO	Granted	30-May-12	13/902638	24-May-13	US-2013-0321190-A1	8782970	6/3/2014
P109742USPC02	P1136US02	US	V2US	Granted	3-Aug-07	13/942303	4-Aug-08	US-2013-0300508-A1	8988189	3/24/2015
P122056US00	P1305US00	US	MEMS-P Vest	Granted	24-Sep-12	13/974797	23-Aug-13	US-2014/0084396-A1	8737171	5/27/2014
P122056US01	P1305US01	US	MEMS-P Plug	Granted	24-Sep-12	14/176811	23-Aug-13	US-2014-0161290	8987844	3/24/2015
P1088929USPC02	P10964US02	US	MEMS x2 ICP Chip	Granted	17-Aug-07	14/203131	15-Aug-08	US-2014-0191344-A1	8803261	8/12/2014
P115365US00	P1239US00	US	Backside HCD	Pending	10-Sep-10	13/229181	9-Sep-11	US-2012-0062190-A1		
P115852US00	P1245US00	US	CP AnubisRa	Pending	23-Dec-10	13/336835	23-Dec-11	US-2012-0170770		
P119546US00	P1279US00	US	AH Elastic Switching TDM+	Pending	27-May-11	13/481403	25-May-12	US-2012-0309960-A1		
P113302USPC	P1230USPC	US	2 Speed Interface	Pending	17-Dec-09	13/515885	17-Dec-10	US-2012-0272089		
P116082US00	P1253US00	US	PLL Sync	Pending	21-Nov-11	13/678300	15-Nov-12	US-2013/0129114-A1		
P120333US00	P1291US00	US	PLM PWN	Pending	14-Dec-11	13/715495	14-Dec-12	US-2013-0197920-A1		
P118737US00	P1274US00	US	NBY Churchill's Error Mic	Pending	21-Feb-12	13/773276	21-Feb-13	US-2013-0216060-A1		
P118686US00	P1272US00	US	NBY-WINCODE	Pending	16-Mar-12	13/796120	12-Mar-13	US-2013-0243214-A1		
P110271US02	P1071US02	US	2FB CAP D/SC	Pending	22-Dec-06	13/854707	13-Dec-07	US-2013-0314151-A1		
P109028USPC01	P1099USPC01	US	SPQ ROM Mobile	Pending	26-Jan-06	13/919743	17-Jan-07	US-2014-02003613-A1		
P107777USPC01	P1033USPC01	US	MEMS-P Mic Array	Pending	24-Feb-06	13/937067	23-Feb-07	US-2014-0016798-A1		
P109033USPC01	P1109US01	US	SPQ Leaky Earbuds	Pending	16-Feb-07	13/952035	6-Feb-08	US-2013-0308786-A1		
P113608US01	P1233USPF01	US	PMIC1 BF pulse skipping	Pending	22-Jul-09	13/955317	22-Jul-10	US-2013-0314062-A1		

P117602USPC	P1262USPC	US	NBY Churchill's Ashtry	Pending	10-Dec-10	13/992,196	9-Dec-11	US-2013-0766150-A1
P121283US00	P1300US00	US	NBY Lecture Post +	Pending	14-Sep-12	14/024436	11-Sep-13	US-2014-0513753
P120212US00	P1290US00	US	NBY AGA-RPF	Pending	12-Oct-12	14/054555	15-Oct-13	2014-0105413
P116644US01	P1252US01	US	DC Cal MRI Mixed Loops	Pending	16-Dec-10	14/142276	15-Dec-11	US2014/0112500 A1
P109030USPC02	P1106USPC02	US	SPQ IB ADC Cont. 02	Pending	12-Apr-06	14/142329	11-Apr-07	US2014/0112492 A1
P111468US01	P1195US01	US	MEMS E-Test Broaden	Pending	30-Dec-08	14/156723	30-Dec-09	US2014/0132294 A1
P116272US07	P1069US07	US	Class G+ DMCP-Y	Pending	22-Dec-06	14/174553	13-Dec-07	US-2014-0512388
P108434USPC01	P1119US01	US	1-Bit ETA-V	Pending	3-Aug-07	14/182445	4-Aug-08	
P117672USPC	P1264USPC	US	NBY Neutral Emissions MKII	Pending	15-Jul-11	14/232834	6-Jul-12	US-2014-0169584
P120892US00	P1296US00	US	Mic Clamp	Pending	11-Apr-13	14/244075	3-Apr-14	US2014-0307910-A1
P121861US00	P1299US00	US	ALON K2 Speech Path	Pending	26-Jan-13	14/314182	25-Jan-14	US2015-0039303-A1
P121869US00	P1301US00	US	OS Env Det	Pending	5-Jul-13	14/322747	2-Jul-14	US2015-0008962-A1
P120128USPC	P1284USPC	US	MEMS-P Strong Membrane	Pending	24-Sep-12	14/430438	19-Sep-13	
P120129USPC	P1285USPC	US	MEMS-P Cartwheel	Pending	24-Sep-12	14/430466	19-Sep-13	
P121995US00	P1303US00	US	MEMS-E LNA Vbs	Pending	2-Aug-13	14/448848	31-Jul-14	
P108929USPC03	P1096US03	US	MEMS x2 ICP BV=X,Y+	Pending	17-Aug-07	14/452067	15-Aug-08	US-2014-0341402-A1
P113901US04	P1075US04	US	AH14 Codec	Pending	28-Dec-05	14/481486	9-Sep-14	
P122268US00	P1307US00	US	Class D Multi-Rate	Pending	23-Oct-13	14/521191	22-Oct-14	
P115835US02	P1249US02	US	CP Calibration/Thoth/Horus	Pending	23-Dec-10	14/542086	27-Dec-10	
P109792USPC01	P1177US01	US	T1 LRE-ACOF	Pending	21-Dec-07	14/551632	12-Dec-08	
P122284US00	P1308US00	US	MEMS-E Vbg Bias	Pending	19-Dec-13	14/574714	18-Dec-14	
P109036USPC01	P1160US01	US	SPQ ASM Plug & Socket	Pending	9-May-07	14/601854	8-May-08	
P122264US00	P1306US00	US	SP Thermal & Signal	Pending	24-Jan-14	14/602685	22-Jan-15	
P109742USPC03	P1136US03	US	Dither SV	Pending	3-Aug-07	14/626468	4-Aug-08	
P122056US02	P1305US02	US	MEMS-P Plug in Aperture	Pending	24-Sep-12	14/636793	23-Aug-13	
P125386USPROV	P1328USPROV	US	MEMS-Pa WLP x2 ICP	Pending	23-Dec-14	62/096,363	23-Dec-14	

