

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT6720152

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
BRIAN GUTTAG	05/06/2021
NITIN DESHMUKH	05/06/2021
SREESAN VENKATAKRISHNAN	05/13/2021
SATISH SIVASWAMY	05/15/2021
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	XILINX, INC.
<b>Street Address:</b>	2100 LOGIC DRIVE
<b>City:</b>	SAN JOSE
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	95124
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	17316584
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(408)371-4613
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	4088796539
<b>Email:</b>	jschon@pattersonsheridan.com, psdocketing@pattersonsheridan.com
<b>Correspondent Name:</b>	XILINX, INC.
<b>Address Line 1:</b>	2100 LOGIC DRIVE
<b>Address Line 4:</b>	SAN JOSE, CALIFORNIA 95124
<b>ATTORNEY DOCKET NUMBER:</b>	X-6493
<b>NAME OF SUBMITTER:</b>	KEITH TABOADA
<b>SIGNATURE:</b>	/Keith Taboada/
<b>DATE SIGNED:</b>	05/20/2021
<b>Total Attachments: 4</b>	
source=X-6493 US Executed Assignment#page1.tif	
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**PATENT**

**REEL: 056299 FRAME: 0636**

ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, I/we

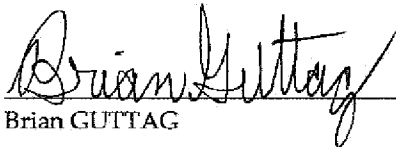
1)	Brian GUTTAG 3130 Rubino Dr. Apt 213 San Jose, California 95125	2)	Nitin DESHMUKH 13788 195th Dr SE Monroe, Washington 98272
3)	Sreesan VENKATAKRISHNAN 735 Creekland Cir San Jose, California 95133	4)	Satish SIVASWAMY 34742 Gladstone Pl Fremont, California 94555

hereby sell, assign and transfer to **Xilinx, Inc., a Delaware corporation**, having a place of business at **2100 Logic Drive, San Jose, California 95124**, its successors and assigns, the entire right, title and interest throughout the world in my/our invention

OPTIMIZING HARDWARE DESIGN THROUGHPUT BY LATENCY AWARE BALANCING OF RE-CONVERGENT PATHS

for which I/we will execute a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; I/we authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from me/us; I/we agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, its successors, assigns and nominees, at its request and expense, in obtaining and enforcing patents for said invention in all countries; and I/we request the Commissioner of Patents and Trademarks to issue all patents granted for said invention to the above-named assignee, its successors and assigns.

IN WITNESS WHEREOF, the said Assignors have executed and delivered this instrument to said Assignee on the dates indicated below.

1) May 6, 2021 (DATE)   
Brian GUTTAG

2) \_\_\_\_\_ (DATE) \_\_\_\_\_  
Nitin DESHMUKH

3) \_\_\_\_\_ (DATE) \_\_\_\_\_  
Sreesan VENKATAKRISHNAN

4) \_\_\_\_\_ (DATE) \_\_\_\_\_  
Satish SIVASWAMY

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1) \_\_\_\_\_ (DATE) \_\_\_\_\_  
Brian CUTTAG

2) 05/06/2024 (DATE) *Nitin Deshmukh*  
Nitin DESHMUKH

3) \_\_\_\_\_ (DATE) \_\_\_\_\_  
Sreesan VENKATAKRISHNAN

4) \_\_\_\_\_ (DATE) \_\_\_\_\_  
Satish SIVASWAMY

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4) \_\_\_\_\_ (DATE) \_\_\_\_\_  
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- 3) \_\_\_\_\_ (DATE) \_\_\_\_\_  
Sreesan VENKATAKRISHNAN
- 4) 05/15/2021 (DATE) Satish  
Satish SIVASWAMY