PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT6752182

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
VEXATA (ABC), LLC	06/18/2019

RECEIVING PARTY DATA

Name:	NEXSAN CORPORATION
Street Address:	1289 ANVILWOOD AVENUE
City:	SUNNYVALE
State/Country:	CALIFORNIA
Postal Code:	94089

PROPERTY NUMBERS Total: 7

Property Type	Number
Application Number:	14562110
Application Number:	15159703
Application Number:	15421122
Application Number:	15159654
Application Number:	15374365
Application Number:	15421083
Application Number:	15421111

CORRESPONDENCE DATA

Fax Number: (617)443-0004

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ATTORNEY DOCKET NUMBER:	4262/0001
NAME OF SUBMITTER:	ROBERT M. ASHER
SIGNATURE:	/ROBERT M. ASHER/
DATE SIGNED:	06/09/2021

Total Attachments: 5
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EXHIBIT 6.2 (ii)

TO ASSET PURCHASE AGREEMENT
PATENT ASSIGNMENT AGREEMENT

THIS PATENT ASSIGNMENT AGREEMENT is made as of June 28, 2019, by and between VEXATA (ABC), LLC, a California limited liability company ("Seller"), as Assignee for the Benefit of Creditors of Vexata, Inc., a Delaware corporation, and Nexsan Corporation, a Delaware corporation ("Buyer"). Seller and Buyer are parties to a certain Asset Purchase Agreement dated as of June 28, 2019 (the "Asset Purchase Agreement"). Capitalized terms used without definitions herein shall have the meanings ascribed to such terms in the Asset Purchase Agreement.

WHEREAS, Seller has agreed to sell to Buyer, and Buyer has agreed to acquire from Seller, all of Seller's rights, title and interest in all patents and patent applications owned by Seller, including those patents and patent applications identified in Schedule A attached herein ("Assigned Patents"); and

WHEREAS, the parties accordingly wish to execute this recordable instrument, assigning all of Seller's right, title and interest in and to the Assigned Patents to Buyer;

NOW, THEREFORE, for valuable consideration set forth in the Asset Purchase Agreement, the receipt and sufficiency of which are hereby acknowledged, Seller and Buyer agree as follows:

1.   Seller hereby sells, assigns, transfers, and sets over to Buyer, and its lawful successors and assigns, the Seller's entire right, title, and interest throughout the world in and to the Assigned Patents, together with all rights to the inventions described or claimed therein, and all divisions, continuations and continuations-in-part thereof, and all Letters Patent of the United States which may be granted thereon, and all reissues thereof, and all rights to claim priority therefrom, and all applications for Letters Patent which may hereafter be filed for this invention in any foreign country and all Letters Patent which may be granted on this invention in any foreign country, and all extensions, renewals, and reissues, thereof and Seller hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States and any official of any foreign country whose duty it is to issue patents on applications as described above, to issue all Letters Patent for any invention disclosed and claimed in any Assigned Patent to Buyer, its successors and assigns, in accordance with the terms of this Patent Assignment Agreement.

2.   Seller further assign to Buyer all rights to sue and recover for any past, present or future actions, causes of action and rights to recover damages or payments (including lost profits), for infringement or misappropriations of any Assigned Patent, as well as the right to take over and continue any and all existing suits related to any Assigned Patent.

3.   This Patent Assignment Agreement is subject to the terms and conditions of the Asset Purchase Agreement and this Patent Assignment Agreement shall not be deemed to limit, enlarge or extinguish any obligation of Seller or Buyer under the Asset Purchase Agreement, all of which obligations shall survive the delivery of this Patent Assignment Agreement in accordance with the terms of the Asset Purchase Agreement, and that to the extent there is any conflict between this Patent Assignment Agreement and the terms and conditions of the Asset Purchase Agreement, the Asset Purchase Agreement shall control.

4.   This Patent Assignment Agreement may be executed in counterparts, each of which shall be deemed an original, but all of which together shall constitute one and the same instrument.
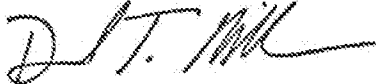
SELLER:

VEXATA (ABC), LLC, a California
limited liability company, as Assignee for
the Benefit of Creditors of Vexata, Inc.

By: _____

Name: David Miller

Title: Manager

BUYER:

NEXSAN CORPORATION, a Delaware
corporation

By: _____

Name: _____

Title: _____

SELLER:
VEXATA (ABC), LLC, a California
limited liability company, as Assignee for
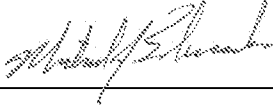the Benefit of Creditors of Vexata, Inc.

By:_____

Name: David Miller

Title: Manager

BUYER:
NEXSAN CORPORATION, a Delaware
corporation

By:_____

Name:_____

Title:_____

Exhibit 6.2 (ii)

Schedule A
To Patent Assignment Agreement
Assigned Patents Including

1   U. S. Application Serial No. 14/562,110 Filed: December 5, 2014
For: STORAGE ARCHITECTURE FOR STORAGE CLASS MEMORIES
Overall architecture patent for the Vexata architecture (US patent US-2019-0050155-A1)

2   U.S. Patent Application Serial No. 15/159,703 Filed May 19, 2016
For: INPUT OUTPUT SCHEDULING FOR SOLID STATE MEDIA
for Inventors: Surya VARANASI, Shailendra JHA
US Patent Number 10,134,473 issued Nov 20th, 2018
This patent covers scheduling random access write and read IO to a SSD

3   U.S. Patent Application No. 15/421,122 filed January 31, 2017
for HIGH PERFORMANCE AND LOW-LATENCY REPLICATION USING STORAGE MIRRORING
for Inventor: Shailendra JHA
Notice of Allowance – we are in the process of paying the $500 "issue fee" to issue the patent.
It has passed all the checks with the examiner.
This patent provides a method of synchronous replication between Vexata storage systems

4   U.S. Patent Application Serial No. 15/159,654 filed May 19, 2016,
for ENDURANCE AWARE RAID SCHEME FOR FLASH BASED SSDS WITH FPGA
     OPTIMIZED IMPLEMENTATION
Inventors: Surya VARANASI, Shailendra JHA
Notice of Allowance – we are in the process of paying the $500 'issue fee" to issue the patent.
It has passed all the checks with the examiner.
This patent provides a scheme to adjust RAID calculations over time based on life left in low endurance
(QLC) SSDs. Without this scheme, basic RAID schemes will make all drives fail at the same time.

5. U.S. Patent Application No. 15/374,365 filed December 9, 2016
Title: METHOD TO USE PCIe PEER-TO-PEER COMMUNICATION TO ELIMINATEMEMORY
     BANDWIDTH CONTENTION IN NETWORKS/STORAGE PROCESSING
Inventor(s): Shailendra JHA, Mandar JOSHI, John Michael TERRY, Prateek SHARMA,
  Venkata Pramod BALAKAVI, Vinodh RAVINDRAN

6. U.S. Patent Application No. 15/421,083 filed January 31, 2017
   for DISAGGREGATED MODEL FOR STORAGE SYSTEM
   Inventor Surya P. VARANASI
Our Reference No. 023815-0450980 (VX-060)
   The patent describes Vexata architecture on standard x86 servers

7. U.S. Patent Application No. 15/421,111 filed January 31, 2017
for UNIVERSAL DE-DUPLICATION ENGINE
Inventor Surya P. VARANASI

Our Reference No. 023815-0450979 (VX-050)

This patents describes a universal de-duplication engine that is a separate implementation from a storage array.

This engine provides a method for de-duplication across any 3rd party storage array.

Exhibit 6.2 (ii)

PATENT

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