

## PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT6804495

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
ISHWAR AGARWAL	07/06/2021
GEORGE ZACHARIAS CHRYSOS	07/09/2021
OSCAR ROSELL MARTINEZ	07/08/2021
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	MICROSOFT TECHNOLOGY LICENSING, LLC
<b>Street Address:</b>	ONE MICROSOFT WAY
<b>City:</b>	REDMOND
<b>State/Country:</b>	WASHINGTON
<b>Postal Code:</b>	98052
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
Application Number:	17371422
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(425)401-5199
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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<b>Email:</b>	jim@liangip.com
<b>Correspondent Name:</b>	CHEN LIANG
<b>Address Line 1:</b>	1750 112TH AVE NE, SUITE D-152
<b>Address Line 4:</b>	BELLEVUE, WASHINGTON 98004
<b>ATTORNEY DOCKET NUMBER:</b>	410121-US-NP
<b>NAME OF SUBMITTER:</b>	CHEN LIANG
<b>SIGNATURE:</b>	/Chen Liang/
<b>DATE SIGNED:</b>	07/09/2021
<b>Total Attachments: 6</b>	
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PATENT ASSIGNMENT

MS Docket No.: 4010121-US-NP

Ishwar Agarwal ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "MEMORY TIERING TECHNIQUES IN COMPUTING SYSTEMS" ("APPLICATION"), which:

[X] will be filed. ASSIGNOR hereby authorizes, and requests, ASSIGNEE'S legal representative, Microsoft Technology Licensing, LLC., One Microsoft Way, Redmond, Washington 98052 (ASSIGNEE'S LEGAL REPRESENTATIVES), to insert here in parentheses (filed on July 9, 2021 and given Application No. 17371422 by the following Office US - United States Patent and Trademark Office) this APPLICATION's Application No., filing date, and Office, when known:

[ ] was filed on \_\_\_\_\_ and was given Application No \_\_\_\_\_ by the following Office " \_\_\_\_\_":

Microsoft Technology Licensing, LLC, a Washington limited liability company, on behalf of itself and its successors and assigns ("ASSIGNEE"), is entitled to, and is desirous of acquiring, the entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES")):

ASSIGNOR hereby authorizes, and requests, ASSIGNEE'S LEGAL REPRESENTATIVES to insert the Application No., filing date, and Office into the following table for any APPLICATION DERIVATIVES after they are known;

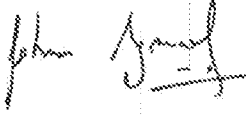
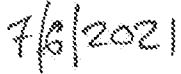
Table with 3 columns: Application No., Filing Date, Office. The table is currently empty.

PATENT ASSIGNMENT

MS Docket No.: 4010121-US-NP

For good and valuable consideration, the receipt of which is hereby acknowledged by the ASSIGNOR, the ASSIGNOR hereby sells, assigns and transfers to the ASSIGNEE, ASSIGNOR's entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

ASSIGNOR agrees to execute all instruments and documents required for the making and prosecution of the APPLICATION (and APPLICATION DERIVATIVES), for litigation regarding letters patent derived therefrom, and for the purpose of protecting and perfecting title to the APPLICATION (and APPLICATION DERIVATIVES).

	
Inventor's Signature	Date
Ishwar Agarwal	
Printed Name in English	Printed Name in Native Language (if other than English)

PATENT ASSIGNMENT

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I, George Zacharias Chrysos ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "MEMORY TIERING TECHNIQUES IN COMPUTING SYSTEMS" ("APPLICATION"), which:

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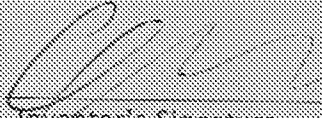
Application No.	Filing Date	Office

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 Inventor's Signature	7/9/21 Date
George Zacharias Chrysos Printed Name in English	Printed Name in Native Language (if other than English)

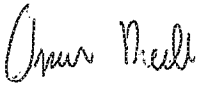


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	8th July, 2021
_____ Inventor's Signature	_____ Date
Oscar Rosell Martinez	Oscar Rosell Martinez
_____ Printed Name in English	_____ Printed Name in Native Language (if other than English)