

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT6833390

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST
CONVEYING PARTY DATA	
Name	Execution Date
JPMORGAN CHASE BANK, N.A., AS ADMINISTRATIVE AGENT	07/27/2021
RECEIVING PARTY DATA	
Name:	MICROCHIP TECHNOLOGY INCORPORATED
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State/Country:	ARIZONA
Postal Code:	85224
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State/Country:	ARIZONA
Postal Code:	85224
PROPERTY NUMBERS Total: 9	

Property Type	Number
Patent Number:	9778289
Patent Number:	9830245
Patent Number:	8169450
Patent Number:	8314572
Patent Number:	9814109
Patent Number:	9622307
Patent Number:	8217887
Patent Number:	8339174
Patent Number:	8604856

CORRESPONDENCE DATA

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NAME OF SUBMITTER:	QUI LU FLOOD
SIGNATURE:	/Qui Lu Flood/
DATE SIGNED:	07/27/2021

Total Attachments: 3

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RELEASE OF SECURITY INTEREST IN CERTAIN PATENT RIGHTS

This RELEASE OF SECURITY INTEREST IN CERTAIN PATENT RIGHTS (this “**Partial Release**”), dated as of July 27, 2021, is executed by JPMorgan Chase Bank, N.A., a national banking association, as Administrative Agent (in such capacity, the “**Administrative Agent**”), in favor of Microchip Technology Incorporated, a Delaware corporation (“**Microchip**”), Silicon Storage Technology, Inc., a California corporation (“**SST**”), Atmel Corporation, a Delaware corporation (“**Atmel**”), Microsemi Corporation, a Delaware corporation (“**Microsemi**”), and Microsemi Storage Solutions, Inc., a Delaware corporation (“**Storage**”, and together with Microchip, SST, Atmel, Microsemi, the “**Grantors**”). All capitalized terms used in this Partial Release and not otherwise defined herein, shall have the respective meanings given to such terms in the Patent Security Agreement (defined below).

RECITALS

A. Pursuant to that certain (i) Amended and Restated Pledge and Security Agreement, dated as of May 29, 2018, by and among Microchip, the subsidiaries of Microchip party thereto, and the Administrative Agent, and (ii) Grant of Security Interest in Patent Rights, dated as of May 29, 2018 (the “**Patent Security Agreement**”), executed by the Grantors in favor of Administrative Agent, the Grantors granted to Administrative Agent a security interest in the Patent Collateral (defined below).

B. The Patent Security Agreement was recorded with the Patent Division of the United States Patent and Trademark Office on June 25, 2018, at Reel/Frame 046426/0001 to evidence the security interest granted under the Patent Security Agreement.

C. Administrative Agent agrees to execute this Partial Release in order to evidence the release of its security interest solely in the Patent Collateral owned by the Grantors specified below.

AGREEMENT

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, Administrative Agent hereby agrees as follows:

(a) Administrative Agent expressly releases all of Administrative Agent’s right, title and interest in, to and under the Patents referred to on Schedule A hereto (collectively, the “**Patent Collateral**”).

(b) Administrative Agent represents and warrants that it has the full power and authority to execute this Partial Release.

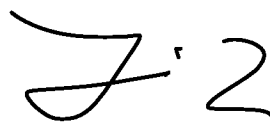
(c) Administrative Agent authorizes and requests the Patent Division of the United States Patent and Trademark Office to record this Partial Release.

(d) This Partial Release applies solely to the Patent Collateral as described above owned by the Grantors. The Patent Security Agreement shall continue in full force and effect as between Administrative Agent and the Grantors and against all remaining Patents referenced therein.

[Signature Page Follows]

IN WITNESS WHEREOF, Administrative Agent has executed and delivered this Partial Release as of the day and year first above written.

JPMORGAN CHASE BANK, N.A.,
as Administrative Agent

By: 

Name:
Title:

SCHEDULE A**United States
Patents & Patent Applications**

Patent / Publication Number	Application Number	Title	Filed Date
<u>9,778,289</u>	14/883,381	MEASURING OUTPUT CURRENT IN A BUCK SMPS	10/14/2015
<u>9,830,245</u>	13/929,605	TRACING EVENTS IN AN AUTONOMOUS EVENT SYSTEM	6/27/2013
<u>8,169,450</u>	11/805,522	SYSTEM AND METHOD FOR AMBIENT-LIGHT ADAPTIVE INTENSITY CONTROL FOR AN ELECTRONIC DISPLAY	5/22/2007
<u>8,314,572</u>	12/409,088	APPARATUS AND METHODOLOGY FOR ENHANCING EFFICIENCY OF A POWER DISTRIBUTION SYSTEM HAVING POWER FACTOR CORRECTION CAPABILITY BY USING A SELF-CALIBRATING CONTROLLER	3/23/2009
<u>9,814,109</u>	11/942,239	Apparatus and Technique for Modular Electronic Display Control	11/19/2007
<u>9,622,307</u>	13/426,301	APPARATUS AND TECHNIQUE FOR MODULAR ELECTRONIC DISPLAY CONTROL	3/21/2012
<u>8,217,887</u>	12/018,399	SYSTEM AND METHOD FOR BACKLIGHT CONTROL FOR AN ELECTRONIC DISPLAY	1/23/2008
<u>8,339,174</u>	12/712,930	APPARATUS, CIRCUIT AND METHOD FOR AUTOMATIC PHASE-SHIFTING PULSE WIDTH MODULATED SIGNAL GENERATION	2/25/2010
<u>8,604,856</u>	13/724,649	APPARATUS, CIRCUIT AND METHOD FOR AUTOMATIC PHASE-SHIFTING PULSE WIDTH MODULATED SIGNAL GENERATION	12/21/2012