

PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT6855484

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
MARVELL SEMICONDUCTOR, INC.	10/15/2020
RECEIVING PARTY DATA	
Name:	MARVELL ASIA PTE, LTD. (REGISTRATION NO. 199702379M)
Street Address:	NO. 8 TAI SENG LINK
City:	SINGAPORE
State/Country:	SINGAPORE
Postal Code:	534158
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17071910
CORRESPONDENCE DATA	
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<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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Correspondent Name:	KELVIN VIVIAN
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ATTORNEY DOCKET NUMBER:	MP13155
NAME OF SUBMITTER:	KELVIN VIVIAN
SIGNATURE:	/Kelvin Vivian/
DATE SIGNED:	08/09/2021
Total Attachments: 2	
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ASSIGNMENT
Corporate

WHEREAS Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, California 95054 (hereafter the "Assignor"), is the owner of the patents, including utility models and other similar registrations, or patent applications identified below (hereafter referred to as the "Patent Application(s)"); and

Marvell Asia Pte, Ltd., a corporation of Singapore, having a place of business at Tai Seng Centre, 3 Irving Road #10-01, Singapore 369522 (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application(s).

THEREFORE, in consideration of the sum of One Dollar (\$1.00) US or equivalent and other good and valuable consideration paid to it, the receipt and sufficiency of which hereby is acknowledged, Assignor hereby sells, assigns and transfers to the Assignee the entire right, title, and interest, together with all priority rights, in and to the Patent Application(s), and all inventions described and claimed therein, in any and all patents including utility models and other similar registrations granted from the Patent Application(s) and from patents or patent applications claiming priority directly or indirectly from the Patent Application(s), including but not limited to any and all reissues, renewals, reexaminations, divisions, and to the full end of the term or terms for which such patents issue in the United States, and/or any foreign countries, such entire right, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this assignment not been made, including the right to sue and recover damages for past, present and future infringement.

<u>Marvell Reference No.</u>	<u>Application No.</u>	<u>Filing Date</u>	<u>Title</u>
MP13155	Not Yet Assigned	Not Yet Assigned	SYSTEMS AND METHODS FOR CONFIGURING A CENTRAL PROCESSING UNIT HAVING MULTIPLE CORES
MP13155PR	62/916,105	October 16, 2019	ULTRA-CORE CPU FOR HIGH PERFORMANCE CHIPS

Assignor hereby covenants and agrees to execute and deliver, at the request of Assignee, such further instruments of transfer and assignment and to take any other action as such Assignee may

reasonably request to more effectively consummate the assignment contemplated by this agreement. Specifically, Assignor agrees to, at Assignee's expense, execute, acknowledge and deliver such further documents, instruments, conveyances and assurances and take such further actions as may be reasonably required to register in the name of Assignee the assignment of the patents in any appropriate governmental agency or registrar.

The undersigned representatives of the Assignor and the Assignee authorize and request that the attorneys of record in any of the Patent Application(s) insert in this assignment the filing date(s) and application number(s) of the Patent Application(s) when officially known.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the name of the Assignor's undersigned representative, which date shall be the effective date of this assignment.

MARVELL SEMICONDUCTOR, INC.

By: Kelvin Vivian Digitally signed by Kelvin Vivian
DN: cn=Kelvin Vivian, o=Marvell, ou=MG,
email=kelvin@marvell.com, c=US
Date: 2020.10.15 15:12:00 -0700

Name: Kelvin Vivian

Title: Director, Assistant General Counsel, IP Development

Date: 15 October 2020

MARVELL ASIA PTE, LTD.

By: 

Name: Chen Seok Ching

Title: Vice President, Assembly & Test Operations
and Country Manager

Date: 16 October 2020