

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT6858990

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT	
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT	
<b>CONVEYING PARTY DATA</b>		
	<b>Name</b>	<b>Execution Date</b>
	GIDEON INTRATER	08/10/2021
<b>RECEIVING PARTY DATA</b>		
<b>Name:</b>	DIALOG SEMICONDUCTOR US INC.	
<b>Street Address:</b>	3600 PETERSON WAY	
<b>City:</b>	SANTA CLARA	
<b>State/Country:</b>	CALIFORNIA	
<b>Postal Code:</b>	95054	
<b>PROPERTY NUMBERS Total: 1</b>		
	<b>Property Type</b>	<b>Number</b>
	Application Number:	17399263
<b>CORRESPONDENCE DATA</b>		
<b>Fax Number:</b>		
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>		
<b>Phone:</b>	(408)421-5617	
<b>Email:</b>	mike@stephenspatents.com	
<b>Correspondent Name:</b>	MICHAEL C. STEPHENS, JR.	
<b>Address Line 1:</b>	22779 MONTROSE COURT	
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<b>ATTORNEY DOCKET NUMBER:</b>	I-21014	
<b>NAME OF SUBMITTER:</b>	MICHAEL C. STEPHENS, JR.	
<b>SIGNATURE:</b>	/Michael C. Stephens, Jr./	
<b>DATE SIGNED:</b>	08/11/2021	
<b>Total Attachments: 2</b>		
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**ASSIGNMENT**

For good and valuable consideration, the receipt of which is hereby acknowledged, the person(s) named below (referred herein as "INVENTOR" whether singular or plural) has sold, assigned, and transferred and does hereby sell, assign, and transfer to **DIALOG SEMICONDUCTOR US INC.**, having a place of business at 3600 PETERSON WAY, SANTA CLARA, CALIFORNIA, 95054 ("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

1. The entire worldwide right, title, and interest in all inventions and improvements ("SUBJECT MATTER") that are disclosed in the provisional application filed under 35 U.S.C. §111(b) or non-provisional application filed under 35 U.S.C. §111(a) and entitled **LATENCY REDUCTION IN SPI FLASH MEMORY DEVICES** ("APPLICATION"), which:

☐ is to be filed herewith

☒ was filed on 8/11/91, now bearing U.S. serial number 12/399,263; and

2. The entire worldwide right, title, and interest in and to:  
(a) the APPLICATION; (b) all applications claiming priority from the APPLICATION; (c) all divisional, continuation, substitute, renewal, reissue, and other related applications thereto which have been or may be filed in the United States or elsewhere in the world; (d) all patents (including reissues and re-examinations) which may be granted on the applications set forth in (a), (b), (c) and (d) above; and (e) all right of priority in the APPLICATION, together with all rights to recover damages for infringement of provisional rights.

INVENTOR agrees that ASSIGNEE may apply for and receive patents for SUBJECT MATTER in ASSIGNEE's own name.

INVENTOR agrees to do the following, when requested, and without further consideration, in order to carry out the intent of this Assignment: (1) execute all assignments, oaths, powers of attorney, applications, and other papers (and/or documents) necessary or desirable to fully secure to ASSIGNEE the rights, titles and interests herein conveyed; (2) communicate to ASSIGNEE all known facts relating to the SUBJECT MATTER; and (3) generally do all lawful acts that ASSIGNEE shall consider desirable for securing, maintaining, and enforcing worldwide patent protection relating to the SUBJECT MATTER and for vesting in ASSIGNEE the rights, titles, and interests herein conveyed. INVENTOR further agrees to provide any successor, assign, or legal representative of ASSIGNEE with the benefits and assistance provided to ASSIGNEE hereunder.

INVENTOR represents that INVENTOR has the rights, titles, and interests to convey as set forth herein, and covenants with ASSIGNEE that the INVENTOR has not made and will not hereafter make any assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

INVENTOR grants the attorney(s) of record the power to insert on this Assignment any further identification that may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

Title of Document: **ASSIGNMENT**

Re:

Title: LATENCY REDUCTION IN SPI FLASH MEMORY DEVICES

Filed: (if applicable) 08/11/2021

Atty. Docket No.: I-21014

Serial No.: (if applicable) 17/399,263

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This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

Name and SignatureDate of Signature  
Gideon Intrater

8/10/2021