PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT6895216

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
X DEVELOPMENT LLC	08/26/2021

RECEIVING PARTY DATA

Name:	GOOGLE LLC	
Street Address:	1600 AMPHITHEATRE PARKWAY	
City:	MOUNTAIN VIEW	
State/Country:	CALIFORNIA	
Postal Code:	94043	

PROPERTY NUMBERS Total: 19

Application Number: 15811191 Application Number: 16530866 Application Number: 16826020 Application Number: 17141044 Application Number: 62423350 Application Number: 15810496 Application Number: 16784875 Application Number: 62423535 Application Number: 15811042 Application Number: 16725811 Application Number: 62440613 Application Number: 16042839 Application Number: 16827409 Application Number: 17397465 Application Number: 62535652 Application Number: 16042752 Application Number: 16042752 Application Number: 17135607 Application Number: 62535612	Property Type	Number		
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Application Number: 17397465 Application Number: 62535652 Application Number: 16042752 Application Number: 17135607	Application Number:	16042839		
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Application Number: 17135607	Application Number:	62535652		
	Application Number:	16042752		
Application Number:62535612	Application Number:	17135607		
	Application Number:	62535612		

PATENT REEL: 057353 FRAME: 0295

506848395

CORRESPONDENCE DATA

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using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

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ATTORNEY DOCKET NUMBER:	16113-8900001		
NAME OF SUBMITTER:	LISA SWANSON		
SIGNATURE:	/Lisa Swanson/		
DATE SIGNED:	09/01/2021		

Total Attachments: 3

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PATENT REEL: 057353 FRAME: 0296

PATENT ASSIGNMENT

This PATENT ASSIGNMENT (this "Assignment") is made and entered into as of August 26, 2021, (the "Effective Date"), by and between X Development LLC, a Delaware Limited Liability Company ("Assignor"), and Google LLC, a Delaware Limited Liability Company ("Assignee").

WHEREAS, Assignor owns certain right, title, and interest in and to certain Assigned Patents (as defined below);

NOW, THEREFORE, for good and valuable consideration paid by Assignee to Assignor, the receipt and sufficiency of which are hereby acknowledged, Assignor and Assignee agree as follows:

As used herein, the "Assigned Patents" means (a) the patents and patent applications listed on Schedule 1 attached hereto ("Listed Patents"), and (b) all reissues, reexaminations, continuations, continuations-in-part, and foreign counterparts filed by Assignor prior to the Effective Date that claim priority to the Listed Patents. As used above, "priority" means a formal claim of priority to the filing date of an earlier patent or patent application that is expressly made (i) in a U.S. patent under 35 U.S.C. § 119 or 35 U.S.C. § 120; or (ii) in a foreign patent under the corresponding legal requirements for claiming priority in the foreign country; in each case, where the claimed subject matter is entitled to priority to the earlier patent or patent application.

Assignor hereby assigns and transfers to Assignee all of Assignee's right, title, and interest in and to the Assigned Patents, including its rights under the Assigned Patents to sue for injunctive relief and damages for past, present, and future infringement of the Assigned Patents. Assignee acknowledges and agrees that the Assigned Patents remain subject to a certain non-exclusive license to Assignor and its Affiliates and certain other rights and licenses that, prior to the date hereof, have been granted or are required to be granted under the Assigned Patents, which will remain in effect notwithstanding assignment of such Assigned Patents. This Assignment does not assign or transfer any right, title or interest in or to any patents or patent applications other than with respect to the Assigned Patents.

This Assignment will be binding upon and will inure to the benefit of the parties and their respective successors and assigns.

This Assignment will be governed by, and construed in accordance with, the laws of the United States in respect to patent issues and in all other respects by the laws of the State of California, without reference to its choice of law principles to the contrary.

This Assignment may be executed in counterparts, each of which will be deemed an original, and all of which together constitute one and the same instrument.

IN WITNESS WHEREOF, Assignor and Assignee have caused this Assignment to be executed by their respective duly authorized representatives as of the date first set forth above.

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X Development LLC (Assignor)

By:

Name: Jeffery Scott Heileson

Title: Head of Intellectual Property, X

Google LLC (Assignee)

By: Michael Lu

Michael Lee

Name: _____

Director, Head of Patents Title: ____

RECORDED: 09/01/2021

SCHEDULE 1

LISTED PATENTS

X Ref.	Google Ref.	Application No.	Patent No.	Title	Status
X-50142-00-PR	GP-400006-00-PR	62/423,350		BRIDGED INTEGRATED CIRCUITS	EXPIRED
X-50142-00-US	GP-400006-00-US	15/811,191	10,374,608	BRIDGED INTEGRATED CIRCUITS	ISSUED
X-50142-00-US-CO N	GP-400006-00-US- CON-01	16/530,866	10,637,478	BRIDGED INTEGRATED CIRCUITS	ISSUED
	GP-400006-00-US- CON-02	16/826,020	10,886,923	BRIDGED INTEGRATED CIRCUITS	ISSUED
	GP-400006-00-US- CON-03	17/141,044		BRIDGED INTEGRATED CIRCUITS	PENDING
X-50170-00-PR	GP-400007-00-PR	62/440,357		ALLOCATING RESOURCES FOR A MACHINE LEARNING MODEL	EXPIRED
X-50170-00-US	GP-400007-00-US	15/859,077	10,685,295	ALLOCATING RESOURCES FOR A MACHINE LEARNING MODEL	ISSUED
	GP-400007-00-US- CON-01	16/827,376		ALLOCATING RESOURCES FOR A MACHINE LEARNING MODEL	ALLOWED
	GP-400007-00-US- CON-02	16/869,195		ALLOCATING RESOURCES FOR A MACHINE LEARNING MODEL	PENDING
	GP-400007-00-US- CON-03			ALLOCATING RESOURCES FOR A MACHINE LEARNING MODEL	PROPOSED
X-50172-00-PR	GP-400008-00-PR	62/423,535		FIELD PROGRAMMABLE GATE ARRAY INCLUDING COUPLED LOOKUP TABLES	EXPIRED
X-50172-00-US	GP-400008-00-US	15/810,496	10,320,390	FIELD PROGRAMMABLE GATE ARRAY INCLUDING COUPLED LOOKUP TABLES	ISSUED
X-50172-00-US-CO N	GP-400008-00-US- CON-01	16/435,842	10,560,102	FIELD PROGRAMMABLE GATE ARRAY INCLUDING COUPLED LOOKUP TABLES	ISSUED
	GP-400008-00-US- CON-02	16/784,875	10,868,539	FIELD PROGRAMMABLE GATE ARRAY INCLUDING COUPLED LOOKUP TABLES	ISSUED
X-50232-00-PR	GP-400010-00-PR	62/440,613		DYNAMIC PARTITIONING	EXPIRED
X-50232-00-US	GP-400010-00-US	15/811,042	10,521,488	DYNAMIC PARTITIONING	ISSUED
	GP-400010-00-US- CON-01	16/725,811		DYNAMIC PARTITIONING	PENDING
	GP-400010-00-US- CON-02			DYNAMIC PARTITIONING	PROPOSED
X-50504-00-PR	GP-400013-00-PR	62/535,652		APPLICATION SPECIFIC INTEGRATED CIRCUIT ACCELERATORS	EXPIRED
X-50504-00-US	GP-400013-00-US	16/042,839	10,790,828	APPLICATION SPECIFIC INTEGRATED CIRCUIT ACCELERATORS	ISSUED
	GP-400013-00-US- CON-01	16/827,409	11,088,694	APPLICATION SPECIFIC INTEGRATED CIRCUIT ACCELERATORS	ISSUED
	GP-400013-00-US- CON-02	17/397,465		APPLICATION SPECIFIC INTEGRATED CIRCUIT ACCELERATORS	PENDING
X-50505-00-PR	GP-400014-00-PR	62/535,612		APPLICATION SPECIFIC INTEGRATED CIRCUIT ACCELERATORS	EXPIRED
X-50505-00-US	GP-400014-00-US	16/042,752	10,879,904	APPLICATION SPECIFIC INTEGRATED CIRCUIT ACCELERATORS	ISSUED
	GP-400014-00-US- CON-01	17/135,607		APPLICATION SPECIFIC INTEGRATED CIRCUIT ACCELERATORS	PENDING

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PATENT REEL: 057353 FRAME: 0299