

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
 Stylesheet Version v1.2

EPAS ID: PAT6909007

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
JEAN PHILIPPE LOISON	08/11/2021
BENOIT DE LESCURE	08/10/2021
MOHAMMED KHALEELUDDIN	08/13/2021
PARIMAL GAIKWAD	08/11/2021
ROHIT BANSAL	08/10/2021
ALEXIS BOUTILLER	08/10/2021
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	ARTERIS, INC.
<b>Street Address:</b>	595 MILLICH DR.
<b>Internal Address:</b>	SUITE 200
<b>City:</b>	CAMPBELL
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	95008
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	16686094
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	650-387-0003
<b>Email:</b>	docket@danalegalservices.com
<b>Correspondent Name:</b>	JUBIN DANA
<b>Address Line 1:</b>	325 SHARON PARK DR
<b>Address Line 2:</b>	STE 217
<b>Address Line 4:</b>	MENLO PARK, CALIFORNIA 94025
<b>ATTORNEY DOCKET NUMBER:</b>	ART-060US1
<b>NAME OF SUBMITTER:</b>	JUBIN DANA
<b>SIGNATURE:</b>	/Jubin Dana/
<b>DATE SIGNED:</b>	09/10/2021

PATENT

**Total Attachments: 9**

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source=2021-08-09 ART-060US1\_Updated\_Assignment\_AB\_Executed\_AsFiled#page1.tif  
source=2021-08-09 ART-060US1\_Updated\_Assignment\_AB\_Executed\_AsFiled#page2.tif  
source=2021-08-09 ART-060US1\_Updated\_Assignment\_JP\_Benoit\_Executed\_AsFiled#page1.tif  
source=2021-08-09 ART-060US1\_Updated\_Assignment\_JP\_Benoit\_Executed\_AsFiled#page2.tif  
source=2021-08-09 ART-060US1\_Updated\_Assignment\_PG\_Executed\_AsFiled#page1.tif  
source=2021-08-09 ART-060US1\_Updated\_Assignment\_PG\_Executed\_AsFiled#page2.tif  
source=2021-08-09 ART-060US1\_Updated\_Assignment\_RB\_Executed\_AsFiled#page1.tif  
source=2021-08-09 ART-060US1\_Updated\_Assignment\_RB\_Executed\_AsFiled#page2.tif

**ASSIGNMENT OF PATENT APPLICATION and RELATED RIGHTS**

WHEREAS: **Mohammed Khaleeluddin, a resident of** Union City, CA ;

(hereinafter referred to, individually and collectively, as "the ASSIGNOR") hereby declare and acknowledge that:

- (1) any willful false statement made herein is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both;
- (2) the application identified below was made or was authorized to be made by the ASSIGNOR as the affiant or declarant; and
- (3) the ASSIGNOR believes himself or herself to be the original inventor or an original joint inventor of a claimed invention described as set forth in the below-identified application for United States Letters Patent:

**TITLE: SYSTEM AND METHOD FOR LOGIC FUNCTIONAL REDUNDANCY**

**FILING DATE: November 15, 2019**

**ATTORNEY DOCKET No.: ART-060US1**

**US APPLICATION SERIAL No.: 16/686,094**

and, whereas ARTERIS, INC., a corporation located at 595 Millich Drive, Suite 200, Campbell, CA 95008, United States (hereinafter referred to as "the ASSIGNEE") is desirous of acquiring the entire interest in, to, and under said invention and application and in, to, and under any Letters Patent or similar legal protection to be obtained therefor in the United States and in any and all foreign countries and Registrations that may be granted on the same.

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN:

Be it known that in consideration of the valuable consideration by ASSIGNEE to ASSIGNOR, receipt of which is hereby acknowledged by ASSIGNORS, ASSIGNOR hereby sells, assigns and transfers to ASSIGNEE the full and exclusive right, title, and interest in and to said invention and application and to all foreign counterparts (including patent, utility model, and industrial design), and in and to any Letters Patent and Registrations that may hereafter be granted on the same in the United States and its territorial possessions and in any and all countries throughout the world, and to claim the priority from the application as provided by the Paris Convention. The right, title, and interest is to be held and enjoyed by ASSIGNEE and ASSIGNEE'S successors and assigns fully and exclusively for the full term of any Letters Patent and Registrations that may be granted thereon, or of any division, renewal, continuation in whole or in part, substitution, conversion, reissue, prolongation, or extension thereof.

ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into that would conflict with this Assignment.

ASSIGNOR further agrees that her or she will, without charge to ASSIGNEE, but at ASSIGNEE'S expense, (a) cooperate with ASSIGNEE in the prosecution of US Patent application and foreign counterparts on the invention and any improvements, (b) execute, verify, acknowledge and deliver all such further papers, including patent application and instruments of transfer, and (c) perform such other acts as ASSIGNEE lawfully may request to obtain or maintain Letters Patent and Registrations for the invention identified above and improvements in any and all countries, and to vest title thereto in ASSIGNEE, or ASSIGNEE'S successors and assigns.

IN TESTIMONY WHEREOF, ASSIGNOR has signed his or her name on the date indicated.

13-Aug-21

Date

DocuSigned by:  
*Mohammed Khaleeluddin*  
678A2E148E454AA

Mohammed Khaleeluddin

## ASSIGNMENT OF PATENT APPLICATION and RELATED RIGHTS

WHEREAS: **Jean Philippe LOISON, a resident of San Jose, CA;**  
**Benoit de LESCURE, a resident of Campbell, CA;**  
**Alexis BOUTILLER, a resident of Campbell, CA;**  
**Rohit BANSAL, a resident of San Jose, CA;**  
**Parimal GAIKWAD, a resident of Sunnyvale, CA;**  
**Muhammad KAHLEELUDIN, a resident of Santa Clara, CA;**

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IN TESTIMONY WHEREOF, ASSIGNOR has signed his or her name on the date indicated.

**PATENT**  
**REEL: 057439 FRAME: 0822**

\_\_\_\_\_  
Date

\_\_\_\_\_  
Jean Philippe LOISON

\_\_\_\_\_  
Date

\_\_\_\_\_  
Benoit de LESCURE

10-Aug-21  
\_\_\_\_\_  
Date

DocuSigned by:  
  
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3521E8CA82824DD...  
Alexis BOUTILLER

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Date

\_\_\_\_\_  
Rohit BANSAL

\_\_\_\_\_  
Date

\_\_\_\_\_  
Parimal GAIKWAD

\_\_\_\_\_  
Date

\_\_\_\_\_  
Muhammad KAHLEELUDIN

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IN TESTIMONY WHEREOF, ASSIGNOR has signed his or her name on the date indicated.

**PATENT**  
**REEL: 057439 FRAME: 0824**

10-Aug-21  
Date

DocuSigned by:  
*Jean Philippe Loison*  
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Jean Philippe LOISON

10-août-21  
Date

DocuSigned by:  
*Benoit de Lescure*  
977A0DAE32DB4DA...  
Benoit de LESCURE

Date

Alexis BOUTILLER

Date

Rohit BANSAL

Date

Parimal GAIKWAD

Date

Muhammad KAHLEELUDIN

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**Parimal GAIKWAD, a resident of Sunnyvale, CA;**  
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**TITLE: SYSTEM AND METHOD FOR LOGIC FUNCTIONAL REDUNDANCY**

**FILING DATE: November 15, 2019**

**ATTORNEY DOCKET No.: ART-060US1**

**US APPLICATION SERIAL No.: 16/686,094**

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**PATENT**  
**REEL: 057439 FRAME: 0826**



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Date

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Jean Philippe LOISON

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Date

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Benoit de LESCURE

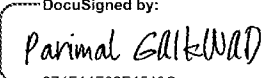
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Alexis BOUTILLER

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Date

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Rohit BANSAL

10-Aug-21  
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Date

DocuSigned by:  
  
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Parimal GAIKWAD

\_\_\_\_\_  
Date

\_\_\_\_\_  
Muhammad KAHLEELUDIN

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**PATENT**  
**REEL: 057439 FRAME: 0828**

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Date

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Jean Philippe LOISON

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Date

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Benoit de LESCURE

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Date

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Alexis BOUTILLER

10-Aug-21

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Date

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Rohit BANSAL

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Parimal GAIKWAD

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Date

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Muhammad KAHLEELUDIN