PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2

ersion v1.1 EPAS ID: PAT6942635

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
DAN TROCK	12/02/2020
ALON POSTAVSKI	12/03/2020
ETAI WAGNER	12/03/2020
VICTOR DAVID ROMANOV	09/29/2021

RECEIVING PARTY DATA

Name:	AMAZON TECHNOLOGIES, INC.	
Street Address:	PO BOX 81226	
City:	SEATTLE	
State/Country:	WASHINGTON	
Postal Code:	98108-1226	

PROPERTY NUMBERS Total: 1

Property Type	Number	
Application Number:	17247217	

CORRESPONDENCE DATA

Fax Number: (510)663-0920

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

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Address Line 1: P.O. BOX 70250

Address Line 4: OAKLAND, CALIFORNIA 94612-0250

ATTORNEY DOCKET NUMBER:	AMZ2P123/P70994-US01
NAME OF SUBMITTER:	MELANIE HITCHCOCK
SIGNATURE:	/ Melanie Hitchcock /
DATE SIGNED: 09/29/2021	
	This document serves as an Oath/Declaration (37 CFR 1.63).

Total Attachments: 11

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PATENT REEL: 057639 FRAME: 0863

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U.S. Patent Appln. No. <u>17/247,217</u> Filing Date <u>03 Dec 2020</u> Attorney Docket No. AMZ2P123 Client Docket No. P70994-US01

PARTIES TO THE ASSIGNMENT

Assignor(s):

Dan Trock 410 Terry Avenue North Seattle, WA 98109-5210

Alon Postavski 410 Terry Avenue North Seattle, WA 98109-5210

Etai Wagner 410 Terry Avenue North Seattle, WA 98109-5210

Victor David Romanov 410 Terry Avenue North Seattle, WA 98109-5210

Assignee:

Amazon Technologies, Inc. PO Box 81226 Seattle, WA 98108-1226

AGREEMENT

WHEREAS, ASSIGNOR(S) (listed above) are inventor(s) of an invention entitled

"INPUT/OUTPUT VOLTAGE TESTING WITH BOUNDARY SCAN BYPASS" (Invention) for which a

non-provisional application for United States Letters Patent (Application):

was filed on ______ and accorded U.S. Application No. ______; or

will be filed concurrently with the submission of this executed PATENT ASSIGNMENT

for recordation. ASSIGNOR(S) hereby authorizes and requests ASSIGNEE'S legal

representatives, of Weaver Austin Villeneuve & Sampson LLP, associated with

Customer No. 136609, to insert in the header above and here in parentheses (U.S.

U.S. Patent Appln. No. 17/247,217

Filing Date 03 Dec 2020

Attorney Docket No. AMZ2P123

Client Docket No. P70994-US01

Title of Invention: INPUT/OUTPUT VOLTAGE TESTING WITH BOUNDARY SCAN BYPASS

Inventors: Dan Trock, Alon Postavski, Etai Wagner, Victor David Romanov

Patent Application No. 17/247,217, filed 03 Dec 2020) this application's U.S.

application number and filing date, when known.

WHEREAS, ASSIGNEE, a corporation of the State of Nevada, is desirous of acquiring the

entire right, title and interest in and to the Invention and in and to any letters patent that may

be granted therefor in the United States and in any and all foreign countries.

NOW, THEREFORE, in exchange for good and valuable consideration, the receipt of

which is hereby acknowledged, ASSIGNOR(S) hereby sell, assign and transfer unto ASSIGNEE its

successors and assigns, the entire right, title and interest in and to said Invention and any

improvements thereto, said Application and any and all letters patent which may be granted for

said Invention in the United States of America and its territorial possessions and in any and all

foreign countries, and in any and all provisional, divisions, reissues, re-examinations and

continuations thereof, including the right to file foreign applications directly in the name of

ASSIGNEE and to claim priority rights deriving from said Application to which said foreign

applications are entitled by virtue of international convention, treaty or otherwise, said

Invention, Application and all letters patent on said Invention to be held and enjoyed by

ASSIGNEE and its successors and assigns for their use and benefit and of their successors and

assigns as fully and entirely as the same would have been held and enjoyed by ASSIGNOR(S) had

this assignment, transfer and sale not been made. ASSIGNOR(S) hereby authorize and request

the Commissioner of Patents and Trademarks, as well as all other applicable patent offices

throughout the world, to issue all letters patent on said Invention to ASSIGNEE. ASSIGNOR(S)

agree to execute all instruments and documents required for the making and prosecution of

Page 2 of 7

U.S. Patent Appln. No. <u>17/</u>247,217 Filing Date 03 Dec 2020

Attorney Docket No. AMZ2P123 Client Docket No. P70994-US01

Title of Invention: INPUT/OUTPUT VOLTAGE TESTING WITH BOUNDARY SCAN BYPASS

Inventors: Dan Trock, Alon Postavski, Etai Wagner, Victor David Romanov

applications for United States and foreign letters patent on said Invention, for litigation

regarding said letters patent, or for the purpose of protecting title to said Invention or letters

patent therefor.

AND ASSIGNOR(S) DOES HEREBY sell, assign, transfer, and convey to ASSIGNEE, its

successors, legal representatives, and assigns all claims for damages and all remedies arising out

of any violation of the rights assigned hereby that may have accrued prior to the date of

assignment to ASSIGNEE, or may accrue hereafter, including, but not limited to, the right to sue

for, collect, and retain damages for past infringements of said letters patent before or after

issuance.

AND ASSIGNOR(S) DOES HEREBY covenant and agree that ASSIGNOR(S) will

communicate to ASSIGNEE, its successors, legal representatives and assigns, any facts known to

ASSIGNOR respecting the Invention or said Application, and testify in any legal proceeding, assist

in the preparation of any other patent property relating to the Application and the Invention or

any improvements made thereto, sign/execute all lawful papers, provide all requested

documents, execute and make all rightful oaths and/or declarations in connection with the

Application and the Invention including any improvements made thereto, any patent

applications filed therefrom, and any continuing application filed from any of the

aforementioned applications, and generally do everything possible to aid ASSIGNEE, its

successors, legal representatives and assigns, to obtain and enforce proper patent protection for

the Invention in all countries. These provisions are binding upon ASSIGNOR's heirs, legal

representatives, administrators, and assigns.

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PATENT

REEL: 057639 FRAME: 0867

U.S. Patent Appln. No. 17/247,217 Filing Date 03 Dec 2020 Attorney Docket No. AMZ2P123 Client Docket No. P70994-US01

Title of Invention: INPUT/OUTPUT VOLTAGE TESTING WITH BOUNDARY SCAN BYPASS

Inventors: Dan Trock, Alon Postavski, Etai Wagner, Victor David Romanov

DECLARATION

As a below named inventor, I hereby declare that:

This declaration is directed to:

the attached application; or		
United States Patent Application number	filed on	; or

The above-identified application was made or authorized to be made by me.

PCT International Application number ______filed on ____

I believe that I am the original inventor or an original joint inventor of a claimed invention in the application.

I have reviewed and understand the contents of the above-identified application, and acknowledge the duty to disclose to the US Patent & Trademark Office all information known to me to be material to the patentability as defined in 37 CFR 1.56.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 USC 1001 by fine or imprisonment of not more than five (5) years, or both.

Day Trock

Dan Trock

Dec 2nd, 2020

Date

U.S. Patent Appin. No. <u>17/247,217</u> Filing Date <u>03 Dec 2020</u> Attorney Docket No. AMZ2P123 Client Docket No. P70994-US01

Title of Invention: INPUT/OUTPUT VOLTAGE TESTING WITH BOUNDARY SCAN BYPASS

inventors: Dan Trock, Alon Postavski, Etal Wagner, Victor David Romanov

DECLARATION

As a	pelow	named	inventor,	•	hereb	Ŋε	lecia	6. 1	tert:	•
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Alon Postavski

Dec. 03-2020

Date

U.S. Patent Appln. No. 17/247,217 Filing Date 03 Dec 2020

Attorney Docket No. AMZ2P123 Client Docket No. P70994-US01

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Etai Wagner AN

Etai Wagner

Dec. 03 - 2020

U.S. Patent Appln. No. 17/247,217 Attorney Docket No. AMZ2P123 Client Docket No. P70994-US01 Filing Date 03 Dec 2020 Title of Invention: INPUT/OUTPUT VOLTAGE TESTING WITH BOUNDARY SCAN BYPASS Inventors: Dan Trock, Alon Postavski, Etai Wagner, Victor David Romanov **DECLARATION** As a below named inventor, I hereby declare that: This declaration is directed to: the attached application; or United States Patent Application number ______ filed on _____; or PCT International Application number ______ filed on ______. The above-identified application was made or authorized to be made by me. I believe that I am the original inventor or an original joint inventor of a claimed Invention in the application. I have reviewed and understand the contents of the above-identified application, and acknowledge the duty to disclose to the US Patent & Trademark Office all information known to me to be material to the patentability as defined in 37 CFR 1.56. I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 USC 1001 by fine or imprisonment of not more than five (5) years, or both. Victor David Romanov

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Page 1 of 4 **PATENT** REEL: 057639 FRAME: 0872

U.S. Patent Appln. No. 17/247,217

Filing Date 03 Dec 2020

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Page 3 of 4

PATENT

REEL: 057639 FRAME: 0874

U.S. Patent Appln. No. 17/247,217 Filing Date 03 Dec 2020

RECORDED: 09/29/2021

Attorney Docket No. AMZ2P123 Client Docket No. P70994-US01

Title of Invention: INPUT/OUTPUT VOLTAGE TESTING WITH BOUNDARY SCAN BYPASS

Inventors: Dan Trock, Alon Postavski, Etai Wagner, Victor David Romanov
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As a below named inventor, I hereby declare that:
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☑ United States Patent Application number 17/247,217 filed on 03 Dec 2020; or
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The above-identified application was made or authorized to be made by me.
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Victor David Romanov
29-Sep-21
Date

Page 4 of 4