

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
ARVIND CHOKHANI	10/11/2021
JOSEPH MICHAEL SWENTON	10/12/2021
MARTIN THOMAS AMODEO	10/11/2021
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17499414
CORRESPONDENCE DATA	
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DATE SIGNED:	10/12/2021
Total Attachments: 2	
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ASSIGNMENT

WHEREAS, I, the undersigned inventor (or one of the undersigned joint inventors), of residence as listed, having invented certain new and useful improvements as below entitled, for which application for United States Letters Patent is made, the said application having been executed on the date set forth below; and

WHEREAS, Cadence Design Systems, Inc., a corporation organized and existing under the laws of the Country of the United States of America, with a place of business at 2655 Seely Avenue, San Jose, California, 95134, is desirous of acquiring my entire right, title and interest in and to the said invention, and to the said application and any Letters Patent that may issue thereon;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, I hereby sell and assign to the said Cadence Design Systems, Inc., its successors and assigns, my/our entire right, title and interest in and to the said invention and in to the said application and all patents which may be granted therefore, and all divisions, reissues, substitutions, continuations, and extensions thereof; and I hereby authorize and request the Commissioner of Patents and Trademarks to issue all patents for said invention, or patent resulting therefrom, insofar as my interest is concerned, to the said Cadence Design Systems, Inc., as assignee of my entire right, title and interest.

I also hereby sell and assign to Cadence Design Systems, Inc., its successors and assigns, my foreign rights to the invention disclosed in said application, in all countries of the world, including but not limited to, the right to file applications and obtain patents under the terms of the International Convention for the Protection of Industrial Property, and of the European Patent Convention, and further agree to execute any and all patent applications, assignments, affidavits, and any other papers in connection therewith necessary to perfect such patent rights.

I hereby further agree that I will communicate to said Cadence Design Systems, Inc., or to its successors, assigns, and legal representatives, any facts known to me respecting said invention, and at the expense of said assignee company, testify in any legal proceedings, sign all lawful papers, execute all divisional, continuation, reissue and substitute applications, make all lawful oaths, and generally do everything possible to aid said Cadence Design Systems, Inc., its successors, assigns and nominees to obtain and enforce proper patent protection for said invention in all countries.

I hereby further authorize and direct the attorneys of record to insert the serial number and filing date of said application now identified by the attorney docket number and title set forth above as soon as the same shall have been made known to them by the United States Patent and Trademark Office.

IN WITNESS WHEREOF, I hereunto set hand and seal this day and year;

TITLE	UTILIZING TRANSITION ATPG TEST PATTERNS TO DETECT MULTICYCLE FAULTS AND/OR DEFECTS IN AN IC CHIP		
NONPROVISIONAL APPL. NO.	17/499,414	FILING DATE	2021-10-12

SIGNATURE OF INVENTOR	<i>Arvind Chokhani</i> <small>Arvind Chokhani (Oct 11, 2021, 14:59 EDT)</small>
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SIGNATURE OF INVENTOR	<i>Joseph M. Swenton</i> <small>Joseph M. Swenton (Oct 12, 2021 18:45 EDT)</small>
PRINTED NAME OF INVENTOR	JOSEPH MICHAEL SWENTON
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SIGNATURE OF INVENTOR	<i>Martin Amodeo</i> <small>Martin Amodeo (Oct 11, 2021 13:13 PDT)</small>
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