PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT7016212

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST

CONVEYING PARTY DATA

Name	Execution Date
WELLS FARGO BANK, NATIONAL ASSOCIATION, AS ADMINISTRATIVE AGENT	11/09/2021

RECEIVING PARTY DATA

Name:	LATTICE SEMICONDUCTOR CORPORATION
Street Address:	5555 NE MOORE COURT
City:	HILLSBORO
State/Country:	OREGON
Postal Code:	97124

PROPERTY NUMBERS Total: 44

Property Type	Number
Patent Number:	10033552
Patent Number:	10129318
Patent Number:	6771192
Patent Number:	6809567
Patent Number:	6976201
Patent Number:	7039121
Patent Number:	7058121
Patent Number:	7113507
Patent Number:	7257129
Patent Number:	7340558
Patent Number:	7746798
Patent Number:	7903684
Patent Number:	8680712
Patent Number:	8832338
Patent Number:	9232265
Patent Number:	9234930
Patent Number:	9366712
Patent Number:	9398329
Patent Number:	9407469
Patent Number:	9509669

Property Type	Number
Patent Number:	9852103
Patent Number:	6717478
Patent Number:	6891910
Patent Number:	6961095
Patent Number:	7103013
Patent Number:	7154905
Patent Number:	7187307
Patent Number:	7231009
Patent Number:	7269673
Patent Number:	7500032
Patent Number:	7557863
Patent Number:	7599316
Patent Number:	7856520
Patent Number:	7921231
Patent Number:	8116240
Patent Number:	8275914
Patent Number:	8484387
Patent Number:	8601173
Patent Number:	8920188
Patent Number:	9274992
Patent Number:	9471525
Patent Number:	9472873
Patent Number:	9685785
Patent Number:	9703729

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SIGNATURE:	/Betty G. Smith/
DATE SIGNED:	11/10/2021

Total Attachments: 6

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NOTICE OF RELEASE OF SECURITY INTEREST IN PATENTS

Dated as of November 9, 2021

Reference is hereby made to (a) that certain Notice of Grant of Security Interest in Patents, dated as of May 17, 2019, by and among, LATTICE SEMICONDUCTOR CORPORATION, a Delaware corporation having its chief executive office at 5555 NE Moore Court, Hillsboro, OR 97124 (the "Grantor"), and WELLS FARGO BANK, NATIONAL ASSOCIATION, as Administrative Agent (the "Administrative Agent"), with offices at 1700 Lincoln Street, 3rd Floor, MAC C7300-033, Denver, Colorado 80203-4500, which was duly recorded on May 21, 2019, at Reel 049980, Frame 0786 in the United States Patent and Trademark Office and (b) that certain Notice of Grant of Security Interest in Patents, dated as of July 18, 2019, by and among the Grantor and the Agent, which was duly recorded on July 18, 2019 at Reel 049795, Frame 0481 in the United States Patent and Trademark Office (collectively, the "Patent Security Agreements").

The lien and security interests in the specific patents set forth on Schedule 1 attached hereto and any other right, title or interest granted to the Administrative Agent in the patents set forth on Schedule 1 attached hereto (the "Specified Patent Collateral") are hereby terminated, cancelled and released, without representation or warranty, and the Administrative Agent hereby re-assigns to the Grantor any right, title or interest it may have in or to any of the Specified Patent Collateral; provided, however, that no other mortgages, liens or security interests are released hereby except as expressly set forth herein, and the Patent Security Agreements referenced above shall remain in full force and effect with respect to all patents, patent applications and all related proceeds, products, rents, profits and claims with respect thereto, other than the Specified Patent Collateral.

WELLS FARGO BANK, NATIONAL ASSOCIATION, as Administrative Agent

By: _____

Name: Cheryl L. Ebner

Title: Senior Vice President / Relationship Manager

SCHEDULE 1

PATENTS

LATTICE SEMICONDUCTOR CORPORATION:

REEL: 049980 FRAME: 0786

Patent / Publication Number	Application Number	Title	Country Code	Status (Active/Expired/ Unknown)	Type (Grant/Application/ Unpublished Application)
US10033552	US15/224286	Driving data of multiple protocols through a single set of pins	US	Active	Grant
US10129318	US14/617755	Media stream data and control parameter synchronization	US	Active	Grant
US6771192	US10/045600	Method and system for dc-balancing at the physical layer	US	Active	Grant
US6809567	US09/989645	System and method for multiple-phase clock generation	US	Active	Grant
US6976201	US10/036794	Method and system for host handling of communications errors	US	Active	Grant
US7039121	US10/045393	Method and system for transition-controlled selective block inversion communications	US	Active	Grant
US7058121	US09/989647	Logic gates including diode-connected metal-oxide-semiconductor field-effect transistors (mosfets) to control input threshold voltage levels and switching transients of output logic signals	US	Active	Grant
US7113507	US10/053461	Method and system for communicating control information via out-of-band symbols	US	Active	Grant
US7257129	US10/045297	Memory architecture with multiple serial communications ports	US	Active	Grant

Patent / Publication Number	Application Number	Title	Country Code	Status (Active/Expired/ Unknown)	Type (Grant/Application/ Unpublished Application)
US7340558	US10/045601	Multisection memory bank system	US	Active	Grant
US7746798	US10/045625	Method and system for integrating packet type information with synchronization symbols	US	Active	Grant
US7903684	US11/828286	Communications architecture for transmission of data between memory bank caches and ports	US	Active	Grant
US8680712	US12/636063	Power delivery over digital interaction interface for video and audio (diiva)	US	Active	Grant
US8832338	US13/736765	Mechanism for facilitating dynamic timestamp-less clock generation for transmitting media streams over shared channels	US	Active	Grant
US9232265	US13/434273	Method, apparatus and system for transitioning an audio/video device between a source mode and a sink mode	US	Active	Grant
US9234930	US13/021958	Determination of physical connectivity status of devices based on electrical measurement	US	Active	Grant
US9366712	US14/559523	Determination of physical connectivity status of devices based on electrical measurement	US	Active	Grant
US9398329	US13/521762	Video management and control in home multimedia network	US	Active	Grant
US9407469	US14/128551	Driving data of multiple protocols through a single set of pins	US	Active	Grant
US9509669	US14/610855	Efficient routing of streams encrypted using point-to-point authentication protocol	US	Active	Grant
US9852103	US14/681992	Bidirectional transmission of usb data using audio/video data channel	US	Active	Grant

PATENT
REEL: 058067 FRAME: 0902

Patent / Publication Number	Application Number	Title	Country Code	Status (Active/Expired/ Unknown)	Type (Grant/Application/ Unpublished Application)
US6717478	US09/989587	Multi-phase voltage controlled oscillator (vco) with common mode control	US	Active	Grant
US6891910	US09/759624	Baud-rate timing recovery	US	Active	Grant
US6961095	US09/922990	Digital display jitter correction apparatus and method	US	Active	Grant
US7103013	US09/989580	Bidirectional bridge circuit having high common mode rejection and high input sensitivity	US	Active	Grant
US7154905	US10/035911	Method and system for nesting of communications packets	US	Active	Grant
US7187307	US10/459989	Method and system for encapsulation of multiple levels of communication protocol functionality within line codes	US	Active	Grant
US7231009	US10/371220	Data synchronization across an asynchronous boundary using, for example, multi-phase clocks	US	Active	Grant
US7269673	US10/781405	Cable with circuitry for asserting stored cable data or other information to an external device or user	US	Active	Grant
US7500032	US11/848758	Cable with circuitry for asserting stored cable data or other information to an external device or user	US	Active	Grant
US7557863	US11/219323	Digital display jitter correction apparatus and method	US	Active	Grant
US7599316	US11/441669	Bi-directional bridge circuit having high common mode rejection and high input sensitivity	US	Active	Grant
US7856520	US11/969852	Control bus for connection of electronic devices	US	Active	Grant

Patent / Publication Number	Application Number	Title	Country Code	Status (Active/Expired/ Unknown)	Type (Grant/Application/ Unpublished Application)
US7921231	US11/969865	Discovery of electronic devices utilizing a control bus	US	Active	Grant
US8116240	US12/573847	Bi-directional bridge circuit having high common mode rejection and high input sensitivity	US	Active	Grant
US8275914	US12/577707	Discovery of connections utilizing a control bus	US	Active	Grant
US8484387	US13/172745	Detection of cable connections for electronic devices	US	Active	Grant
US8601173	US13/172742	Detection of cable connections for electronic devices	US	Active	Grant
US8920188	US13/779372	Integrated connector/flex cable	US	Active	Grant
US9274992	US14/322753	Cable with circuitry for communicating performance information	US	Active	Grant
US9471525	US15/010166	Cable with circuitry for communicating performance information	US	Active	Grant
US9472873	US14/824717	Reversible receptacle connector	US	Active	Grant
US9685785	US14/181446	Power delivery over digital interaction interface for video and audio (diiva)	US	Active	Grant
US9703729	US14/677806	Detecting the orientation of a multimedia link connected to a device	US	Active	Grant

RECORDED: 11/10/2021
PATENT
REEL: 058067 FRAME: 0904