

## PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT7074036

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
HIROKI TAKAHASHI	12/10/2021
TORU ISHIKAWA	12/10/2021
<b>RECEIVING PARTY DATA</b>	
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<b>Street Address:</b>	8000 SOUTH FEDERAL WAY
<b>City:</b>	BOISE
<b>State/Country:</b>	IDAHO
<b>Postal Code:</b>	83716
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	17551095
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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<b>ATTORNEY DOCKET NUMBER:</b>	P292951.US.01
<b>NAME OF SUBMITTER:</b>	LEJLA NYCUM-DUVNJAK
<b>SIGNATURE:</b>	/Lejla Nycum-Duvnjak/
<b>DATE SIGNED:</b>	12/14/2021
<b>Total Attachments: 2</b>	
source=20211210_SignedAssignment_P292951US01 4865-5268-3014 v.1#page1.tif	
source=20211210_SignedAssignment_P292951US01 4865-5268-3014 v.1#page2.tif	

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

HIROKI TAKAHASHI and TORU ISHIKAWA

Filed : Concurrently herewith

For : APPARATUSES AND METHODS FOR 1T  
AND 2T MEMORY CELL ARCHITECTURES

Docket No. P292951.US.01

Disclosure No. 2021-136892-US

ASSIGNMENT:

Enclosed for recording

Previously recorded

Date: \_\_\_\_\_

Reel: \_\_\_\_\_

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned do hereby:

SELL, ASSIGN AND TRANSFER to **Micron Technology, Inc.** (the "Assignee"), a corporation of Delaware, having a place of business at 8000 South Federal Way, Boise, Idaho 83716-9632, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith and is entitled "**APPARATUSES AND METHODS FOR 1T AND 2T MEMORY CELL ARCHITECTURES**"; such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States of America;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or will be made to others by the undersigned, and that the full right to convey the same as herein expressed is possessed by the undersigned;

COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee;

TO BE BINDING on the heirs, assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

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**ASSIGNOR:**

Signature: 高橋 弘樹

Date: 2021-12-10  
(YYYY-MM-DD)

Assignor Name: HIROKI TAKAHASHI

**ASSIGNOR:**

Signature: Toru Ishikawa

Date: 2021-12-10  
(YYYY-MM-DD)

Assignor Name: TORU ISHIKAWA