507090635 01/24/2022 PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:		NEW ASSIGNMENT					
NATURE OF CONVEYANCE:		ASSIGNMENT					
CONVEYING PARTY D	ΑΤΑ						
		Name Exc	ecution Date				
JONATHAN ALEXANDE	R ROSS	05/2	7/2020				
DENNIS CHARLES ABT	S	05/2	7/2020				
JOHN THOMPSON		05/2	7/2020				
GREGORY M. THORSC	N	05/2	8/2020				
RECEIVING PARTY DATA							
Name:	GROQ,	INC.					
Street Address:	400 CASTRO ST., SUITE 600						
City:	MOUNTAIN VIEW						
State/Country:	CALIFC	CALIFORNIA					
Postal Code:	94041						
PROPERTY NUMBERS	Total: 1	Numbor					
Property Type		Number					
		Number 17582895					
Property Type Application Number:							
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COMBINED DECLARATION AND ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, the person(s) named below (referred to as "INVENTOR" whether singular or plural) has sold, assigned, and transferred and does hereby sell, assign, and transfer to **Groq, Inc.**, a Delaware Corporation, having a place of business at 400 Castro Street, Suite 600, Mountain View, CA, 94041 ("ASSIGNEE"), for itself and its successors, transferees, and assignees, the following:

1. The entire worldwide right, title, and interest in all inventions and improvements ("SUBJECT MATTER") that are disclosed in the following provisional or non-provisional application(s), including those filed under 35 U.S.C. § 111, design applications filed under 35 U.S.C. § 171, Registered Community Designs, international applications filed according to the Patent Cooperation Treaty (PCT), U.S. national phase applications filed under 35 U.S.C. § 371, national or regional applications filed in any other country throughout the world, and any patents issuing thereon ("PATENT RIGHTS"):

- U.S. Application No. 16/526,966, entitled Processor Architecture, filed on July 30, 2019, which is a continuation of application, filed on September 14, 2018, now bearing US Application No. 16/132,243, which claims priority from a provisional application, filed on September 15, 2017, now bearing US Application No. 62/559,333.
- 2. The entire worldwide right, title, and interest in and to:

(a) the PATENT RIGHTS, including any right of priority; (b) any provisional, divisional, continuation, substitute, renewal, reissue, and other related applications thereto which have been or may be filed in the United States or elsewhere in the world; (c) any patents which may be granted on the applications set forth in (a) and (b) above; and (d) the right to sue in its own name and to recover for past infringement of any or all of any applications or patents issuing therefrom together with all rights to recover damages for infringement of provisional rights.

INVENTOR agrees that ASSIGNEE may apply for and receive patents for SUBJECT MATTER in ASSIGNEE's own name.

INVENTOR agrees to do the following, when requested, and without further consideration, in order to carry out the intent of this Assignment: (1) execute all oaths, assignments, powers of attorney, applications, and other papers necessary or desirable to fully secure to ASSIGNEE the rights, titles and interests herein conveyed; (2) communicate to ASSIGNEE all known facts relating to the SUBJECT MATTER; and (3) generally do all lawful acts that ASSIGNEE shall consider desirable for securing, maintaining, and enforcing worldwide patent protection relating to the SUBJECT MATTER and for vesting in ASSIGNEE the rights, titles, and interests herein conveyed. INVENTOR further agrees to provide any successor, assign, or legal representative of ASSIGNEE with the benefits and assistance provided to ASSIGNEE hereunder.

34180/44239/FW/11415364.1

Title:	Processor Architecture				
Filed:	July 30, 2019	Attorney Docket #:	34180-44239/US		
Application #:	16/526,966				

INVENTOR represents that INVENTOR has the rights, titles, and interests to convey as set forth herein, and covenants with ASSIGNEE that the INVENTOR has not made and will not hereafter make any assignment, grant, mortgage, license, or other agreement affecting the rights, titles, and interests herein conveyed.

INVENTOR grants the attorney of record the power to insert on this Assignment any further identification that may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office or other authority for recordation of this document.

This Assignment may be executed in one or more counterparts, each of which shall be deemed an original and all of which may be taken together as one and the same Assignment.

DECLARATION

By signing below, INVENTOR further attests to the following:

- The APPLICATION was made or authorized to be made by INVENTOR.
- INVENTOR believes that INVENTOR is the original inventor or an original joint inventor of a claimed invention in the APPLICATION.

INVENTOR hereby acknowledges that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.

Mar. 07 0000

Date: <u>May 27, 2020</u> (Required)

Date: May 27, 2020 (Required)

Date: ______ (Required)

Jen

Jonathan Alexander Ross

Smark

Dennis Charles Abts

John Thompson

John Thompson

Title:	Processor Architecture				
Filed:	July 30, 2019	Attorney Docket #:	34180-44239/US		
Application #:	16/526,966				

Mychiller

May 28, 2020

(Required)

Gregory Michael Thorson