PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT7137500

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
MARTIN RUSSELL RAUMANN	09/03/2021
QI ZHENG	09/06/2021
BANDISH B. SHAH	09/07/2021
RAVINDER KUMAR	09/03/2021
KIN HING LEUNG	09/08/2021
SUMTI JAIRATH	09/03/2021
GREGORY FREDERICK GROHOSKI	09/03/2021

RECEIVING PARTY DATA

Name:	SAMBANOVA SYSTEMS, INC.
Street Address:	2100 GENG ROAD, SUITE 200
City:	PALO ALTO
State/Country:	CALIFORNIA
Postal Code:	94303

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	17582925

CORRESPONDENCE DATA

Fax Number: (650)712-0263

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone:	650-712-0340
Email:	amolina@hmbay.com
Correspondent Name:	HAYNES BEFFEL & WOLFELD LLP
Address Line 1:	637 MAIN ST.
Address Line 4:	PALO ALTO, CALIFORNIA 94019

ATTORNEY DOCKET NUMBER:	SBNV 1039-3
NAME OF SUBMITTER:	ANA MOLINA
SIGNATURE:	/Ana Molina/
DATE SIGNED:	01/24/2022

COMBINED DECLARATION (37 C.F.R. 1.63) AND ASSIGNMENT

Docket No.: SBNV 1039-2
Title of Invention: Dataflow All-Reduce for Reconfigurable Processor Systems
Application No.: 17/379,924
Filing Date: July 19, 2021
Inventor: Martin Russell RAUMANN, Qi ZHENG, Bandish B. SHAH, Ravinder KUMAR, Kin Hing LEUNG, Sumti JAIRATH, Gregory Frederick GROHOSKI

WHEREAS, as the above-identified inventor ("the Assignor"), I have made certain inventions, improvements, and discoveries ("the Inventions"). The Inventions are disclosed in the above-identified patent application/s and further identified by the docket number/s and title/s provided above in the header of this document.

I hereby declare that:

This declaration is directed to the attached patent application, or (if following box is checked)

____filed currently herewith;

_x_ United States application or PCT international application number 17/379,924 filed on July 19, 2021;

The above-identified application was made or authorized to be made by me.

I believe that I am the original inventor or an original joint inventor of a claimed invention in the application.

I hereby acknowledge that any willful false statement made in this declaration is punishable under 18 U.S.C. 1001 by fine or imprisonment of not more than five (5) years, or both.

I hereby state that I have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, CFR § 1.56.

I now convey all worldwide right, title, and interest in:

- the Inventions,

- the above-identified patent application/s, and any other applications for patent, design, utility model, certificate of invention, trademark, or trade dress, in any country or under any international or regional authority (e.g., WIPO/PCT, USPTO, EPO, ARIPO, EAPO, OAPI), to the extent some or all of its subject matter is disclosed and entitled to the benefit of priority in the above-identified patent application/s, including provisional, continuation, divisional, continuation-in-part, reissue, and reexamination applications (collectively "the Application/s"),

- the benefit of earlier filing date and right to claim priority to the Inventions and the Application/s under applicable law and treaties, including the Paris Convention for

COMBINED DECLARATION (37 C.F.R. 1.63) AND ASSIGNMENT

Docket No.: SBNV 1039-2
Title of Invention: Dataflow All-Reduce for Reconfigurable Processor Systems
Application No.: 17/379,924
Filing Date: July 19, 2021
Inventor: Martin Russell RAUMANN, Qi ZHENG, Bandish B. SHAH, Ravinder KUMAR, Kin Hing LEUNG, Sumti JAIRATH, Gregory Frederick GROHOSKI

the Protection of Industrial Property as revised and amended and 35 U.S.C. §§ 119, 120,

- any rights accrued by the publication of the Application/s, such as 35 U.S.C. § 154(d), Art. 67(1) EPC,

- any Letters Patent or Certificates of Invention that may be issued or granted from the Application/s, including reissued patents, reexamination certificates, renewals, and any rights resulting from an extension or adjustment of patent term, and

- any rights arising from the Inventions under laws and conventions protecting copyright, trademark, trade dress, or other industrial property,

(collectively "the IP Rights"), and

WHEREAS, **SambaNova Systems, Inc.**, a corporation organized and existing under the laws of the State of Delaware and having a principal place of business at 2100 Geng Road, Suite 200, Palo Alto, CA 94303 United States of America, desires to acquire the entire worldwide right, title, and interest for itself, its successors, and its assigns (collectively "the Assignee") in the IP Rights.

I warrant and represent to the Assignee that I have the full right to convey my right, title, and interest in the IP Rights without encumbrance, and that I have not made any earlier assignment, license, grant, or mortgage to another, or made any agreement with another, that would affect my right, title, and interest in the IP Rights.

By this Assignment and by virtue of any earlier agreements to assign, I hereby assign, transfer, convey, and sell my entire right, title, and interest in the IP Rights to the Assignee, including any claims or causes of action for damages or other remedies arising out of any earlier violation of the IP Rights, for and to inure to the Assignee's full benefit of the IP Rights as fully as they would have been held by me if this Assignment had not been made.

NOW, THEREFORE, to all whom it may concern, be it known that, for good and valuable consideration, the receipt and sufficiency of which I hereby acknowledge and hereby waive the right to any additional compensation concerning the IP Rights.

I authorize the Assignee and its legal representatives to:

(a) insert into this Assignment any supplemental information to make the identification more complete for any of the Application/s or Letters Patent already listed above,

**PATENT
REEL: 058748 FRAME: 0410**

COMBINED DECLARATION (37 C.F.R. 1.63) AND ASSIGNMENT

Docket No.: SBNV 1039-2
Title of Invention: Dataflow All-Reduce for Reconfigurable Processor Systems
Application No.: 17/379,924
Filing Date: July 19, 2021
Inventor: Martin Russell RAUMANN, Qi ZHENG, Bandish B. SHAH, Ravinder KUMAR, Kin Hing LEUNG, Sumti JAIRATH, Gregory Frederick GROHOSKI

(b)   prosecute the Application/s, file new applications for the Invention/s, and do all acts permitted by law to protect and enforce the IP Rights, and

(c)   request that Letters Patent and Certificates of Invention be issued and granted to and in the name of the Assignee by the issuing or granting authority, such as the U.S. Commissioner for Patents, 37 C.F.R. § 3.81.

I agree with the Assignee that I and my legal representatives, heirs, and executors will perform all acts deemed necessary or desirable by the Assignee to permit and assist it to secure the entire right, title, and interest in the IP Rights so that the Assignee may benefit from this Assignment, including:

(a)   communicating to the Assignee all facts known to me that relate to the history and extent of my individual contribution to the IP Rights, including the circumstances of the conception of the invention and its development and reduction to practice, and furnishing supporting documents or items in my control,

(b)   assisting in the preparation of the Application/s and their prosecution by the Assignee, 37 C.F.R. § 3.71, and in the enforcement of the IP Rights, including making, executing, and delivering to the Assignee any other necessary documents, such as further application papers, declarations, affidavits, disclaimers, petitions, or assignments, and

(c)   assisting and cooperating in administrative and legal proceedings, including giving written and oral testimony, so that these will be performed without further consideration, except for reasonable actual expenses to be reimbursed.

I further agree with the Assignee that I will not perform any future act, execute any subsequent document, or make any subsequent agreement with another person or party that conflicts with this Assignment, that purports to transfer any right, title, or interest in the IP Rights that are the subject of this Assignment, unless agreed to in writing by the Assignee or that prevents the Assignee from receiving the full benefit of the bargain under this Assignment.

I further authorize the Assignee, or anyone it may properly designate, to insert on each page of this instrument (where indicated) the filing date and application number of the Application/s when known.

If any provision of this Assignment is held invalid or unenforceable, that provision shall be enforced to the maximum extent permissible in order to put into effect the intent of the Assignors and the Assignee, and the remainder of this Assignment shall not be affected but continue in full force and effect.

**PATENT
REEL: 058748 FRAME: 0411**

COMBINED DECLARATION (37 C.F.R. 1.63) AND ASSIGNMENT

Docket No.: SBNV 1039-2
Title of Invention: Dataflow All-Reduce for Reconfigurable Processor Systems
Application No.: 17/379,924
Filing Date: July 19, 2021
Inventor: Martin Russell RAUMANN, Qi ZHENG, Bandish B. SHAH, Ravinder KUMAR, Kin Hing LEUNG, Sumti JAIRATH, Gregory Frederick GROHOSKI

I acknowledge my prior and ongoing obligations to sell, assign, and transfer the IP Rights under this Assignment to the Assignee. I am unaware of any reason why the Assignee may not have the full and unencumbered right to sell, assign, and transfer the IP Rights hereby sold, assigned, and transferred. I have not executed, and will not execute, any document or instrument in conflict with this Assignment.

Legal Name of Inventor: <u>Martin Russell RAUMANN</u>

Signature: / _____ /   Date: _____ September 3, 2021

Legal Name of Inventor: <u>Qi ZHENG</u>

Signature: / _____ /   Date: _____ September 6, 2021

Legal Name of Inventor: <u>Bandish B. SHAH</u>

Signature: / _____ /   Date: _____ September 7, 2021

Legal Name of Inventor: <u>Ravinder KUMAR</u>

Signature: / _____ /   Date: _____ September 3, 2021

PATENT
REEL: 058748 FRAME: 0412

COMBINED DECLARATION (37 C.F.R. 1.63) AND ASSIGNMENT

Docket No.: SBNV 1039-2
Title of Invention: Dataflow All-Reduce for Reconfigurable Processor Systems
Application No.: 17/379,924
Filing Date: July 19, 2021
Inventor: Martin Russell RAUMANN, Qi ZHENG, Bandish B. SHAH, Ravinder KUMAR, Kin Hing LEUNG, Sumti JAIRATH, Gregory Frederick GROHOSKI

Legal Name of Inventor: <u>Kin Hing LEUNG</u>

DocuSigned by:

Kin Hing LEUNG

E33D8ADCB191496...

Signature: / _____ /   Date: _____ September 8, 2021

Legal Name of Inventor: <u>Sumti JAIRATH</u>

DocuSigned by:

Sumti JAIRATH

FABAB24763594D8...

Signature: / _____ /   Date: _____ September 3, 2021

Legal Name of Inventor: <u>Gregory Frederick GROHOSKI</u>

DocuSigned by:

Gregory Frederick GROHOSKI

04A27B2D55884D4..

Signature: / _____ /   Date: _____ September 3, 2021

PATENT