

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT7163884

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE OF SECURITY INTEREST AT REEL 038707 FRAME 0703
CONVEYING PARTY DATA	
Name	Execution Date
JPMORGAN CHASE BANK, N.A.	02/03/2022
RECEIVING PARTY DATA	
Name:	WESTERN DIGITAL CORPORATION
Street Address:	3355 MICHELSON DRIVE
Internal Address:	SUITE 100
City:	IRVINE
State/Country:	CALIFORNIA
Postal Code:	92612
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	6049973
CORRESPONDENCE DATA	
Fax Number:	(202)408-3141
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
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Correspondent Name:	CSC
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Address Line 4:	WASHINGTON, D.C. 20005
NAME OF SUBMITTER:	JEAN PATERSON
SIGNATURE:	/jep/
DATE SIGNED:	02/08/2022
Total Attachments: 4	
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RELEASE OF SECURITY INTEREST IN PATENTS

This RELEASE OF SECURITY INTEREST IN PATENTS (this “Release”), dated as of February 3, 2022 (the “Effective Date”), is made by JPMorgan Chase Bank, N.A., in its capacity as Agent (the “Agent”), in favor of the grantor party identified on the signature page hereto (the “Grantor”).

WHEREAS, pursuant to that certain Security Agreement, dated as of May 12, 2016, by and among the Agent, the Grantor and certain other parties thereto (as amended, amended and restated, or otherwise modified from time to time, the “Security Agreement”), the Grantor granted to the Agent, in its capacity as Agent, a lien on and security interest in and to certain collateral;

WHEREAS, pursuant to the Security Agreement, the Grantor executed and delivered a Patent Collateral Agreement, dated as of May 12, 2016 (the “Patent Collateral Agreement”), for recordal with the United States Patent and Trademark Office;

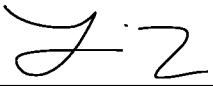
WHEREAS, the Patent Collateral Agreement was recorded with the United States Patent and Trademark Office on May 16, 2016 at Reel/Frame 038707/0703;

NOW, THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged, the Agent hereby agrees as follows:

1. Defined Terms. All capitalized terms used, but not otherwise defined herein, shall have the respective meanings ascribed in or otherwise referenced in the Security Agreement or the Patent Collateral Agreement, as applicable.
2. Release. The Agent, without representation or warranty of any kind, hereby absolutely, unconditionally and irrevocably releases, discharges, terminates and cancels all of its lien on and security interest in and to the Patent Collateral, including the patents and patent applications set forth Schedule A attached hereto, arising under the Security Agreement and/or the Patent Collateral Agreement.
3. Termination. The Agent, without representation or warranty of any kind, terminates and cancels the Patent Collateral Agreement.
4. Further Assurances. The Agent agrees to take all further actions, and provide to the Grantor and its successors, assigns or other legal representatives, all such cooperation and assistance (including, without limitation, the execution and delivery of any and all documents or other instruments), reasonably requested by the Grantor, at the Grantor’s sole cost and expense, to more fully and effectively effectuate the purposes of this Release.
5. Governing Law. This Release shall be governed exclusively under the laws of New York State, without regard to conflicts of law or choice of law principles.
6. Successor and Assigns. This Release shall be binding on and shall inure to the benefit of the parties hereto and their respective successors and assigns.

IN WITNESS WHEREOF, the Agent has caused this Release to be executed by its duly authorized representative as of the Effective Date:

**JPMORGAN CHASE BANK, N.A., acting in its
capacity as agent for the Lenders**

By: 
Name: Timothy Lee
Title: Executive Director

GRANTORS:
Western Digital Corporation

SCHEDULE A

U.S. PATENTS AND APPLICATIONS

Owner: Western Digital Corporation

No.	Title	Patent Number
1.	METHOD OF ASSEMBLING AN INTEGRATED COMPUTER MODULE	6049973