

PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT7199231

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
MARVELL SEMICONDUCTOR, INC.	01/26/2022
RECEIVING PARTY DATA	
Name:	MARVELL ASIA PTE LTD
Street Address:	TAI SENG CENTRE
Internal Address:	3 IRVING ROAD #10-01
City:	SINGAPORE
State/Country:	SINGAPORE
Postal Code:	369522
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16864006
CORRESPONDENCE DATA	
Fax Number:	(408)222-2755
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	408-222-2500
Email:	sherir@marvell.com
Correspondent Name:	KELVIN VIVIAN
Address Line 1:	5488 MARVELL LANE
Address Line 4:	SANTA CLARA, CALIFORNIA 95054
ATTORNEY DOCKET NUMBER:	MP13168
NAME OF SUBMITTER:	KELVIN VIVIAN
SIGNATURE:	/Kelvin Vivian/
DATE SIGNED:	02/28/2022
Total Attachments: 2	
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ASSIGNMENT
Corporate

WHEREAS Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, CA 95054 (hereafter the "Assignor"), is the owner of the patents, including utility models and other similar registrations, or patent applications identified below (hereafter referred to as the "Patent Application(s)").

Marvell Asia Pte Ltd, a corporation of Singapore, having a place of business at Tai Seng Centre, 3 Irving Road #10-01, Singapore 369522 (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application(s).

THEREFORE, in consideration of the sum of One Dollar (\$1.00) US or equivalent and other good and valuable consideration paid to it, the receipt and sufficiency of which hereby is acknowledged, Assignor hereby sells, assigns and transfers to the Assignee the entire rights, title, and interest, together with all priority rights, in and to the Patent Application(s), and all inventions described and claimed therein, in any and all patents including utility models and other similar registrations granted from the Patent Application(s) and from patents or patent applications claiming priority directly or indirectly from the Patent Application(s), including but not limited to any and all reissues, renewals, reexaminations, divisions, and continuations in whole and in part, and foreign counterparts of the foregoing including national stage applications to the full end of the term or terms for which such patents issue in the United States, and/or any foreign countries, such entire rights, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this assignment not been made, including the right to sue and recover damages for past, present and future infringement.

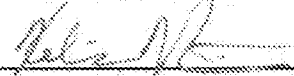
Marvell Reference No.	Application No.	Filing Date	Title
MP13168	16/864,006	4/30/2020	SYSTEM AND METHODS FOR ON-CHIP MEMORY (OCM) PORT THROTTLING FOR MACHINE LEARNING OPERATIONS
MP13168DE	102021111195.1	4/30/2021	SYSTEM AND METHODS FOR ON-CHIP MEMORY (OCM) PORT THROTTLING FOR MACHINE LEARNING OPERATIONS

Assignor hereby covenants and agrees to execute and deliver, at the request of Assignee, such further instruments of transfer and assignment and to take any other action as such Assignee may reasonably request to more effectively consummate the assignment contemplated by this agreement. Specifically, Assignor agrees to, at Assignee's expense, execute, acknowledge and deliver such further documents, instruments, conveyances and assurances and take such further actions as may be reasonably required to register in the name of Assignee the assignment of the patents in any appropriate governmental agency or registrar.

The undersigned representatives of the Assignor and the Assignee authorize and request that the attorneys of record in any of the Patent Application(s) insert in this assignment the filing date(s) and application number(s) of the Patent Application(s) when officially known.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the name of the Assignor's undersigned representative, which date shall be the effective date of this assignment.

MARVELL SEMICONDUCTOR, INC.

By: 

Name: Kelvin M. Vivian

Title: Director, Assistant General Counsel, IP Development

Date: 26 January 2022

MARVELL ASIA PTE LTD

By: 

Name: Chen Seok Ching

Title: Vice President, Assembly & Test Operations and County Manager

Date: 1 - Feb - 2022