

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

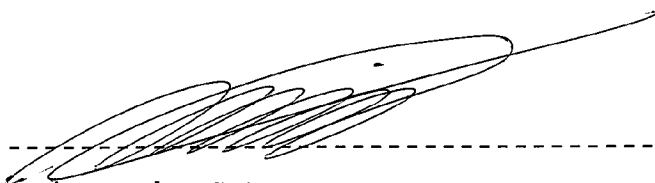
EPAS ID: PAT7230843

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
TADASHI KAI	11/27/2018
RECEIVING PARTY DATA	
Name:	KIOXIA CORPORATION
Street Address:	1-21, SHIBAURA 3-CHOME, MINATO-KU
City:	TOKYO
State/Country:	JAPAN
Postal Code:	108-0023
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17003846
CORRESPONDENCE DATA	
Fax Number:	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Email:	docketing@kimandstewart.com, mguajardo@kimandstewart.com
Correspondent Name:	KIM & STEWART LLP
Address Line 1:	111 N. MARKET ST.
Address Line 2:	SUITE 414
Address Line 4:	SAN JOSE, CALIFORNIA 95113
ATTORNEY DOCKET NUMBER:	TAI/3066US
NAME OF SUBMITTER:	MICHAEL D. STEWART
SIGNATURE:	/Michael D. Stewart/
DATE SIGNED:	03/17/2022
Total Attachments: 3	
source=TAI3066US_ASG_20220317#page1.tif	
source=TAI3066US_ASG_20220317#page2.tif	
source=TAI3066US_ASG_20220317#page3.tif	

CERTIFICATION

I, Mitsunobu Sato, Toranomom East Bldg, 7-13, Nishi-Shimbashi 1-chome, Minato-ku, Tokyo, Japan, do hereby certify that I am conversant with the English and Japanese languages and am a competent translator thereof, and I further certify that to the best of my knowledge and belief the attached English translation is a true and correct translation made by me.

Signed on this 7th day of March, 2022

A handwritten signature in black ink, consisting of several overlapping loops and a long horizontal stroke extending to the right. The signature is positioned above a horizontal dashed line.

Mitsunobu Sato

Only for Invention/Device Proposal and within Responsible Department/Section

Invention/Device Notification

Date of Notification November 14, 2018

Proposal Control Number / Application Control Number

Proposal Reference Number

B0089B18

Responsible Department and Section

(Device Technology Research & Development Center)

[Device Technology Development Department 1]

(Device Technology Group 1)

Title of the Invention

Magnetoresistive Element

Summary of the Invention

In the layered type magnetic memory including a selector and an MTJ (Magnetic Tunnel Junction), heat generation of the selector by applying voltage is liable to cause deterioration in the properties of the elements. For this reason, a heat dissipation layer is added to suppress heat generation when voltage is applied. The current proposal makes it possible to efficiently release heat by using an insulator high in heat conductivity as the heat dissipation layer.

Internal Inventor(s) (6 inventors)

Employee No.	Name	Company	Office	Dept./Sect.	Inventor's Contribution Rate
16003010	Taichi IGARASHI	TMC	(RDC)	(Device Technology Research & Development Center) [Device Technology Development Department 1] (Device Technology Group 1)	30.00%
06315510	Tadaomi DAIBOU	TMC	(RDC)	(Device Technology Research & Development Center) [Device Technology Development Department 1] (Device Technology Group 1)	20.00%
16610586	Junichi ITO	TMC	(RDC)	(Device Technology Research & Development Center) [Device Technology Development Department 1] (Device Technology Group 1)	20.00%
00252210	Tadashi KAI	TMC	(RDC)	(Device Technology Research & Development Center) [Device Technology Development Department 1] (Device Technology Group 1)	15.00%
13002510	Shogo ITAI	TMC	(MC)	(Device Technology Research & Development Center) [TC Technology Development] (TC III)	10.00%
92030310	Toshiyuki ENDA	TMC	(MC)	(Device Technology Research & Development Center) [TC Technology Development]	5.00%

Consent Matters

I (Inventor/Designer) hereby agree as follows:

- The Invention, etc. falls under an Employee Invention, etc.
- In countries where the right to file an intellectual property application for an Employee Invention, etc. shall belong to a corresponding Inventor(s), etc. pursuant to applicable domestic laws and regulations, the Company shall succeed the right to file an intellectual property application for the Invention, etc. at the time of completion of the Invention, etc.
- In countries where the right to file an intellectual property application for an Employee Invention, etc. shall belong to the relevant Inventor(s), etc. pursuant to the applicable domestic laws and regulations, a joint inventor(s) (designer(s)) will transfer his/her share in the right to file an intellectual property application for the Invention, etc. to his/her employer, etc.
- I will cooperate with the Company in performing the procedures for acquiring a patent, etc. in Japan and foreign countries.
- The inventor's contribution rate for the Invention, etc. shall be determined as specified above.

Internal Inventor(s) (Every Sealer)	November 27, 2018: Taichi IGARASHI	November 27, 2018: Shogo ITAI
	November 27, 2018: Tadaomi DAIBOU	November 27, 2018: Toshiyuki ENDA
	November 27, 2018: Junichi ITO	
	November 27, 2018: Tadashi KAI	

発明考案届出書

届出日 2018/11/14

提案管理番号/出願管理番号
提案整理番号

B0089B18

提案部課 (D技C) [D一開] (一DG)

発明の名称

磁気抵抗素子

発明の概要

セレクトラとMTJからなる積層型磁気メモリにおいて、電圧印加によるセレクトラの発熱は素子の特性劣化を引き起こす恐れがある。そこで電圧印加時の発熱を抑えるための放熱層を加える。今回の提案では放熱層として熱伝導度の高い絶縁体を用いることで、効率的に熱を逃がすことができる。

社内発明者 (6名)

従業員番号	氏名	カンパニー	事業場	所属	発明者寄与率
16003010	五十嵐 太一	T M C	(R D C)	(D技C) [D一開] (一DG)	30.00%
06315510	大坊 忠臣	T M C	(R D C)	(D技C) [D一開] (一DG)	20.00%
16610586	伊藤 順一	T M C	(R D C)	(D技C) [D一開] (一DG)	20.00%
00252210	甲斐 正	T M C	(R D C)	(D技C) [D一開] (一DG)	15.00%
13002510	板井 翔吾	T M C	(M C)	(D技C) [T C開] (T C三)	10.00%
92030310	遠田 利之	T M C	(M C)	(D技C) [T C開]	5.00%

同意事項

- 私(発明者・考案者)は、以下の事項に同意いたします。
- この発明等が職務発明等であること
 - 職務発明等に係る出願権が発明者等に帰属する国内法を有する国において、この発明等が完成した時点でその出願権を会社が承継すること
 - 職務発明等に係る出願権が発明者等に帰属する国内法を有する国において、共同発明者(考案者)が、この発明等の出願権に係る自己の持分をその使用者等に譲渡すること
 - 日本および外国において、会社が行う特許等の取得のための手続きに協力すること
 - この発明等の発明者寄与率に関し、上記の通り決定すること

社内発明者 (全員捺印)	2018/11/27:五十嵐 太一 2018/11/27:板井 翔吾 2018/11/27:大坊 忠臣 2018/11/27:遠田 利之 2018/11/27:伊藤 順一 2018/11/27:甲斐 正
-----------------	--