507191148 03/22/2022

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT7237998

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
PULKIT SHAH	09/13/2019

RECEIVING PARTY DATA

Name:	Cypress Semiconductor Corporation	
Street Address:	198 Champion Court	
City:	San Jose	
State/Country:	CALIFORNIA	
Postal Code:	95134	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16987023

CORRESPONDENCE DATA

Fax Number: (408)545-6911

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 408-943-6878

Email: Adnan.Fazlic@infineon.com, patents@cypress.com
Correspondent Name: CYPRESS SEMICONDUCTOR CORPORATION

Address Line 1: 198 CHAMPION COURT

Address Line 4: SAN JOSE, CALIFORNIA 95134

ATTORNEY DOCKET NUMBER:	TTORNEY DOCKET NUMBER: CD19027C1	
NAME OF SUBMITTER: STOYCHO D. DRAGANOFF		
SIGNATURE:	/StoychoDDraganoff#56181/	
DATE SIGNED: 03/22/2022		

Total Attachments: 2

source=CD19027_Assignment_signed#page1.tif source=CD19027_Assignment_signed#page2.tif

507191148 PATENT REEL: 059343 FRAME: 0300

ASSIGNMENT OF PATENT APPLICATION	Attorney Docket No.: CD19027	
For good and valuable consideration, the receipt an	d sufficiency of which is hereby	
acknowledged, I/we, the undersigned, Pulkit Shah		
hereby sell, assign, and transfer to <u>Cypress Semiconduc</u>	itor Corporation	
a <u>corporation</u> (Type of Assignee: e.g., corporation, company, partnership, university, e	of <u>Delaware</u>	
having a principal place of business at 198 Champion Cou	urt, San Jose, California 95134-1709	
("Assignee"), and its successors, assigns, and legal repress for the United States and all foreign countries, in and to any are disclosed in the application (provisional or non-provision have a declaration executed by the undersigned prior heret	and all inventions or improvements that nail for the United States patent which may	
CONTROLLED GATE-SOURCE VOLTAGE N	-CHANNEL FIELD EFFECT	
TRANSISTOR (NFET) GATE DRIVER said patent application also identified as follows (when known	vn):	
[I/we hereby authorize an attorney or agent for said number and filing date of said patent application wh	Assignee to insert below the application en known.]	
United States Patent Application Number16/572,210	filed 09-16-2019	
and in and to said application (provisional or non-provisional provisional applications, utility applications, design applications applications, continued prosecution applications, continuated applications, renewal applications, reissue applications, reexpatent applications that have been or shall be filed in the Unany of said inventions or improvements; and in and to all or reexamination certificates, and extensions, that have been call foreign countries on said inventions or improvements; and from the filing of said United States application;	ons, divisional applications, continuation on-in-part applications, substitute xaminations, extensions, and all other nited States and all foreign countries on ginal patents, reissued patents, or shall be issued in the United States and	
agree that said Assignee may apply for and receive a patent or patents for said inventions or improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all provisional applications, non-provisional applications, utility applications, design applications, divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications on any and all said inventions or improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said inventions or improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said inventions or improvements and for vesting title to said inventions or improvements, and all applications for patents and all patents on said inventions or improvements, in said Assignee, its successors, assigns, and legal representatives; and		

-1-

PATENT REEL: 059343 FRAME: 0301

Rev. 8/03/2011

 assignment, grant, mortgage, license 	, its successors, assigns, and legal representatives that no e, or other agreement affecting the rights and property herein by the undersigned, and that full right to convey the same as ne undersigned.
Each Inventor: Please	Sign <u>and Date</u> Below:
13 TH	<u>lulich Club</u> Signature
	Pulkit Shah
	Printed/Typed Name
, 20 Date	Signature
	Printed/Typed Name
, 20	Signature
	Printed/Typed Name
	Assignment Document Return Address: CYPRESS SEMICONDUCTOR CORPORATION 198 Champion Court San Jose, CA 95134 (408) 545-7194
	*;

-2-

Rev. 8/03/2011