

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
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EPAS ID: PAT7348607

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
SHUBHENDU SEKHAR MUKHERJEE	09/27/2020
RECEIVING PARTY DATA	
Name:	MARVELL SEMICONDUCTOR, INC.
Street Address:	5488 MARVELL LANE
City:	SANTA CLARA
State/Country:	CALIFORNIA
Postal Code:	95054
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17081074
CORRESPONDENCE DATA	
Fax Number:	(408)222-2755
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	4082222500
Email:	lgalang@marvell.com
Correspondent Name:	KELVIN VIVIAN
Address Line 1:	5488 MARVELL LANE
Address Line 4:	SANTA CLARA, CALIFORNIA 95054
ATTORNEY DOCKET NUMBER:	MP13183
NAME OF SUBMITTER:	KELVIN VIVIAN
SIGNATURE:	/Kelvin Vivian/
DATE SIGNED:	05/25/2022
Total Attachments: 2	
source=MP13183-Assignment_Inventor to Marvell Semiconductor, Inc._EXECUTED#page1.tif	
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ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, the undersigned inventor,

Shubhendu Sekhar Mukherjee

who has created a certain invention for which one or more patent applications identified below (hereinafter called "the Patent Application(s)") that have been, or are to be, filed:

<u>Marvell Reference No.</u>	<u>Application No.</u>	<u>Filing Date</u>	<u>Title</u>
MP13183	Not Yet Assigned	Herewith	PIPELINES FOR SECURE MULTITHREAD EXECUTION
MP13183DE	Not Yet Assigned	Herewith	PIPELINES FOR SECURE MULTITHREAD EXECUTION
MP13183KR	Not Yet Assigned	Herewith	PIPELINES FOR SECURE MULTITHREAD EXECUTION
MP13183PR	62/944,243	December 5, 2019	INSTRUCTION SCHEDULING ALGORITHM TO PREVENT SIDE-CHANNEL ATTACK IN A SIMULTANEOUS MULTITHREADING (SMT) PROCESSOR

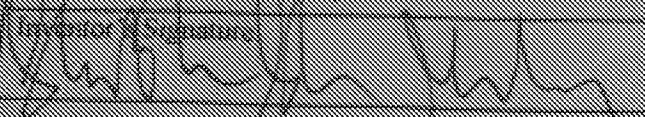
Does hereby sell, assign and transfer Marvell Semiconductor, Inc., a corporation of California, having a place of business at 5488 Marvell Lane, Santa Clara, CA 95054, its successors, assigns, and legal representatives, the full and exclusive right to said invention and said Patent Application(s) and to any and all inventions described in said Patent Application(s), including applications for Letters Patent for the United States, its territorial possessions, and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be granted therefor in the United States, its territorial possessions, and all foreign countries; to any and all Letters Patent which may be entitled to the benefit thereof in the United States, its territorial possessions, and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions, and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

Authorizes and requests that the attorneys of record in any of said Patent Application(s) insert in this assignment the filing date(s) and application number(s) of said Patent Application(s) when officially known;

Agrees that Marvell Semiconductor, Inc., hereinafter referred to as Assignee, may apply for and receive Letters Patent for said invention and said inventions, hereinafter referred to as said invention, in its own name, in the United States, its territorial possessions, and all foreign countries; and that, when requested to carry out in good faith the intent and purpose of this assignment, at the expense of said Assignee, its successors, assigns and legal representatives, the undersigned inventors will execute all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, execute all rightful oaths, assignments, powers of attorney and other papers, testify in any legal or quasi legal proceedings; communicate to said Assignee, its successors, assigns or legal representatives all facts known to the undersigned inventors relating to said invention and the history thereof; and generally do everything possible which said Assignee, its successors, assigns, or legal representatives shall consider desirable for aiding in securing, maintaining and enforcing proper patent protection for said invention and for vesting title to said invention and all applications for patents on said invention in said Assignee, its successors, assigns, or legal representatives; and

Covenants with said Assignee, its successors, assigns, or legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned inventors, and that full right to convey the same as herein expressed is possessed by the undersigned inventors.

IN TESTIMONY WHEREOF we have hereunto set our signatures on the date(s) indicated below:

Full Name of First Inventor	
Shubhendu Bhatnagar	
Inventor's Signature	Date: Month/Day/Year
	9/27/20

Assignee's undersigned representative is empowered to sign this certificate on behalf of Assignee. Assignee's undersigned representative Authorizes and requests that the attorneys of record in any of said Patent Application(s) insert in this assignment the filing date(s) and application number(s) of said Patent Application(s) when officially known.

Accepted on behalf of

MARVELL SEMICONDUCTOR, INC.

Kelvin Vivian

Digitally signed by Kelvin Vivian
DN: cn=Kelvin Vivian, o=Marvell,
ou=MSI,
email=kvivian@marvell.com, c=US
Date: 2020.10.06 15:37:29 -0700

By: _____

Name: Kelvin Vivian

Title: Director, Assistant General Counsel, IP Development

Date: 06 October 2020