507301926 05/25/2022

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
SHUBHENDU SEKHAR MUKHERJEE	05/01/2020

RECEIVING PARTY DATA

Name:	MARVELL SEMICONDUCTOR, INC.	
Street Address:	5488 MARVELL LANE	
City:	SANTA CLARA	
State/Country:	CALIFORNIA	
Postal Code:	95054	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	16863290

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ATTORNEY DOCKET NUMBER:	MP13107	
NAME OF SUBMITTER:	KELVIN VIVIAN	
SIGNATURE:	/Kelvin Vivian/	
DATE SIGNED:	05/25/2022	

Total Attachments: 2

source=MP13107-Assignment_Inventor to MSI_EXECUTED#page1.tif source=MP13107-Assignment_Inventor to MSI_EXECUTED#page2.tif

PATENT 507301926 REEL: 060010 FRAME: 0826

Docket#: MP13107 **PATENT**

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, the undersigned inventor,

Shubhendu Sekhar Mukherjee

who has created a certain invention for which one or more patent applications identified below (hereinafter called "the Patent Application(s)") that have been, or are to be, filed:

Marvell Reference No.	Application No.	Filing Date	<u>Title</u>
MP13107	16/863,290	April 30, 2020	MANAGING PREFETCH LOOKAHEAD DISTANCE BASED ON MEMORY ACCESS LATENCY
MP13107PR	62/876,468	July 19, 2019	USING BACKPRESSURE LATENCY TO ADJUST PREFETCH LOOKUP DISTANCE IN OUT-OF-ORDER PROCESSORS

Does hereby sell, assign and transfer to Marvell Semiconductor, Inc., a corporation of California, having a place of business at 5488 Marvell Lane, Santa Clara, CA 95054, its successors, assigns, and legal representatives, the full and exclusive right to said invention and said Patent Application(s) and to any and all inventions described in said Patent Application(s), including applications for Letters Patent for the United States, its territorial possessions, and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be granted therefor in the United States, its territorial possessions, and all foreign countries; to any and all Letters Patent which may be entitled to the benefit thereof in the United States, its territorial possessions, and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions, and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties:

Authorizes and requests that the attorneys of record in any of said Patent Application(s) insert in this assignment the filing date(s) and application number(s) of said Patent Application(s) when officially known;

Agrees that Marvell Semiconductor, Inc., hereinafter referred to as Assignee, may apply for and receive Letters Patent for said invention and said inventions, hereinafter referred to as said invention, in its own name, in the United States, its territorial possessions, and all foreign countries; and that, when requested to carry out in good faith the intent and purpose of this assignment, at the expense of said Assignee, its successors, assigns and legal representatives, the undersigned inventors will execute all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, execute all rightful oaths, assignments, powers of attorney and other papers, testify in any legal or quasi legal proceedings; communicate to said Assignee, its successors, assigns or legal representatives all facts known to the undersigned inventors relating to said invention and the history thereof; and generally do everything possible which said Assignee, its successors, assigns, or legal representatives shall consider desirable for aiding in securing, maintaining and enforcing proper patent protection for said invention and for vesting title to said invention and all applications for patents on said invention in said Assignee, its successors, assigns, or legal representatives; and

Covenants with said Assignee, its successors, assigns, or legal representatives that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned inventors, and that full right to convey the same as herein expressed is possessed by the undersigned inventors.

IN TESTIMONY WHEREOF I have hereunto set my signature on the date(s) indicated below.

PATENT REEL: 060010 FRAME: 0827 Docket#: MP13107 **PATENT**

Full Name of First Inventor:	
Shubhendu Sekhar Mukherjee	
Inventor's Signature:	Date: Month/Day/Year
/ Shubhendu S. Mukherjee /	5/1/2020

Assignees' undersigned representative is empowered to sign this certificate on behalf of Assignee. Assignees' undersigned representative Authorizes and requests that the attorneys of record in any of said Patent Application(s) insert in this assignment the filing date(s) and application number(s) of said Patent Application(s) when officially known.

Accepted on behalf of:

RECORDED: 05/25/2022

MARVELL SEMICONDUCTOR, INC.

Kelvin Vivian By: Name: Kelvin Vivian Director, Assistant General Counsel, IP Development Title:

May 4, 2020 Date:

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