

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

EPAS ID: PAT7368351

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
VERISITI LLC	06/06/2022
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	VST HOLDING CORPORATION
<b>Street Address:</b>	3010 LEE AVENUE
<b>Internal Address:</b>	ATTN: LEGAL
<b>City:</b>	SANFORD
<b>State/Country:</b>	NORTH CAROLINA
<b>Postal Code:</b>	27332
<b>PROPERTY NUMBERS Total: 11</b>	
<b>Property Type</b>	<b>Number</b>
Patent Number:	9287879
Patent Number:	9218511
Patent Number:	8975748
Patent Number:	9437555
Patent Number:	9466576
Patent Number:	9972398
Patent Number:	10037950
Patent Number:	9972585
Patent Number:	10453807
Patent Number:	10903175
Application Number:	17153420
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(919)869-2804
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Email:</b>	katieb@scc-inc.com
<b>Correspondent Name:</b>	CATHERINE BRIDGES
<b>Address Line 1:</b>	3010 LEE AVENUE
<b>Address Line 2:</b>	ATTN: LEGAL
<b>Address Line 4:</b>	SANFORD, NORTH CAROLINA 27332

<b>NAME OF SUBMITTER:</b>	WILLIAM L LONDON
<b>SIGNATURE:</b>	/William L London/
<b>DATE SIGNED:</b>	06/07/2022
<b>Total Attachments: 13</b> source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page1.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page2.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page3.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page4.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page5.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page6.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page7.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page8.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page9.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page10.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page11.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page12.tif source=Verisiti Dissolution and Assignment of Patent 6.6.2022 for submission to EPAS#page13.tif	

## DISSOLUTION AND ASSIGNMENT OF PATENTS

The parties to this Dissolution and Assignment of Patents ("Dissolution Agreement") are Verisiti LLC, a North Carolina limited liability company ("Verisiti"), and its members, VST Holding Corporation, a North Carolina corporation ("VST") and Ninestar Image Tech Limited, a company organized under the laws of Hong Kong ("Ninestar"), the board of directors of Verisiti (the "Board") and the members of Verisiti (the "Members") (collectively, the "Parties").

### 1. Background of Dissolution Agreement

- a. Verisiti's current operating agreement is the amended and restated operating agreement dated October 31, 2018 ("the Operating Agreement"). The only members of Verisiti as of October 31, 2018, were VST and Ninestar.
- b. The Operating Agreement allows for the admission of new members to Verisiti, but no new members were added.
- c. The Operating Agreement allows for amendments, but no amendments were made.
- d. The only assets of Verisiti are certain patents and patent applications, a list of which attached to this Dissolution Agreement as Exhibit A.
- e. The Parties are not aware of any liabilities of Verisiti.
- f. The expenses of maintaining the patents and patent applications and the continued existence of Verisiti are a burden. Therefore, the members of Verisiti wish to dissolve Verisiti and assign its patents and patent applications.

### 2. Assignment of Patents and Patent Applications

- a. The patents and patent applications listed on Exhibit A are assigned to VST in exchange for the agreements set forth herein, including the releases set forth below (the "Assignment"). The Parties are of the opinion that the value of these patents and patent applications are speculative. Neither Verisiti or Ninestar make any representations or warranties concerning these patents. Neither Verisiti or Ninestar claim any right, title, or interest in these patents and patent applications after this Assignment is made.
- b. The Parties agree that VST may file a short form of this Assignment in any relevant office, similar to the document attached as exhibit B, and that they will execute any documents necessary to file documents similar to exhibit B in order to effectuate this Assignment.

### 3. Dissolution of Verisiti

- a. Pursuant to paragraphs 8.10 and 15.1 (a) of the Operating Agreement, the Members of Verisiti and the Board of Verisiti have unanimously voted to dissolve Verisiti.
- b. The business of Verisiti shall be discontinued, and its affairs wound up pursuant to paragraph 15.2 of the Operating Agreement.
- c. To the best of the Parties' knowledge, there are no assets or liabilities of Verisiti other than the patents and patent applications transferred above and that pursuant to paragraph 15.3 (a) of the Operating Agreement those patents and applications have a value of zero dollars in light of the expenses necessary to maintain them.

- d. The Parties have no ongoing obligations to each other under the Operating Agreement other than those necessary to effectuate this Dissolution Agreement.

4. Release

- a. VST releases and discharges Verisiti and Ninestar, and any of their Members, Boards of Directors, management, shareholders, employees, or agents of and from any and all known or unknown claims arising out of the administration and operation of Verisiti from Verisiti's inception to the date of this Agreement except for those obligations referenced in this Assignment.
- b. Verisiti releases and discharges VST and Ninestar, and any of their Members, Boards of Directors, management, shareholders, employees, or agents of and from any and all known or unknown claims arising out of the administration and operation of Verisiti from Verisiti's inception until the date of this Agreement except for those obligations referenced in this Agreement.
- c. Ninestar releases and discharges VST and Verisiti and any of their members, Board of Directors, management, shareholders, employees, or agents of and from any and all known or unknown claims arising out of the administration and operation of Verisiti from Verisiti's inception until the effective date of this Dissolution Agreement.

5. Miscellaneous

- a. This Dissolution Agreement is governed by the laws of North Carolina.
- b. Elizabeth McKee is authorized to file the necessary documents with the Secretary of State of North Carolina to effectuate this Dissolution Agreement.
- c. The Parties agree that this Dissolution Agreement may be executed by electronic means such as, and including, through email or via DocuSign or similar electronic signature software.
- d. This Dissolution Agreement may be executed in counterparts, each of which when executed shall constitute an original and all counterparts together shall constitute one and the same instrument.

[signatures on the following page]

IN WITNESS WHEREOF, the Parties have agreed to the foregoing Dissolution Agreement. This Dissolution Agreement is effective as of the date of the last signature affixed hereto.

Ninestar Image Tech Limited, by its \_\_\_\_\_

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date

VST Holding Corporation, by its President, William London

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Signature

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William London  
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May 27, 2022

\_\_\_\_\_  
Date

Versiti, LLC, by its President and Board Member Yangyun Zeng

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Signature

\_\_\_\_\_  
Date

Versiti, LLC, by its Board Member Wei Yan

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Signature

\_\_\_\_\_  
Date

Versiti, LLC, by its Board Member Dongjie Wang

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date

Versiti, LLC, by its Board Member Li Ding

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date

Versiti, LLC, by its Board Member, Secretary, and Treasurer William Swartz

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Signature

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William Swartz  
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May 27, 2022

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Date

Versiti, LLC, by its Board Member Michael Swartz

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Signature

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Michael Swartz  
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May 27, 2022

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Date

Versiti, LLC, by its Board Member William London

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Signature


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William London  
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May 27, 2022

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Date

IN WITNESS WHEREOF, the Parties have agreed to the foregoing Dissolution Agreement. This Dissolution Agreement is effective as of the date of the last signature affixed hereto.

Ninestar Image Tech Limited, by its Director, Rusong Lv

Signature 

June 6, 2022  
Date

VST Holding Corporation, by its President, William London

Signature

Date

Versiti, LLC, by its President and Board Member Yangyun Zeng

Signature

Date

Versiti, LLC, by its Board Member Wei Yan

Signature

Date

Versiti, LLC, by its Board Member Dongjie Wang

Signature

Date

Versiti, LLC, by its Board Member Li Ding

Signature

Date

Versiti, LLC, by its Board Member, Secretary, and Treasurer William Swartz

Signature

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Versiti, LLC, by its Board Member Michael Swartz

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Versiti, LLC, by its Board Member William London

Signature

Date

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Signature Date

VST Holding Corporation, by its President, William London

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Signature Date

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Versiti, LLC, by its Board Member William London

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Signature Date

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Ninestar Image Tech Limited, by its \_\_\_\_\_

\_\_\_\_\_  
Signature Date

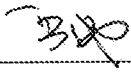
VST Holding Corporation, by its President, William London

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Signature Date

Versiti, LLC, by its President and Board Member Yangyun Zeng

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Signature Date

Versiti, LLC, by its Board Member Wei Yan

  
\_\_\_\_\_  
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Versiti, LLC, by its Board Member William London

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Signature Date



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Signature

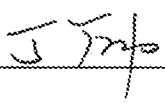
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Signature

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Date

Versiti, LLC, by its Board Member William London

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Signature

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Date

## EXHIBIT A

File #	Title	Application #	Date Filed	Publication #	Date Published	Patent #	Date Issued
IMG.153	Secure Semiconductor Device Having Features to Prevent Reverse Engineering	13/663,921	10/30/12	2013/0154687	06/20/13	9,287,879	03/15/16
IMG.158	Secure Semiconductor Device Having Features to Prevent Reverse Engineering	13/739,463	01/11/13	2014/0198554	07/17/14	9,218,511	12/22/15
SSL.001	Secure Semiconductor Device Having Features to Prevent Reverse Engineering	14/036,827	09/25/13	2015/0084195	03/26/15	8,975,748	03/10/15
SSL.003	Secure Semiconductor Device Having Features to Prevent Reverse Engineering	14/504,868	10/02/14	2016/0099219	04/07/16	9437555	09/06/16
SSL.004	Secure Semiconductor Device Having Features to Prevent Reverse Engineering	14/638,199	03/04/15	2015/0311167	10/29/15	9466576	10/11/16
SSL.007	Secure Semiconductor Device Having Features to Prevent Reverse Engineering	14/856,919	09/17/15	2016/0005485	01/07/16	9,972,398	05/15/18
SSL.010	Semiconductor Device Having Features to Prevent Reverse Engineering	15/251,560	08/30/16	2017/0092599	03/30/17	10,037,950	07/31/18
SSL.011	Semiconductor Device Having Features to Prevent Reverse Engineering	15/287,288	10/06/16	2017/0117234	04/27/17	9,972,585	05/15/18
SSL.012	Semiconductor Device Having Features to Prevent Reverse Engineering	15/976,077	05/10/18	2018/0261558	09/13/18	10,453,807	10/22/19
SSL.013	Semiconductor Device Having Features to Prevent Reverse Engineering	16/599,633	10/11/19	2020/0058602	02/20/20	10,903,175	01/26/21
SSL.014	Semiconductor Device Having Features to Prevent Reverse Engineering	17/153,420	01/20/21	2021/0242143	08/05/21	Pending	Pending

## Verisiti Active Cases

Reference #	Client Reference	Title	Country ID	Serial #	Filed Date	Patent #	Issue Date	Status	Expiration Date
041484-000009	IMG.153CN	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	CN	201280076808.7	12/14/2012	ZL201280076808.7	2/14/2020	ISSUED	12/14/2032
041484-000009DIV	IMG.153CNDIV	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	CN	201910962117.0	12/14/2012			PUBLISHED	12/14/2032
041484-000011	IMG.153EP	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	EP	12887826.1	12/14/2012	2915257	7/1/2020	ISSUED	12/14/2032
041484-000011DE	IMG.153EPDE	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	DE	12887826.1	12/14/2012	2915257	7/1/2020	ISSUED	12/14/2032
041484-000011FR	IMG.153EPFR	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	FR	12887826.1	12/14/2012	2915257	7/1/2020	ISSUED	12/14/2032
041484-000011GB	IMG.153EPGB	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	GB	12887826.1	12/14/2012	2915257	7/1/2020	ISSUED	12/14/2032
041484-000013	IMG.153MX	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	MX	MX/s/2015/053399	12/14/2012	344765	12/1/2016	ISSUED	12/14/2032
041484-000017	IMG.155CN	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	CN	201480004534.X	1/3/2014	ZL201480004534.X	12/7/2018	ISSUED	1/3/2034
041484-000019	IMG.155EP	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	EP	14737723.8	1/3/2014			PUBLISHED	1/3/2034
041484-000021	IMG.155MX	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	MX	MX/s/2015/008948	1/3/2014	348002	1/5/2017	ISSUED	1/3/2034
041484-000033	IMG.158CN	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	CN	201480004515.7	1/7/2014	ZL201480004515.7	3/2/2018	ISSUED	1/7/2034
041484-000035	IMG.158EP	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	EP	14738091.9	1/7/2014	2943981	9/6/2017	ISSUED	1/7/2034
041484-000035DE	IMG.158DE	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	DE	14738091.9	1/7/2014	2943981	9/6/2017	ISSUED	1/7/2034
041484-000035FR	IMG.158FR	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	FR	14738091.9	1/7/2014	2943981	9/6/2017	ISSUED	1/7/2034
041484-000035GB	IMG.158GB	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	GB	14738091.9	1/7/2014	2943981	9/6/2017	ISSUED	1/7/2034
041484-000037	IMG.158MX	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	MX	MX/s/2015/008949	1/7/2014	344039	12/1/2016	ISSUED	1/7/2034
041484-000054	IMG.155MXDIV	SEMICONDUCTOR DEVICE HAVING FEATURES TO PREVENT REVERSE ENGINEERING	MX	MX/s/2016/017344	1/3/2014	-	9/8/2020	ISSUED	1/3/2034

**EXHIBIT B**U.S. DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office**RECORDATION FORM COVER SHEET  
PATENTS ONLY**

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

<b>1. Name of conveying party(ies)</b>  Additional name(s) of conveying party(ies) attached? <input type="checkbox"/> Yes <input type="checkbox"/> No	<b>2. Name and address of receiving party(ies)</b> Name: _____ Internal Address: _____ _____ Street Address: _____ _____ City: _____ State: _____ Country: _____ Zip: _____ Additional name(s) & address(es) attached? <input type="checkbox"/> Yes <input type="checkbox"/> No
<b>3. Nature of conveyance/Execution Date(s):</b> Execution Date(s) _____ <input type="checkbox"/> Assignment <input type="checkbox"/> Merger <input type="checkbox"/> Security Agreement <input type="checkbox"/> Change of Name <input type="checkbox"/> Joint Research Agreement <input type="checkbox"/> Government Interest Assignment <input type="checkbox"/> Executive Order 9424, Confirmatory License <input type="checkbox"/> Other _____	<b>4. Application or patent number(s):</b> <input type="checkbox"/> This document serves as an Oath/Declaration (37 CFR 1.63). A. Patent Application No.(s) _____ B. Patent No.(s) _____  Additional numbers attached? <input type="checkbox"/> Yes <input type="checkbox"/> No
<b>5. Name and address to whom correspondence concerning document should be mailed:</b> Name: _____ Internal Address: _____ _____ Street Address: _____ _____ City: _____ State: _____ Zip: _____ Phone Number: _____ Docket Number: _____ Email Address: _____	<b>6. Total number of applications and patents involved:</b> _____ <b>7. Total fee (37 CFR 1.21(h) &amp; 3.41) \$</b> _____ <input type="checkbox"/> Authorized to be charged to deposit account <input type="checkbox"/> Enclosed <input type="checkbox"/> None required (government interest not affecting title) <b>8. Payment Information</b>  Deposit Account Number _____ Authorized User Name _____
<b>9. Signature:</b> <div style="display: flex; justify-content: space-between; margin-top: 10px;"> <div style="width: 60%;">           _____            Signature         </div> <div style="width: 30%;">           _____            Date         </div> </div> <div style="display: flex; justify-content: space-between; margin-top: 10px;"> <div style="width: 60%;">           _____            Name of Person Signing         </div> <div style="width: 30%;">           Total number of pages including cover sheet, attachments, and documents: <span style="border: 1px solid black; display: inline-block; width: 40px; height: 20px; vertical-align: middle;"></span> </div> </div> <p style="font-size: small; text-align: center; margin-top: 10px;">Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to: Mail Stop Assignment Recordation Services, Director of the USPTO, P.O.Box 1450, Alexandria, V.A. 22313-1450</p>	

**EXHIBIT B****Guidelines for Completing Patents Cover Sheets  
(PTO-1595)**

Cover Sheet information must be submitted with each document to be recorded. If the document to be recorded concerns both patents and trademarks separate patent and trademark cover sheets, including any attached pages for continuing information, must accompany the document. All pages of the cover sheet should be numbered consecutively, for example, if both a patent and trademark cover sheet is used, and information is continued on one additional page for both patents and trademarks, the pages of the cover sheet would be numbered from 1 to 4.

**Item 1. Name of Conveying Party(ies).**

Enter the full name of the party(ies) conveying the interest. If there is insufficient space, enter a check mark in the "Yes" box to indicate that additional information is attached. The name of the additional conveying party(ies) should be placed on an attached page clearly identified as a continuation of the information Item 1. Enter a check mark in the "No" box, if no information is contained on an attached page. If the document to be recorded is a joint research agreement, enter the name(s) of the party(ies) other than the owner of the patent or patent application as the conveying party(ies).

**Item 2. Name and Address of Receiving Party(ies).**

Enter the name and full address of the first party receiving the interest. If there is more than one party receiving the interest, enter a check mark in the "Yes" box to indicate that additional information is attached. Enter a check mark in the "No" box, if no information is contained on an attached page. If the document to be recorded is a joint research agreement, enter the name(s) of the patent or patent application owner(s) as the receiving party.

**Item 3. Nature of Conveyance/Execution Date(s).**

Enter the execution date(s) of the document. It is preferable to use the name of the month, or an abbreviation of that name, in order that confusion over dates is minimized. Place a check mark in the appropriate box describing the nature of the conveying document. If the "Other" box is checked, specify the nature of the conveyance.

**Item 4. Application Number(s) or Patent Number(s).**

Indicate the application number(s), and/or patent number(s) against which the document is to be recorded. National application numbers must include both the series code and a six-digit number (e.g., 07/123,456), and international application numbers must be complete (e.g., PCT/US91/12345).

Enter a check mark in the appropriate box: "Yes" or "No" if additional numbers appear on attached pages. Be sure to identify numbers included on attached pages as the continuation of Item 4. Also enter a check mark if this Assignment is being filed as an Oath/Declaration ( 37 CFR 1.63).

**Item 5. Name and Address of Party to whom correspondence concerning the document should be mailed.**

Enter the name and full address of the party to whom correspondence is to be mailed.

**Item 6. Total Applications and Patents involved.**

Enter the total number of applications and patents identified for recordation. Be sure to include all applications and patents identified on the cover sheet and on additional pages.

**Block 7. Total Fee Enclosed.**

Enter the total fee enclosed or authorized to be charged. A fee is required for each application and patent against which the document is recorded.

**Item 8. Payment Information.**

Enter the deposit account number and authorized user name to authorize charges.

**Item 9. Signature.**

Enter the name of the person submitting the document. The submitter must sign and date the cover sheet. Enter the total number of pages including the cover sheet, attachments, and document.

This collection of information is required by 35 USC 261 and 262 and 15 USC 1057 and 1060. The information is used by the public to submit (and by the USPTO to process) patent and trademark assignment requests. After the USPTO records the information, the records for patent and trademarks, assignments, and other associated documents can be inspected by the public. To view documents recorded under secrecy orders or documents recorded due to the interest of the federal government, a written authorization must be submitted. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the form to the USPTO. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Manager of the Assignment Division, USPTO, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop Assignment Recordation Services, Director of the USPTO, P.O. Box 1450, Alexandria, VA 22313-1450.

**EXHIBIT B****Privacy Act Statement for Patent Assignment Recordation Form Cover Sheet**

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with the above request for information. This collection of information is authorized by 35 U.S.C. 1, 2, 261 and E.O. 9424. This information will primarily be used by the USPTO for the recordation of assignments related to patents and patent applications. Submission of this information is voluntary but is required in order for the USPTO to record the requested assignment. If you do not provide the information required on the cover sheet, the assignment will not be recorded, and all documents will be returned to you.

After the information is recorded, the records and associated documents can be inspected by the public and are not confidential, except for documents that are sealed under secrecy orders or related to unpublished patent applications. Assignment records relating to unpublished patent applications are maintained in confidence in accordance with 35 U.S.C. 122. Records open to the public are searched by users for the purpose of determining ownership for other property rights with respect to patents and trademarks.

Routine uses of the information you provide may also include disclosure to appropriate Federal, state, local, or foreign agencies in support of their enforcement duties and statutory or regulatory missions, including investigating potential violations of law or contract and awarding contracts or other benefits; to a court, magistrate, or administrative tribunal in the course of presenting evidence; to members of Congress responding to requests for assistance from their constituents; to the Office of Management and Budget in connection with the review of private relief legislation; to the Department of Justice in connection with a Freedom of Information Act request; to a contractor in the performance of their duties; to the Office of Personnel Management for personnel studies; and to the General Services Administration (GSA) as part of their records management responsibilities under the authority of 44 U.S.C. 2904 and 2906. Such disclosure to GSA shall not be used to make determinations about individuals.