

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
RUNZI CHANG	07/27/2020
HUAHUNG KAO	07/27/2020
RECEIVING PARTY DATA	
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Postal Code:	95054
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	16940727
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NAME OF SUBMITTER:	KELVIN VIVIAN
SIGNATURE:	/Kelvin Vivian/
DATE SIGNED:	07/07/2022
Total Attachments: 3	
source=MP13101-Assignment_Inventors to Marvell Semiconductor, Inc._(EXECUTED)#page1.tif	
source=MP13101-Assignment_Inventors to Marvell Semiconductor, Inc._(EXECUTED)#page2.tif	
source=MP13101-Assignment_Inventors to Marvell Semiconductor, Inc._(EXECUTED)#page3.tif	

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, the undersigned inventor,

Runzi Chang

Huahung Kao

who has created a certain invention for which one or more patent applications identified below (hereinafter called "the Patent Application(s)") that have been, or are to be, filed:

<u>Marvell Reference No.</u>	<u>Application No.</u>	<u>Filing Date</u>	<u>Title</u>
MP13101	To Be Assigned	To Be Assigned	DECOUPLING CAPACITOR INTEGRATED IN SYSTEM ON CHIP (SOC) DEVICE
MP13101PR	62/879,789	July 29, 2019	ON-CHIP HIGH-DENSITY CAPACITOR, STRUCTURES AND METHOD OF MAKING THE SAME

Does hereby sell, assign and transfer to Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, CA 95054, USA, its successors, assigns, and legal representatives, the full and exclusive right to said invention and said Patent Application(s) and to any and all inventions described in said Patent Application(s), including applications for Letters Patent for the United States, its territorial possessions, and all foreign countries, and the entire right, title and interest in and to any and all Letters Patent which may be granted therefor in the United States, its territorial possessions, and all foreign countries; to any and all Letters Patent which may be entitled to the benefit thereof in the United States, its territorial possessions, and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for Letters Patent relating thereto which have been or shall be filed in the United States, its territorial possessions, and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties;

Authorizes and requests that the attorneys of record in any of said Patent Application(s) insert in this assignment the filing date(s) and application number(s) of said Patent Application(s) when officially known;

Agrees that Marvell Semiconductor, Inc., hereinafter referred to as Assignee, may apply for and receive Letters Patent for said invention and said inventions, hereinafter referred to as said invention, in its own name, in the United States, its territorial possessions, and all foreign countries; and that, when requested to carry out in good faith the intent and purpose of this assignment, at the expense of said Assignee, its successors, assigns and legal representatives, the undersigned inventor will execute all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, execute all rightful oaths, assignments, powers of attorney and other papers, testify in any legal or quasi legal proceedings; communicate to said Assignee, its successors, assigns or legal representatives all facts known to the undersigned inventor relating to said invention and the history thereof; and generally do everything possible which said Assignee, its successors, assigns, or legal representatives shall consider desirable for aiding in securing, maintaining and enforcing proper patent protection for said invention and for vesting title to said invention and all applications for patents on said invention in said Assignee, its successors, assigns, or legal representatives; and

Covenants with said Assignee, its successors, assigns, or legal representatives that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned inventor, and that full right to convey the same as herein expressed is possessed by the undersigned inventor.

IN TESTIMONY WHEREOF we have hereunto set our signatures on the date(s) indicated below.

First Inventor: Runzi Chang	
Inventor's Signature: <i>/Runzi Chang/</i>	Date: Month/Day/Year <i>7/27/2020</i>

Second Inventor: Huahung Kao	
Inventor's Signature:	Date: Month/Day/Year

Assignees' undersigned representative is empowered to sign this certificate on behalf of Assignee. Assignees' undersigned representative Authorizes and requests that the attorneys of record in any of said Patent Application(s) insert in this assignment the filing date(s) and application number(s) of said Patent Application(s) when officially known.

Accepted on behalf of:

MARVELL SEMICONDUCTOR, INC.

By: Kelvin Vivian
Digitally signed by Kelvin Vivian
DN: cn=Kelvin Vivian, o=Marvell, ou=MSI,
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Date: 2020.07.28 00:07:46 -07'00'

Name: Kelvin Vivian

Title: Director, Assistant General Counsel, IP Development

Date: 28 July 2020

IN TESTIMONY WHEREOF we have hereunto set our signatures on the date(s) indicated below.

First Inventor: Runzi Chang	
Inventor's Signature:	Date: Month/Day/Year

Second Inventor: Huahung Kao	
Inventor's Signature: /Huahung Kao/	Date: Month/Day/Year 7/27/2020

Assignees' undersigned representative is empowered to sign this certificate on behalf of Assignee. Assignees' undersigned representative Authorizes and requests that the attorneys of record in any of said Patent Application(s) insert in this assignment the filing date(s) and application number(s) of said Patent Application(s) when officially known.

Accepted on behalf of:

MARVELL SEMICONDUCTOR, INC.

By: **Kelvin Vivian**
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Title: Director, Assistant General Counsel, IP Development

Date: 28 July 2020