

## PATENT ASSIGNMENT COVER SHEET

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Stylesheet Version v1.2

EPAS ID: PAT7427926

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
JANAK PATEL	06/23/2021
RICHARD GRAF	06/24/2021
MANISH NAYINI	06/23/2021
NAZMUL HABIB	06/21/2021
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	MARVELL SEMICONDUCTOR, INC.
<b>Street Address:</b>	5488 MARVELL LANE
<b>City:</b>	SANTA CLARA
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	95054
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	17360571
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(408)222-2755
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
<b>Phone:</b>	408-222-2500
<b>Email:</b>	sherir@marvell.com
<b>Correspondent Name:</b>	KELVIN VIVIAN
<b>Address Line 1:</b>	5488 MARVELL LANE
<b>Address Line 4:</b>	SANTA CLARA, CALIFORNIA 95054
<b>ATTORNEY DOCKET NUMBER:</b>	MP13295
<b>NAME OF SUBMITTER:</b>	KELVIN VIVIAN
<b>SIGNATURE:</b>	/Kelvin Vivian/
<b>DATE SIGNED:</b>	07/12/2022
<b>Total Attachments: 8</b>	
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source=MP13295 Executed Assignment INV to MSI (Accepted)#page8.tif

**ASSIGNMENT**

For good and valuable consideration, the receipt and sufficiency for which is hereby acknowledged, I or We, the undersigned,

**Janak Patel, Richard Graf, Manish Nayini, Nazmul Habib**

who have created certain invention for which one or more patent applications identified below (hereinafter "the Patent Application(s)") that have been, or are to be filed:

<u>Marvell Case No.</u>	<u>Application No.</u>	<u>Filing Date</u>	<u>Title</u>
<b>MP13295PR</b>	<b>US 63/045,668</b>	<b>06/29/2020</b>	<b>Stacked Heat Sink Solution for Lidless Multi-Chip Module</b>
<b>MP13295</b>	<b>US 17/360,571</b>	<b>06/28/2021</b>	<b>Heat Sink Configuration for Multi-Chip Module</b>
<b>MP13295WO</b>	<b>PCT/US21/39429</b>	<b>06/28/2021</b>	<b>Heat Sink Configuration for Multi-Chip Module</b>

Do hereby confirm any prior assignment, and to the extent that I have not already done so, agree to sell, assign, and transfer, and do hereby sell, assign and transfer to Marvell Semiconductor, Inc., a corporation of California, having a place of business at 5488 Marvell Lane, Santa Clara, CA 95054, and its successors or assigns (hereinafter "Assignee"), the entire rights, title, and interest, including all priority rights, in or to any and all inventions described in the Patent Application(s), the Patent Application(s), and all continuations in whole or in part, divisions, and other applications claiming priority directly or indirectly from the Patent Application(s) which have been or are to be filed in the United States and/or any foreign countries including national stage applications, and any patents, including utility models and other similar registrations, in the United States and/or any foreign countries which may be granted thereon, including but not limited to reissues, renewals, and reexaminations thereof;

Authorize and request that the attorneys of record in any of the Patent Application(s) insert in this assignment the filing date(s) and application number(s) of the Patent Application(s) when officially known (the Assignee authorizes and requests the same);

Agree that Assignee may apply for and receive patents including utility models and other similar registrations for the foregoing rights, title and interest in its own name, in the United States and/or any foreign countries; and that, when requested to carry out in good faith the intent and purpose of this assignment, at the expense of the Assignee, and its successors or assigns, the undersigned will execute all rightful oaths, declarations, assignments, powers of attorney and other papers, testify in any legal or quasi legal proceedings; communicate to the Assignee, and its successors or assigns all facts known to the undersigned relating to the foregoing rights, title and interest; and generally do everything possible and reasonable which the Assignee, and its successors, or assigns consider necessary, desirable or expedient for aiding in securing, maintaining and enforcing proper protection for applicable patent rights and for vesting applicable title in the Assignee and its successors or assigns; and

Covenant with the Assignee, and its successors or assigns that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to

others by the undersigned, there are no outstanding encumbrances, liens or other obligations or restrictions on the rights, titles and interests herein assigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

IN TESTIMONY WHEREOF I have hereunto set my signature or our signatures on the date indicated below.

Full Name of First Inventor:

Janak Patel

Inventor's Signature:

/Janak Patel/

Date: Month/Day/Year

6/23/2021

Full Name of Second Inventor:

Richard Graf

Inventor's Signature:

Date: Month/Day/Year

Full Name of Third Inventor:

Manish Nayini

Inventor's Signature:

Date: Month/Day/Year

Full Name of Fourth Inventor:

Nazmul Habib


Inventor's Signature:

Date: Month/Day/Year

Accepted by:

MARVELL SEMICONDUCTOR, INC.

By: \_\_\_\_\_



Name: Kelvin M. Vivian

Title: Director, Assistant General Counsel, IP Development

Date: 24 June 2021

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Full Name of First Inventor:

Janak Patel

Inventor's Signature:

Date: Month/Day/Year

Full Name of Second Inventor:

Richard Graf

Inventor's Signature:

Date: Month/Day/Year

/Richard Graf/

06/24/2021

Full Name of Third Inventor:

Manish Nayini

Inventor's Signature:

Date: Month/Day/Year

Full Name of Fourth Inventor:

Nazmul Habib

Inventor's Signature:

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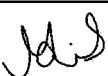
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Full Name of First Inventor: Janak Patel	
Inventor's Signature:	Date: Month/Day/Year


Full Name of Second Inventor: Richard Graf	
Inventor's Signature:	Date: Month/Day/Year

Full Name of Third Inventor: Manish Nayini	
Inventor's Signature: 	Date: 06/23/2021

Full Name of Fourth Inventor: Nazmul Habib	
Inventor's Signature:	Date: Month/Day/Year

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MARVELL SEMICONDUCTOR, INC.

By: 

Name: Kelvin M. Vivian

Title: Director, Assistant General Counsel, IP Development

Date: 24 June 2021



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Inventor's Signature:	Date: Month/Day/Year

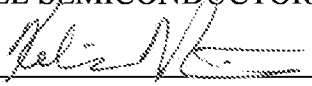
Full Name of Second Inventor: Richard Graf	
Inventor's Signature:	Date: Month/Day/Year

Full Name of Third Inventor: Manish Nayini	
Inventor's Signature:	Date: Month/Day/Year

Full Name of Fourth Inventor: Nazmul Habib	
Inventor's Signature:	Date: Month/Day/Year
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