# PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT7447451

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

#### **CONVEYING PARTY DATA**

Name	Execution Date
Jungsun KIM	02/22/2013
Seungwook PARK	11/12/2012
Jaewon SUNG	02/22/2013
Joonyoung PARK	02/22/2013
Byeongmoon JEON	11/12/2012
Jaehyun LIM	11/12/2012
Yongjoon JEON	11/12/2012
Younghee CHOI	12/14/2012

# **RECEIVING PARTY DATA**

Name:	LG ELECTRONICS INC.
Street Address:	128, YEOUI-DAERO, YEONGDEUNGPO-GU
City:	SEOUL
State/Country:	KOREA, REPUBLIC OF
Postal Code:	07336

### **PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	17827093

### **CORRESPONDENCE DATA**

**Fax Number:** (877)769-7945

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent

using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

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Email: apsi@fr.com
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Address Line 2: P.O.BOX 1022

Address Line 4: MINNEAPOLIS, MINNESOTA 55440-1022

ATTORNEY DOCKET NUMBER: 21613-0081003

NAME OF SUBMITTER: CAROL TYRRELL

SIGNATURE: /Carol Tyrrell/

PATENT 507400524 REEL: 060597 FRAME: 0866

DATE SIGNED:	07/22/2022	
Total Attachments: 6		
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#### **ASSIGNMENT**

For valuable consideration, we, KIM, Jungsun of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, PARK, Seungwook of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, SUNG, Jaewon of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, PARK, Joonyoung of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, JEON, Byeongmoon of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, LIM, Jaehyun of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, JEON, Yongjoon of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seochogu Seoul 137-130 Republic of Korea and CHOI, Younghee of of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, hereby assign to: LG ELECTRONICS INC., a corporation of Republic of Korea having a place of business at: 20, Yeouido-dong, Yeongdeungpo-gu, Seoul, 150-721 Republic of Korea; and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of an application for United States Patent signed by us, entitled METHOD FOR PARTITIONING BLOCK AND DECODING DEVICE, filed on November 5, 2020, and assigned U.S. Serial Number 17/090,519, and we authorize and request the attorneys appointed in said application to hereafter complete this assignment by inserting above the filing date and serial number of said application when known; this assignment including said application, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions or improvements, and the right to claim priority based on the filing date of said application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and we authorize the Assignee to apply in all countries in our name or in its own name for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements; and we agree for ourselves and our respective heirs, legal representatives and assigns, without further compensation to perform such lawful acts

### Attorney's Docket No.:

### BPP2011-0223-US (LG: 11FCVL270PC01)

and to sign such further applications, assignments, Preliminary Statements and other lawful documents as the Assignee may reasonably request to effectuate fully this assignment

	Date:	
KIM, Jungsun		
PARK, Seungwook	Date:	012, 11,12
SUNG, Jaewon	Date:	
PARK, Joonyoung	Date:	
JEON, Byeongmoon	Date:	>0/2.11.12
LIM, Jaehyun	Date: 2	0/2.11.12
JEON, Yongjoon	_ Date:⊃₄	712.11.12
CHOI, Younghee	Date:	· · · · · · · · · · · · · · · · · · ·
Witnessed by:		
Name:	Date:	
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**PATENT** 

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#### ASSIGNMENT

For valuable consideration, we, KIM, Jungsun of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, PARK, Seungwook of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, SUNG, Jaewon of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, PARK, Joonyoung of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, JEON, Byeongmoon of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, LIM, Jaehyun of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, JEON, Yongjoon of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seochogu Seoul 137-130 Republic of Korea and CHOI, Younghee of of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, hereby assign to: LG ELECTRONICS INC., a corporation of Republic of Korea having a place of business at: 20, Yeouido-dong, Yeongdeungpo-gu, Seoul, 150-721 Republic of Korea; and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of an application for United States Patent signed by us, entitled METHOD FOR PARTITIONING BLOCK AND DECODING DEVICE, filed on November 5, 2020, and assigned U.S. Serial Number 17/090,519, and we authorize and request the attorneys appointed in said application to hereafter complete this assignment by inserting above the filing date and serial number of said application when known; this assignment including said application, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions or improvements, and the right to claim priority based on the filing date of said application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and we authorize the Assignee to apply in all countries in our name or in its own name for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements; and we agree for ourselves and our respective heirs, legal representatives and assigns, without further compensation to perform such lawful acts

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18 8 9a		Date: 1	Feb 27	2013
KIM, Jungsun	•	_		
PARK, Seungwook		Date: _		
SUNG, Jaewon	<del> </del>	Date: <u>Ř</u>	Fel 22	2013.
PARK, Joonyoung	· ·	Date:	Feb. 22	. 2013
JEON, Byeongmoon		Date:		
LIM, Jaehyun		Date: _		
JEON, Yongjoon	· · · · · · · · · · · · · · · · · · ·	Date: _		
CHOI, Younghee	·	Date: _		
Witnessed by:				
Name:	· · · · · · · · · · · · · · · · · · ·	Date:	•	
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For valuable consideration, we, KIM, Jungsun of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, PARK, Seungwook of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, SUNG, Jaewon of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, PARK, Joonyoung of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, JEON, Byeongmoon of LG Electronics Inc., Convergence R&D Lab., 221 Yangiae-dong, Seocho-gu Seoul 137-130 Republic of Korea, LIM, Jaehyun of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, JEON, Yongjoon of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seochogu Seoul 137-130 Republic of Korea and CHOI, Younghee of of LG Electronics Inc., Convergence R&D Lab., 221 Yangjae-dong, Seocho-gu Seoul 137-130 Republic of Korea, hereby assign to: LG ELECTRONICS INC., a corporation of Republic of Korea having a place of business at: 20, Yeouido-dong, Yeongdeungpo-gu, Seoul, 150-721 Republic of Korea; and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of an application for United States Patent signed by us, entitled METHOD FOR PARTITIONING BLOCK AND DECODING DEVICE, filed on November 5, 2020, and assigned U.S. Serial Number 17/090.519 , and we authorize and request the attorneys appointed in said application to hereafter complete this assignment by inserting above the filing date and serial number of said application when known; this assignment including said application, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions or improvements, and the right to claim priority based on the filing date of said application under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and we authorize the Assignee to apply in all countries in our name or in its own name for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements; and we agree for ourselves and our respective heirs, legal representatives and assigns, without further compensation to perform such lawful acts

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LIM, Jaehyun		Date: _		
JEON, Yongjoon	· .	Date:		
CHOI, Younghee	· · · · · · · · · · · · · · · · · · ·	Date: .	14th	Dec. 2012
Witnessed by:				
Name:	·······	Date:		
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PATENT REEL: 060597 FRAME: 0873

**RECORDED: 07/22/2022**