

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
JOHN PAUL LESSO	07/01/2019
RECEIVING PARTY DATA	
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17943745
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DATE SIGNED:	09/13/2022
Total Attachments: 2	
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DECLARATION AND ASSIGNMENT

As a below-named inventor (hereinafter "the Inventor"), I hereby declare that I believe that I am the original inventor or joint inventor of an Invention entitled:

DETECTION OF SPEECH

(hereinafter "the Invention") and which is subject to a US Provisional Patent Application (Application Number US 62/854,388, filed 30th May 2019 CIRRUS Ref: P3719USPROV; hereinafter "the Application"). The above-identified application was made or authorised to be made by me. I hereby acknowledge that any wilful false statement made in this Declaration is punishable under 18 U.S.C. §1001 by fine or imprisonment of not more than five (5) years, or both.

WHEREAS,

Cirrus Logic International Semiconductor Ltd., a wholly-owned subsidiary of Cirrus Logic, Inc. organised under and pursuant to the laws of Scotland, and having its principal place of business at 7B Nightingale Way, Edinburgh EH3 9EG, United Kingdom

(hereinafter "the Assignee"), desires to acquire the entire right, title and interest throughout the world in and to said Invention and Application, including any and all divisions and continuations thereof, all rights of priority under the terms of the Paris Convention for the Protection of Industrial Property, and any and all Letters Patent which may be granted thereon, including any and all renewals, reissues and prolongations thereof.

NOW, THEREFORE, in consideration of the sum of one dollar (\$1.00) and other good and valuable consideration, the receipt, adequacy and sufficiency of which are hereby acknowledged, the Inventor hereby confirms the assignment to said Assignee, its assigns and legal representatives, the entire right, title and interest throughout the world, in and to said Invention and Application, including any and all divisions and continuations thereof, all rights of priority under the terms of the Paris Convention for the Protection of Industrial Property, and any and all Letters Patent which may be granted thereon, including any and all renewals, reissues and prolongations thereof, with all the rights, powers, privileges, and advantages in any way arising from or pertaining thereto, for and during the term or terms of any and all such Letters Patent, including the right to recover and take all such proceedings as may be necessary for recovery of damages or otherwise in respect of all infringements of any patent or patents granted pursuant to the Application

TO HAVE HOLD USE AND ENJOY the same unto the Assignee their successors and assigns absolutely, to the intent that the grant of Letters Patent in any country shall be in the name of and shall vest in the Assignee

AND WHEREIN the Inventor further agrees that, when requested, the Inventor will, without charge to Cirrus Logic, Inc. or its subsidiaries, but at its expense, sign all papers, take all rightful oaths, and do all acts which may be necessary, desirable, or convenient for securing and maintaining any and all patent protection (as described above) for the Invention in any and all countries and for respectively vesting title thereto in Cirrus Logic International Semiconductor Ltd., its successors, assigns, and legal representatives or nominees, and wherein to the maximum extent allowed by law, the Inventor hereby irrevocably designates and appoints Cirrus Logic International Semiconductor Ltd. and its duly

authorized officers and agents, or Cirrus Logic International Semiconductor Ltd.'s applicable designee, as the Inventor's agents and attorneys-in-fact to act for and on the Inventor's behalf and instead of the Inventor to sign all papers, take all rightful oaths, and do all acts described in the foregoing sentence should the Inventor refuse or become otherwise unavailable to do so

IN WITNESS WHEREOF, executed by the undersigned

Signed: *John Paul Lesso*

Date: 15th July 2019

Name: John Paul Lesso

Address: c/o Cirrus Logic International Semiconductor Ltd.