

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT7571242

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
PATRICE M. PARRIS	04/29/2019
BERND LIENHARD	04/29/2019
DAVID R. GIFFORD	04/29/2019
RECEIVING PARTY DATA	
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State/Country:	TEXAS
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17958930
CORRESPONDENCE DATA	
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NAME OF SUBMITTER:	KATHERINE WILSON
SIGNATURE:	/Katherine Wilson/
DATE SIGNED:	10/03/2022
Total Attachments: 3	
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source=VT1001-CNT-Assignment-PP-Signed#page1.tif	

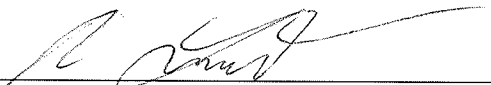
I Bernd Lienhard ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "LEARNING-BASED ANALYZER FOR MITIGATING LATCH-UP IN INTEGRATED CIRCUITS" ("APPLICATION"), which:

- ☒ will be filed; or
☐ was filed on _____ and was given Application
No _____ by the following Office
"_____";

Silicon Space Technology Corporation, a Delaware Corporation, D.B.A. Vorago Technologies, Inc., on behalf of itself and its successors and assigns ("ASSIGNEE"), is entitled to, and is desirous of acquiring, the entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

For good and valuable consideration, the receipt of which is hereby acknowledged by the ASSIGNOR, the ASSIGNOR hereby sells, assigns and transfers to the ASSIGNEE, ASSIGNOR's entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

ASSIGNOR agrees to execute all instruments and documents required for the making and prosecution of the APPLICATION (and APPLICATION DERIVATIVES), for litigation regarding letters patent derived therefrom, and for the purpose of protecting and perfecting title to the APPLICATION (and APPLICATION DERIVATIVES).

 _____ Inventor's Signature	<u>4-29-2019</u> _____ Date
<u>Bernd Lienhard</u> _____ Printed Name in English	_____ Printed Name in Native Language (if other than English)

PATENT ASSIGNMENT

Atty-Docket No.: VT1001


I David R. Gifford ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "LEARNING-BASED ANALYZER FOR MITIGATING LATCH-UP IN INTEGRATED CIRCUITS" ("APPLICATION"), which:

- ☒ will be filed; or
☐ was filed on _____ and was given Application
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"_____";

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 Inventor's Signature	<u>Apr. 29, 2019</u> Date
<u>David R. Gifford</u> Printed Name in English	 Printed Name in Native Language (if other than English)

Any-Docket No.: VT1001

☒ will be filed; or

☐ was filed on _____ and was given Application No
_____ by the following Office

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ASSIGNOR agrees to execute all instruments and documents required for the making and prosecution of the APPLICATION (and APPLICATION DERIVATIVES), for litigation regarding letters patent derived therefrom, and for the purpose of protecting and perfecting title to the APPLICATION (and APPLICATION DERIVATIVES).

<u>Patrice M. Parris</u>	<u>29th April 2019</u>
Inventor's Signature	Date
<u>Patrice M. Parris</u>	
Printed Name in English	Printed Name in Native Language (if other than English)