

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT7498970

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT	
NATURE OF CONVEYANCE:	Corrective Assignment to correct the ASSIGNEE NAME previously recorded on Reel 060646 Frame 0916. Assignor(s) hereby confirms the HANNIBAL BARCA IP LLC.	
CONVEYING PARTY DATA		
Name		Execution Date
FORTIETH FLOOR, LLC		12/26/2021
RECEIVING PARTY DATA		
Name:	HAMILCAR BARCA IP LLC	
Street Address:	5204 BLUEWATER DR	
City:	FRISCO	
State/Country:	TEXAS	
Postal Code:	75036	
PROPERTY NUMBERS Total: 18		
Property Type	Number	
Patent Number:	6748475	
Patent Number:	7343545	
Patent Number:	8010868	
Patent Number:	8086938	
Patent Number:	7486105	
Patent Number:	7671469	
Patent Number:	8407783	
Patent Number:	8669619	
Patent Number:	9147664	
Patent Number:	9392696	
Patent Number:	9806053	
Patent Number:	10163767	
Patent Number:	8848462	
Patent Number:	9379079	
Patent Number:	9397032	
Patent Number:	9947627	
Patent Number:	10170435	
Patent Number:	6690231	

CORRESPONDENCE DATA**Fax Number:**

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 8327259569**Email:** do@longhornip.com**Correspondent Name:** DO KIM**Address Line 1:** 8105 RASOR BLVD STE 210**Address Line 4:** PLANO, TEXAS 75024

NAME OF SUBMITTER:	KHALED FEKIH-ROMDHANE
SIGNATURE:	/Khaled Fekih-Romdhane/
DATE SIGNED:	08/23/2022
	This document serves as an Oath/Declaration (37 CFR 1.63).

Total Attachments: 7

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PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
FORTIETH FLOOR, LLC	12/26/2021
RECEIVING PARTY DATA	
Name:	HANNIBAL BARCA IP LLC
Street Address:	5204 BLUEWATER DRIVE
City:	FRISCO
State/Country:	TEXAS
Postal Code:	75036
PROPERTY NUMBERS Total: 18	
Property Type	Number
Patent Number:	6748475
Patent Number:	7343545
Patent Number:	8010868
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Patent Number:	7671469
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Patent Number:	9806053
Patent Number:	10163767
Patent Number:	8848462
Patent Number:	9379079
Patent Number:	9397032
Patent Number:	9947627
Patent Number:	10170435

PATENT
REEL: 061301 FRAME: 0569

Patent Number:

6690231

CORRESPONDENCE DATA**Fax Number:****Phone:**

8327259569

Email:

do@longhornip.com

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Correspondent Name:

DO KIM

Address Line 1:

8105 RASOR BLVD STE 210

Address Line 4:

PLANO, TEXAS 75024

NAME OF SUBMITTER:

KHALED FEKIH-ROMDHANE

Signature:

/Khaled Fekih-Romdhane/

Date:

07/27/2022

This document serves as an Oath/Declaration (37 CFR 1.63).

Total Attachments: 5

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RECEIPT INFORMATION**EPAS ID:**

PAT7455374

Receipt Date:

07/27/2022

Patent Assignment

Fortieth Floor, LLC, a Delaware limited liability company, with an address at 1209 Orange Street, Wilmington, Delaware 19808 ("Assignor") is the owner of the patents and patent applications listed in Schedule 1 hereto (collectively the "Listed Patents"); and Hamilcar Barca IP LLC, a Texas Limited Liability Company with an office at 5204 Bluewater Drive, Frisco, Texas 75036 ("Assignee") desires to acquire all right, title and interest in the Listed Patents and the other patents and related rights described below.

For good and valuable consideration, the receipt of which is hereby acknowledged, Assignor does hereby sell, assign, transfer and convey to Assignee and its successors and assigns all right, title and interest that may exist today and in the future to any and all:

- (1) Listed Patents;
- (2) patents and patent applications to which any of the Listed Patents directly or, through one or more intermediate patents, claims, or forms the basis for, priority anywhere in the world;
- (3) reissues, reexaminations, extensions, continuations, continuations-in-part, continuing prosecution applications and divisions of any of the items covered by (1) or (2) above;
- (4) foreign counterparts to any of the items covered by (1), (2) or (3) above, including utility models, inventors' certificates, industrial design protection and any other form of governmental grants or issuances for the protection of inventions, designs or discoveries;
- (5) patents that issue from any of the items covered by (1) through (4) above;
- (6) claims, causes of action and enforcement rights of any kind, whether currently pending, filed or otherwise, and whether known or unknown, under or arising from any of the items covered by (1) through (5) above, including all rights to pursue and collect damages, costs, injunctive relief and other remedies for past, current or future infringement thereof and including rights afforded under 35 U.S.C. § 154(d);
- (7) royalties, income and other payments due as of the date hereof or hereafter under or arising from any of the items covered by (1) through (6) above; and
- (8) rights to apply for, file, register, maintain, extend and renew in any or all countries of the world patents, certificates of invention, utility models, industrial design protection, design patent protection and other governmental grants or issuances of any kind related to any of the items covered by (1) through (7) above.

Assignor shall execute and deliver any instruments and do and perform any other acts and things as may be reasonably necessary or desirable for effecting and evidencing the assignments contemplated hereby, including the execution, acknowledgment and

recording of any instruments.

Assignor hereby authorizes and requests the Commissioner of Listed Patents and Trademarks and any other patent office to issue any and all patents, utility models or other governmental grants or issuances pertaining to any of the items assigned hereunder in the name of Assignee.

The assignments and rights pursuant hereto will inure to the benefit of Assignee and its successors, assigns and other legal representatives and is binding upon Assignor and its successors, assigns, heirs and legal representatives.

Assignor, by its duly authorized representative, has executed this assignment on the date set forth below.

DATE: DECEMBER 26, 2021

Fortieth Floor, LLC

By: Steven G. Steger
Title: Manager

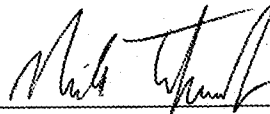
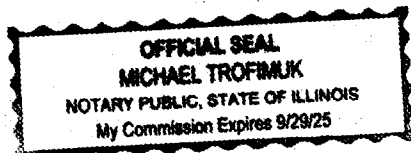


Signature

STATE OF IL)
) ss.
COUNTY OF Cook)

On this 26 day of Dec, 2021, personally appeared before me, Michael Trofimuk, to me known and known to me to be the person aforesaid, who duly acknowledged the signing of the foregoing instrument to be his or her voluntary act and deed, and as Steven Steger of Winnetka, IL did execute the same for the uses and purposes therein set forth.

WITNESS my hand and official seal.
(Seal)



Notary Public in and for the state of IL

Michael Trofimuk

Print Name

My appointment expires on: 9-29-2025

ACCEPTED:

DATE: Dec. 26, 2021

Hamilcar Barca IP LLC

By: Khaled Fekih-Romdhane

Printed/Typed Name

Title: Manager

Khaled Fekih Romdhane
Signature

**Schedule 1
to
Patent Assignment**

Country	Application Number	Application Date	Patent Number	Title
US	US09/706450	11/3/00	6748475	Programmable serial port architecture and system
TW	TW93111174A	4/22/04	I269968	Method for processing the noise in the sata
US	US11/039948	1/24/05	7343545	Method for processing noise interference
US	US11/889152	8/9/07	8010868	Method for processing noise interference
US	US11/960555	12/19/07	8086938	Method for processing noise interference
CN	CN200710186987.0	11/16/07	101231878	Memory systems and memory access methods
TW	TW96141997A	11/7/07	I345152	Memory systems and memory access methods
US	US11/625368	1/22/07	7486105	Memory systems and memory access methods
CN	CN200810097351.3	5/13/08	101477984	Semiconductor device used for alleviating micro-loading effect
JP	JP2008099955A	4/8/08	5029469	SiGe device with SiGe embedded dummy pattern to reduce microloading effect
JP	JP2012022232A	8/4/08	5354038	SiGe device with SiGe embedded dummy pattern to reduce microloading effect
TW	TW97116986A	5/8/08	I371783	Semiconductor device for alleviating micro-loading effect
US	US11/967264	12/31/07	7671469	SiGe device with SiGe-embedded dummy pattern for alleviating micro-loading effect
CN	CN201010610993A	12/29/10	102289608	Computing system and method
EP	EP10168224A	7/2/10	2397958	Computing system providing normal security and high security services
TW	TW99145766A	12/24/10	I438644	Computing system and computing method
US	US12/817387	6/17/10	8407783	Computing system providing normal security and high security services
CN	CN201110254172.8	8/31/11	102468301	Semiconductor structure
TW	TW100132659A	9/9/11	I446511	Semiconductor structure with multi-layer contact etch stop layer structure
US	US12/940022	11/4/10	8669619	Semiconductor structure with multi-layer contact etch stop layer structure

Country	Application Number	Application Date	Patent Number	Title
CN	CN201410531137A	10/10/14		Semiconductor package
WO	CN2013087951W	11/27/13		Semiconductor package
BR	BR112016007928	11/27/13		Semiconductor package
EP	EP13895184.3	11/27/13		Semiconductor package
IN	IN201647012475	11/27/13		Semiconductor package
TW	TW103135150A	10/9/14	I571990	Semiconductor package
US	US14/051548	10/11/13	9147664	Semiconductor package
US	US14/824210	8/12/15	9392696	Semiconductor package
US	US15/185063	6/17/16	9806053	Semiconductor package
US	US15/718141	9/28/17	10163767	Semiconductor package
CN	CN201210374729.6	9/29/12	103092783	Memory system
TW	TW101137216A	10/9/12	1494943	Memory controller and memory system
US	US13/617394	9/14/12	8848462	Low power memory controllers
CN	CN201510319478.5	6/11/15	106206515	Flip-chip and the method for forming flip-chip
US	US14/636137	3/2/15	9379079	Flip chip scheme and method of forming flip chip scheme
CN	CN201310404416A	9/6/13	103811471	Protection ring structure and its manufacture method
US	US14/020367	9/6/13	9397032	Guard ring structure and method for forming the same
US	US15/186937	6/20/16	9947627	Guard ring structure and method for forming the same
US	US15/911414	3/5/18	10170435	Guard ring structure and method for forming the same
CN	CN03122155.6	4/22/03	100578917	Gain amplifier circuit that minimizes the miller effect
US	US10/127909	4/22/02	6690231	Gain stage that minimizes the miller effect