

## PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1  
Stylesheet Version v1.2

EPAS ID: PAT7578897

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|---|---------------------------------------|
| <b>SUBMISSION TYPE:</b>   | NEW ASSIGNMENT                        |
| <b>NATURE OF CONVEYANCE:</b>  | ASSIGNMENT                            |
| <b>CONVEYING PARTY DATA</b>   |                                       |
| <b>Name</b>   | <b>Execution Date</b>                 |
| MARVELL SEMICONDUCTOR, INC.   | 06/22/2021                            |
| <b>RECEIVING PARTY DATA</b>   |                                       |
| <b>Name:</b>  | MARVELL ASIA PTE LTD                  |
| <b>Street Address:</b>  | TAI SENG CENTRE, 3 IRVING ROAD #10-01 |
| <b>City:</b>  | SINGAPORE                             |
| <b>State/Country:</b>   | SINGAPORE                             |
| <b>Postal Code:</b>   | 369522                                |
| <b>PROPERTY NUMBERS Total: 1</b>  |                                       |
| <b>Property Type</b>  | <b>Number</b>                         |
| <b>Application Number:</b>  | 17304598                              |
| <b>CORRESPONDENCE DATA</b>  |                                       |
| <b>Fax Number:</b>  | (408)222-2755                         |
| <i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i> |                                       |
| <b>Phone:</b>   | 4082222500                            |
| <b>Email:</b>   | lgalang@marvell.com                   |
| <b>Correspondent Name:</b>  | KELVIN VIVIAN                         |
| <b>Address Line 1:</b>  | 5488 MARVELL LANE                     |
| <b>Address Line 4:</b>  | SANTA CLARA, CALIFORNIA 95054         |
| <b>ATTORNEY DOCKET NUMBER:</b>  | MP13304                               |
| <b>NAME OF SUBMITTER:</b>   | KELVIN VIVIAN                         |
| <b>SIGNATURE:</b>   | /Kelvin Vivian/                       |
| <b>DATE SIGNED:</b>   | 10/07/2022                            |
| <b>Total Attachments: 3</b>   |                                       |
| source=MP13304-Assignment_Marvell Semiconductor, Inc. to Marvell Asia Pte Ltd_EXECUTED#page1.tif  |                                       |
| source=MP13304-Assignment_Marvell Semiconductor, Inc. to Marvell Asia Pte Ltd_EXECUTED#page2.tif  |                                       |
| source=MP13304-Assignment_Marvell Semiconductor, Inc. to Marvell Asia Pte Ltd_EXECUTED#page3.tif  |                                       |

**ASSIGNMENT**  
Corporate

WHEREAS Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, California 95054 (hereafter the "Assignor"), is the owner of the patents, including utility models and other similar registrations, or patent applications identified below (hereafter referred to as the "Patent Application(s)").

Marvell Asia Pte Ltd, a corporation of Singapore, having a place of business at Tai Seng Centre, 3 Irving Road #10-01, Singapore 369522 Singapore (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application(s).

THEREFORE, in consideration of the sum of One Dollar (\$1.00) US or equivalent and other good and valuable consideration paid to it, the receipt and sufficiency of which hereby is acknowledged, Assignor hereby sells, assigns and transfers to the Assignee the entire rights, title, and interest, together with all priority rights, in and to the Patent Application(s), and all inventions described and claimed therein, in any and all patents including utility models and other similar registrations granted from the Patent Application(s) and from patents or patent applications claiming priority directly or indirectly from the Patent Application(s), including but not limited to any and all reissues, renewals, reexaminations, divisions, and continuations in whole and in part, and foreign counterparts of the foregoing including national stage applications to the full end of the term or terms for which such patents issue in the United States, and/or any foreign countries, such entire rights, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this assignment not been made, including the right to sue and recover damages for past, present and future infringement.

| <u>Marvell Reference No.</u> | <u>Application No.</u> | <u>Filing Date</u> | <u>Title</u>  |
|------------------------------|------------------------|--------------------|---|
| MP13304                      | To Be Assigned         | Herewith           | Processor Core Debugging With Processor Virtualization          |
| MP13304PR                    | US 63/042,994          | June 23, 2020      | CPU DeBugger System Solution Allowing CPU Number Virtualization |

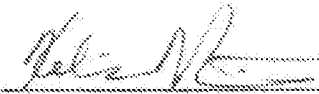
Assignor hereby covenants and agrees to execute and deliver, at the request of Assignee, such further instruments of transfer and assignment and to take any other action as such Assignee may

reasonably request to more effectively consummate the assignment contemplated by this agreement. Specifically, Assignor agrees to, at Assignee's expense, execute, acknowledge and deliver such further documents, instruments, conveyances and assurances and take such further actions as may be reasonably required to register in the name of Assignee the assignment of the patents in any appropriate governmental agency or registrar.

The undersigned representatives of the Assignor and the Assignee authorize and request that the attorneys of record in any of the Patent Application(s) insert in this assignment the filing date(s) and application number(s) of the Patent Application(s) when officially known.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the name of the Assignor's undersigned representative, which date shall be the effective date of this assignment.

MARVELL SEMICONDUCTOR, INC.

By: 

Name: Kelvin Vivian

Title: Director, Assistant General Counsel, IP Development

Date: 22 June 2021

MARVELL ASIA PTE LTD

By: 

Name: Chen Seok Ching

Title: Vice President, Assembly & Test Operations and Country Manager

Date: 23 June 2021