PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT7613402

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
MEYSAM ASADI	10/13/2022
FAN ZHANG	10/13/2022
SEYHAN KARAKULAK	10/19/2022

RECEIVING PARTY DATA

Name:	SK HYNIX INC.
Street Address:	2091, GYEONGCHUNG-DAERO
Internal Address:	BUBAL-EUB, GYEONGGI-DO
City:	ICHEON-SI
State/Country:	KOREA, REPUBLIC OF
Postal Code:	17336

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	17957974

CORRESPONDENCE DATA

Fax Number:

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Email: patentprocurement@perkinscoie.com, JAJones@perkinscoie.com,

RChomik@perkinscoie.com

Correspondent Name: PERKINS COIE LLP - SDO GENERAL

Address Line 1: PO BOX 1247

Address Line 2: ATTENTION: BING AI

Address Line 4: SEATTLE, WASHINGTON 98111-1247

ATTORNEY DOCKET NUMBER:	088453-8828.US00	
NAME OF SUBMITTER:	ROBERT CHOMIK	
SIGNATURE:	/Robert Chomik/	
DATE SIGNED: 10/27/2022		
	This document serves as an Oath/Declaration (37 CFR 1.63).	

Total Attachments: 9

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source=2022-10-13 - Executed Combined Declaration-Assignment - Meysam Asadi - 088453-8828.US00#page1.tif

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088453-8828.US00#page3.tif

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Applicant: SK hynix Inc.

Inventors: Meysam Asadi et al.

Application No.: 17/957,974 Confirmation No.: 1264

Filing Date: September 30, 2022

Title: ASYMMETRIC BIT ERRORS IN LOW-DENSITY PARITY-CHECK CODES FOR NON-VOLATILE MEMORY DEVICES

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

<u>UNDER RULE 37 C.F.R. § 1.63</u>

I, Meysam ASADI, of San Jose, CA, hereby declare that:

I believe I am the sole original inventor or an original joint inventor of a claimed invention in a patent application entitled:

ASYMMETRIC BIT ERRORS IN LOW-DENSITY PARITY-CHECK CODES FOR NON-VOLATILE MEMORY DEVICES

which was filed on September 30, 2022, as Application No. 17/957,974.

I believe the following persons are original joint inventors of the above-identified patent application(s):

<u>Fan ZHANG</u>, of <u>Fremont, CA</u>; and <u>Seyhan KARAKULAK</u>, of <u>San Jose, CA</u>.

The above-identified application was made or authorized to be made by me.

I have reviewed and understand the contents of the above-identified patent application including the claims.

I am aware of the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR §1.56.

155025569.1

Docket No. 088453-8828.US00

Inventors : Meysam ASADI et al.

Appl. No.: 17/957,974

Filed: September 30, 2022

Page : 2 of 3

I hereby acknowledge that any willful false statement made in the above declaration is punishable under 18 USC §1001 by fine or imprisonment of not more than five (5) years, or both.

ASSIGNMENT

I hereby assign to <u>SK hynix Inc.</u>, a corporation of Republic of Korea, having a place of business at 2091, Gyeongchung-daero, Bubal-eub, Gyeonggi-do, Icheon-si, Korea, and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in any and all inventions and improvements which are described in an application for United States Patent, entitled *ASYMMETRIC BIT ERRORS IN LOW-DENSITY PARITY-CHECK CODES FOR NON-VOLATILE MEMORY DEVICES*, filed on **September 30**, **2022**, and assigned U.S. Application Number **17/957,974**.

This assignment includes the entire right, title and interest in said application(s), all legal equivalents thereof in any country, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions and improvements, including, but not limited to, all divisions, continuations, reissues, reexamination certificates, any and all certificates issued in any post-grant proceeding, and extensions of said application(s) and patents of the United States, foreign patents, utility models, and design registrations, and the right to claim priority based on the filing date of said application(s) under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes, and the right to seek relief and recover all damages, including, but not limited to, a reasonable royalty, by reason of infringement or any other violation of patent or patent application rights. I authorize the Assignee to apply in all countries in my name or in the name of the Assignee for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements.

In addition, I hereby confirm the sale, assignment, and transfer to the Assignee of the entire right, title and interest throughout the world in said inventions and improvements by operation of an employment agreement between me and the Assignee existing at the time said inventions and improvements were made. Should I have any remaining interest, I hereby sell, assign, and transfer to the Assignee any right, title and interest I may have in said inventions and improvements anywhere in the world.

The foregoing assignments, sale and transfer have been made for good and valuable consideration, the receipt of which is hereby acknowledged.

I hereby represent and covenant that no assignment, sale, agreement or encumbrance has been or will be made or entered into that would conflict with the assignment of said inventions and improvements to the Assignee.

155025569.1

Inventors : Mevsam ASAIN et al. Docket No. 088453-8828 US00

Appl No. : 17/957,974 Filed : September 30, 2022 Page : 3 of 3

I further covenant for myself and my respective heirs, legal representatives and assigns, to assist the Assignee in handling various procedures, tasks and documentation in connection with this assignment, including, but not limited to, providing to the Assignee promptly upon the request of the Assignee all pertinent facts and documents relating to said invention and improvements, and said patents, said legal equivalents or other legal instrument as may be known and accessible to me and testifying as to the same in any interference, litigation, or proceeding relating thereto, and promptly executing and delivering to the Assignee or its legal representative any and all papers, documents, instruments or affidavits in connection with obtaining, maintaining, issuing or enforcing said application(s), said inventions and improvements, and said patents, said equivalents and other legal instrument which may be necessary or desirable to carry out the purposes thereof.

I am competent to execute the above declaration and assignment. I hereby have duly executed the declaration and assignment below with my name.

Full name of inventor: Meysam ASADI

Inventor's signature:

Date: 10/13/2022

Perkins Coie LLP P.O. Box 1247

Seattle, Washington 98111-1247 Telephone: (858) 720-5700 Facsimile: (206) 359-7198

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Applicant: SK hynix Inc.

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Application No.: 17/957,974 Confirmation No.: 1264

Filing Date: September 30, 2022

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

<u>UNDER RULE 37 C.F.R. § 1.63</u>

I, Fan ZHANG, of Fremont, CA, hereby declare that:

I believe I am the sole original inventor or an original joint inventor of a claimed invention in a patent application entitled:

ASYMMETRIC BIT ERRORS IN LOW-DENSITY PARITY-CHECK CODES FOR NON-VOLATILE MEMORY DEVICES

which was filed on September 30, 2022, as Application No. 17/957,974.

I believe the following persons are original joint inventors of the above-identified patent application(s):

Meysam ASADI, of San Jose, CA; and Seyhan KARAKULAK, of San Jose, CA.

The above-identified application was made or authorized to be made by me.

I have reviewed and understand the contents of the above-identified patent application including the claims.

I am aware of the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR §1.56.

155025569.1

Docket No. 088453-8828.US00

Inventors : Meysam ASADI et al.

Appl. No.: 17/957,974

Filed: September 30, 2022

Page : 2 of 3

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ASSIGNMENT

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This assignment includes the entire right, title and interest in said application(s), all legal equivalents thereof in any country, any and all United States and foreign patents, utility models, and design registrations granted for any of said inventions and improvements, including, but not limited to, all divisions, continuations, reissues, reexamination certificates, any and all certificates issued in any post-grant proceeding, and extensions of said application(s) and patents of the United States, foreign patents, utility models, and design registrations, and the right to claim priority based on the filing date of said application(s) under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes, and the right to seek relief and recover all damages, including, but not limited to, a reasonable royalty, by reason of infringement or any other violation of patent or patent application rights. I authorize the Assignee to apply in all countries in my name or in the name of the Assignee for patents, utility models, design registrations and like rights of exclusion and for inventors' certificates for said inventions and improvements.

In addition, I hereby confirm the sale, assignment, and transfer to the Assignee of the entire right, title and interest throughout the world in said inventions and improvements by operation of an employment agreement between me and the Assignee existing at the time said inventions and improvements were made. Should I have any remaining interest, I hereby sell, assign, and transfer to the Assignee any right, title and interest I may have in said inventions and improvements anywhere in the world.

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Seventors | Maysam ASADI et al. | Docket No. 988453-8328.US60

Appl. No. : 17/957,974 Filed : September 30, 2022

Page 3 of 3

I further covenant for myself and my respective heirs, legal representatives and assigns, to assist the Assignee in handling various procedures, tasks and documentation in connection with this assignment, including, but not limited to, providing to the Assignee promptly upon the request of the Assignee all pertinent facts and documents relating to said invention and improvements, and said patents, said legal equivalents or other legal instrument as may be known and accessible to me and testifying as to the same in any interference, litigation, or proceeding relating thereto, and promptly executing and delivering to the Assignee or its legal representative any and all papers, documents, instruments or affidavits in connection with obtaining, maintaining, issuing or enforcing said application(s), said inventions and improvements, and said patents, said equivalents and other legal instrument which may be necessary or desirable to carry out the purposes thereof.

I am competent to execute the above declaration and assignment. I hereby have duly executed the declaration and assignment below with my name.

Full name of inventor: Fan ZHANG

Inventor's signature:

Date; 10/13/2022

Perkins Coie LLP P.O. Box 1247

Seattle, Washington 98111-1247 Telephone: (858) 720-5700

Facsimile: (206) 359-7198

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Applicant: SK hynix Inc.

Inventors: Meysam Asadi et al.

Application No.: 17/957,974 Confirmation No.: 1264

Filing Date: September 30, 2022

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

<u>UNDER RULE 37 C.F.R. § 1.63</u>

I, Seyhan KARAKULAK, of San Jose, CA, hereby declare that:

I believe I am the sole original inventor or an original joint inventor of a claimed invention in a patent application entitled:

ASYMMETRIC BIT ERRORS IN LOW-DENSITY PARITY-CHECK CODES FOR NON-VOLATILE MEMORY DEVICES

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Docket No. 088453-8828.US00

Inventors: Meysam ASADI et al.

Appl. No.: 17/957,974

Filed: September 30, 2022

Page : 2 of 3

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inventors : Meysam ASADI et al.

Appl. No.: 17/957,974

Filed : September 30, 2022

Page

: 3 of 3 Docket No. 088453-8828,US00

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I am competent to execute the above declaration and assignment. I hereby have duly executed the declaration and assignment below with my name.

Full name of inventor:	Seyhan KARAKULAR
Inventor's signature:	Skolaholi
Date: 10(1	912022

Perkins Coie LLP P.O. Box 1247 Seattle, Washington 98111-1247

Telephone: (858) 720-5700 Facsimile: (206) 359-7198

155025569.1

PATENT REEL: 061560 FRAME: 0751

RECORDED: 10/27/2022