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| PATENT ASSIGNMENT COVER SHEET |
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EPAS ID: PAT7633120

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| SUBMISSION TYPE: | NEW ASSIGNMENT |
| NATURE OF CONVEYANCE: | ASSIGNMENT |
| CONVEYING PARTY DATA | |
| Name | Execution Date |
| KOBE STEEL, LTD. | 08/09/2022 |
| RECEIVING PARTY DATA | |
| Name: | SAMSUNG DISPLAY CO., LTD. |
| Street Address: | 1, SAMSUNG-RO, GIHEUNG-GU, GYEONGGI-DO |
| City: | YONGIN-SI |
| State/Country: | KOREA, REPUBLIC OF |
| Postal Code: | 17113 |
| PROPERTY NUMBERS Total: 5 | |
| Property Type | Number |
| Application Number: | 13642314 |
| Application Number: | 13989944 |
| Application Number: | 14113322 |
| Application Number: | 14436241 |
| Application Number: | 15290715 |
| CORRESPONDENCE DATA | |
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| ATTORNEY DOCKET NUMBER: | PNK2235A |
| NAME OF SUBMITTER: | JAMES J. MERRICK |
| SIGNATURE: | /James J. Merrick/ |
| DATE SIGNED: | 11/08/2022 |
| Total Attachments: 3 source=92R4934#page1.tif | |

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ASSIGNMENT

WHEREAS, **Samsung Display Co., Ltd.**, a corporation organized and existing under the laws of the REPUBLIC OF KOREA, having a place of business at 1, Samsung-ro, Giheung-gu, Yongin-si, Gyeonggi-do, 17113 and **KOBE STEEL, LTD.**, a corporation organized and existing under the laws of the JAPAN, having a place of business at 2-10-26, Wakino-hama-cho, Chuo-Ku, Kobe-Shi, Hyogo, JAPAN (hereinafter referred to as "ASSIGNORS") are the owners of the entire right, title and interest in the inventions, United States patents, and United States patent applications listed on the attached Schedule of U.S. Patents and Patent Applications (hereinafter referred to as the "INVENTIONS," "PATENTS," and "APPLICATIONS," respectively);

WHEREAS, **Samsung Display Co., Ltd.**, (hereinafter referred to as "ASSIGNEE"), a corporation organized and existing under the laws of the REPUBLIC OF KOREA, having a place of business at 1, Samsung-ro, Giheung-gu, Yongin-si, Gyeonggi-do, 17113; is desirous of acquiring an interest in any and all countries, in and to the INVENTIONS, the PATENTS, the APPLICATIONS, and any and all Patents to be obtained therefor;

NOW THEREFORE, TO ALL WHOM IT MAY CONCERN, be it known that for good and valuable consideration, the receipt of which is hereby acknowledged, we, the ASSIGNORS, have assigned and transferred, and hereby assign and transfer unto ASSIGNEE, the entire right, title and interest in and to the INVENTIONS, the PATENTS, the APPLICATIONS, and any and all Patents that may be issued therefrom, in any and all countries, including any and all revivals, refilings, continuations, continuations-in-part, divisions and reissues thereof, to ASSIGNEE; and we do hereby agree that we will execute all papers necessary in connection with any and all patent applications when called upon to do so by ASSIGNEE, its successors or assigns, and that we will, at the cost and expense of ASSIGNEE, fully assist and cooperate in all matters in connection with any and all patent applications and Patents issuing thereon.

The undersigned declares that all statements made herein of his or her own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and

the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the APPLICATION or any Patents issuing therefrom.

By: _____ [ASSIGNOR] Seiji Kameoka

Name: Seiji KAMEOKA

Title: General Manager of Intellectual Property
Department

Date: August 9, 2022

By: _____ [ASSIGNOR]

Name: _____

Title: _____

Date: _____

SCHEDULE OF U.S. PATENTS

| No. | U.S. PATENT NUMBER | ISSUE DATE | TITLE |
|-----|--------------------|------------|--|
| 1 | 8,907,334 | 2014-12-09 | OXIDE FOR SEMICONDUCTOR LAYER OF THIN-FILM TRANSISTOR, SPUTTERING TARGET, AND THIN-FILM TRANSISTOR |
| 2 | 9,178,073 | 2015-11-03 | OXIDE FOR SEMICONDUCTOR LAYER OF THIN-FILM TRANSISTOR, SPATTERING TARGET, AND THIN-FILM TRANSISTOR |
| 3 | 9,093,542 | 2015-07-28 | THIN-FILM TRANSISTOR STRUCTURE, AS WELL AS THIN-FILM TRANSISTOR AND DISPLAY DEVICE HAVING SAID STRUCTURE |
| 4 | 9,508,856 | 2016-11-29 | THIN FILM TRANSISTOR |
| 5 | 10,256,091 | 2019-04-09 | OXIDE FOR SEMICONDUCTOR LAYER OF THIN-FILM TRANSISTOR, SEMICONDUCTOR LAYER OF THIN-FILM TRANSISTOR HAVING SAID OXIDE, AND THIN-FILM TRANSISTOR |