PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT7739440

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
CAVIUM INTERNATIONAL	12/31/2019

RECEIVING PARTY DATA

Name:	MARVELL ASIA PTE, LTD.
Street Address:	TAI SENG CENTRE, 3 IRVING ROAD #10-01
City:	SINGAPORE
State/Country:	SINGAPORE
Postal Code:	369522

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	17098140

CORRESPONDENCE DATA

Fax Number: (978)341-0136

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 9783410036
Email: Diana.DeCaro@hbsr.com
Correspondent Name: TIMOTHY J. MEAGHER
Address Line 1: HAMILTON, BROOK, SMITH & REYNOLDS, P.C.
Address Line 2: 530 VIRGINIA ROAD, P.O. BOX 9133
Address Line 4: CONCORD, MASSACHUSETTS 01742-9133

ATTORNEY DOCKET NUMBER:	MP12872.C1; 3795.1196-001
NAME OF SUBMITTER:	DIANA DECARO
SIGNATURE:	/DIANA DECARO/
DATE SIGNED:	01/11/2023

Total Attachments: 161
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ASSIGNMENT AND QUITCLAIM TRANSFER

WHEREAS, **Cavium International**, a corporation organized under the laws of Cayman Islands, and with offices at c/o Estera Trust (Cayman) Limited, PO Box 1350, Clifton House, 75 Fort Street, Grand Cayman KY1-1108, Cayman Islands (hereinafter "**Assignor**") owns the patents and patent applications listed in Exhibit A attached hereto and incorporated herein by this reference (hereinafter "**Group A**") and may have an ownership interest in some or all of the patents and patent applications listed in Exhibit B attached hereto and incorporated herein by this reference (hereinafter "**Group B**");

WHEREAS, **Marvell Asia Pte, Ltd.**, a corporation organized under the laws of Singapore, and with offices at Tai Seng Centre, 3 Irving Road, #10-01 Singapore 369522, a limited liability corporation (hereinafter "Assignee"), desires to acquire the Assignor's ownership interest in, to and under the **Group A** and the **Group B**; and

WHEREAS, Assignor has executed on an effective date of December 31, 2019 (hereinafter "**Effective Date**") that certain Transfer and Assignment Agreement assigning, among other things, all interest in and to the **Group A** and transferring by quitclaim its ownership interest, if any, in and to the **Group B** to Assignee (the "Purchase Agreement").

NOW, THEREFORE, for consideration of one dollar ($1.00) and other good and valuable consideration paid by Assignee to Assignor, the receipt and sufficiency of which hereby is acknowledged, Assignor does hereby sell, assign and transfer to Assignee its entire interest in and to the **Group A**, including all divisions, continuations, reexaminations, reissues, and foreign counterparts of the applications and patent registrations for the **Group A** (and the right to claim priority and the right to apply for any of the foregoing); including assignment of any and all provisional applications that are relied upon for priority; all rights to causes of action and remedies related thereto (including, without limitation, the right to sue for past, present or future infringement, misappropriation or violation of rights related to the foregoing); and any and all other rights and interests arising out of, in connection with or in relation to the **Group A**.

FURTHER, for consideration of one dollar ($1.00) and other good and valuable consideration paid by Assignee to Assignor, the receipt and sufficiency of which hereby is acknowledged, Assignor does hereby **quitclaim** sell, assign and transfer to Assignee its ownership interest, if any, in and to the **Group B**, including all divisions, continuations, reexaminations, reissues, and foreign counterparts of the applications and patent registrations for the **Group B** (and the right to claim priority and the right to apply for any of the foregoing); including assignment of any and all provisional applications that are relied upon for priority; all rights to causes of action and remedies related thereto (including, without limitation, the right to sue for past, present or future infringement, misappropriation or violation of rights related to the foregoing); and any and all other rights and interests arising out of, in connection with or in relation to the **Group B**.

FURTHER, nothing contained herein shall be deemed to alter or amend the terms and provisions of the Purchase Agreement and in the event of any conflict between the terms and provisions of this Assignment and the Purchase Agreement, the terms and provisions of the Purchase Agreement shall be deemed to govern and be controlling in all circumstances. This Assignment is executed pursuant to the Purchase Agreement and is entitled to the benefits and subject to the

provisions thereof and shall bind and inure to the benefit of the parties thereto and their respective successors and assigns.

FURTHER, Assignor hereby covenants and agrees to execute and deliver, at the request of Assignee, such further instruments of transfer and assignment and to take any other action as such Assignee may reasonably request to more effectively consummate the assignments contemplated by this Assignment. Specifically, Assignor agrees to, at Assignee's expense, execute, acknowledge and deliver such further documents, instruments, conveyances and assurances and take such further actions as may be reasonably required to register in the name of Assignee the assignment of any of the Patents in **Group A** and/or the **Group B** in any appropriate governmental agency or registrar.

IN WITNESS WHEREOF, Assignor and Assignee have executed and delivered this Patent Assignment by their duly authorized representatives at 11:58 pm PST on the **Effective Date**.

ASSIGNOR:

Cavium International,
a corporation organized under the laws of Cayman Islands

By: _____

Philip Anderson
Director

ASSIGNEE:

Marvell Asia Pte, Ltd.,
a corporation organized under the laws of Singapore

By: _____

Steven Parker
Director

provisions thereof and shall bind and inure to the benefit of the parties thereto and their respective successors and assigns.

FURTHER, Assignor hereby covenants and agrees to execute and deliver, at the request of Assignee, such further instruments of transfer and assignment and to take any other action as such Assignee may reasonably request to more effectively consummate the assignments contemplated by this Assignment. Specifically, Assignor agrees to, at Assignee's expense, execute, acknowledge and deliver such further documents, instruments, conveyances and assurances and take such further actions as may be reasonably required to register in the name of Assignee the assignment of any of the Patents in **Group A** and/or the **Group B** in any appropriate governmental agency or registrar.

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By: _____

Philip Anderson
Director

ASSIGNEE:

Marvell Asia Pte, Ltd.,
a corporation organized under the laws of Singapore

By: _____

Steven Parker
Director

Exhibit A - 3A CI TO MAPL Group A

Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
AQUA.P102	AQUA.P102C1	US	12/604,343				Unknown	
AQUA.P103	AQUA.P103C1	US	12/604,351				Unknown	
AQUA.P103	AQUA.P103C2	US	13/965,635				Unknown	
AQUA.P104	AQUA.P104C1	US	12/604,323				Unknown	
AQUA.P105	AQUA.P105C1	US	12/604,358				Unknown	
AQUA.P106	AQUA.P106C1	US	12/563,938				Unknown	
AQUA.P106	AQUA.P106CIP1	US	12/977,844				Unknown	
AQUA.P108	AQUA.P108C1	US	12/118,664				Unknown	
AQUA.P108	AQUA.P108C2	US	13/168,251				Unknown	
AQUA.P109	AQUA.P109	US	13/409,061				Unknown	
AQUA.P110	AQUA.P110	US	13/190,419				Unknown	
AQUA.P111	AQUA.P111	US	13/039,099				Unknown	
AQUA.P115	AQUA.P115	US	09/863,081				Unknown	
AQUA.P116	AQUA.P116	US	09/862,384				Unknown	
AQUA.P117	AQUA.P117	US	10/993,107				Unknown	
AQUA.P117	AQUA.P117CIP1	US	11/198,070				Unknown	
AQUA.P117	AQUA.P117CIP2	US	11/738,864				Unknown	
AQUA.P118	AQUA.P118	US	11/006,329				Unknown	
AQUA.P120	AQUA.P120	US	11/474,158				Unknown	
AQUA.P124	AQUA.P124	US	11/501,324				Unknown	
AQUA.P125	AQUA.P125	US	11/508,593				Unknown	
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AQUA.P129	AQUA.P129	US	11/442,485				Unknown	
AQUA.P130	AQUA.P130	US	11/451,220				Unknown	
AQUA.P131	AQUA.P131	US	11/405,646				Unknown	
AQUA.P132	AQUA.P132	US	11/676,946				Unknown	
AQUA.P133	AQUA.P133	US	11/678,054				Unknown	
AQUA.P136	AQUA.P136C1	US	12/247,833				Unknown	
AQUA.P137	AQUA.P137C1	US	12/118,650				Unknown	
AQUA.P141	AQUA.P141	US	12/139,447				Unknown	
AQUA.P146	AQUA.P146	US	12/329,514				Unknown	

PATENT
REEL: 062380 FRAME: 0118

Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
AQUA.P146	AQUA.P146CIP1	US	13/190,288				Unknown	
AQUA.P147	AQUA.P147	US	12/369,702				Unknown	
AQUA.P148	AQUA.P148C1	US	12/563,927				Unknown	
AQUA.P149	AQUA.P149	US	13/111,785				Unknown	
AQUA.P149	AQUA.P149CIP	US	13/790,333				Unknown	
AQUA.P150	AQUA.P150	US	13/171,380				Unknown	
AQUA.P151	AQUA.P151	US	13/107,659				Unknown	
AQUA.P161	AQUA.P161C1	US	13/350,624				Unknown	
AQUA.P163	AQUA.P163	US	13/625,820				Unknown	
AQUA.P166	AQUA.P166C1	US	15/228,161				Pending	
AQUA.P168	AQUA.P168	US	13/346,534				Unknown	
AQUA.P169	AQUA.P169	US	13/413,029				Unknown	
AQUA.P169	AQUA.P169C1	US	14/843,394				Unknown	
AQUA.P174	AQUA.P174	US	13/523,826				Unknown	
AQUA.P175	AQUA.P175	US	13/966,208				Unknown	
AQUA.P176	AQUA.P176	US	13/671,132				Unknown	
AQUA.P176	AQUA.P176C1	US	14/634,605				Unknown	
AQUA.P176	AQUA.P176CIP1C2	US	16/114,084				Pending	
AQUA.P177	AQUA.P177	US	10/699,761				Unknown	
AQUA.P178	AQUA.P178	US	10/695,166				Unknown	
AQUA.P178	AQUA.P178C1	US	11/702,348				Unknown	
AQUA.P178	AQUA.P178CIP1	US	10/816,409				Unknown	
AQUA.P178	AQUA.P178CIP1CN	CHINA	580018010.7	3/18/2005	580018010	5/26/2010	Issued	SUB-BLOCK DOMAIN TRANSFORMATION MULTIPLE SIGNAL PROCESS
AQUA.P178	AQUA.P178CIP1JP	JAPAN		3/18/2005	4796051	8/5/2011	Issued	SUB-BLOCK DOMAIN TRANSFORMATION MULTIPLE SIGNAL PROCESS
AQUA.P178	AQUA.P178CIP1TW	TAIWA	94108854	3/22/2005	I360305	3/11/2012	Issued	SUB-BLOCK DOMAIN TRANSFORMATION MULTIPLE SIGNAL PROCESS
AQUA.P178	AQUA.P178TW	TAIWA	93132397	10/26/2004	I377814	11/21/2012	Issued	A METHOD AND APPARATUS FOR DOMAIN TRANSFORMATION MULT
AQUA.P180	AQUA.P180	US	11/148,988				Unknown	
AQUA.P180	AQUA.P180TW	TAIWA	95119859	6/5/2006	I338464	3/1/2011	Issued	SINGLE AMPLIFIER PRESAMPLE PROCESSING CIRCUITRY
AQUA.P181	AQUA.P181	US	10/926,699				Unknown	
AQUA.P181	AQUA.P181TW	TAIWA	94128617	8/22/2005	I371929	9/1/2012	Issued	METHOD OF A TRANSCEIVER DECODING AN ETHERNET SIGNAL AND
AQUA.P183	AQUA.P183	US	11/090,109				Unknown	
AQUA.P183	AQUA.P183CIP1	US	12/217,962				Unknown	
AQUA.P183	AQUA.P183CIP1D1	US	12/613,627				Unknown	
AQUA.P184	AQUA.P184	US	11/205,615				Unknown	

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
AQUA.P184	AQUA.P184TW	TAIWA	95129735	8/14/2006	I323983	4/21/2010	Issued	RECEIVER ADC CLOCK DELAY BASED ON ECHO SIGNALS
AQUA.P185	AQUA.P185	US	11/247,805				Unknown	
AQUA.P185	AQUA.P185TW	TAIWA	95137283		I337020	2/1/2011	Issued	MULTIPLE MODULATION RATE 10GBASE-T TRANSMISSION
AQUA.P186	AQUA.P186	US	11/303,364				Unknown	
AQUA.P186	AQUA.P186TW	TAIWA			I326176	6/11/2010	Issued	TRANSCEIVER POWER BACKOFF
AQUA.P189	AQUA.P189TW	TAIWA			I351199	10/21/2011	Issued	TRANCEIVER NON-LINEARITY CANCELLATION
AQUA.P190	AQUA.P190	US	11/438,177				Unknown	
AQUA.P190	AQUA.P190C1	US	12/941,039				Unknown	
AQUA.P190	AQUA.P190C2	US	13/974,551				Unknown	
AQUA.P191	AQUA.P191	US	11/510,934				Unknown	
AQUA.P191	AQUA.P191TW	TAIWA	TW2007012980	8/13/2007	I355150	12/21/2011	Issued	MULTIPLE TRANSMISSION PROTOCOL TRANSCEIVER
AQUA.P192	AQUA.P192	US	11/644,598				Unknown	
AQUA.P193	AQUA.P193TW	TAIWA	97109681		I368392	7/11/2012	Issued	SELECTION OF FILTER COEFFICIENTS FOR TRANCEIVER NON-LINEARI
AQUA.P195	AQUA.P195	US	12/290,181				Unknown	
AQUA.P195	AQUA.P195C1	US	12/960,481				Unknown	
AQUA.P205	AQUA.P205	US	13/398,748				Unknown	
AQUA.P210	AQUA.P210	US	14/459,260				Unknown	
AQUA.P211	AQUA.P211	US	14/180,082				Pending	
AQUA.P212	AQUA.P212	US	14/961,797				Unknown	
AQUA.P212	AQUA.P212C1	US	15/683,618				Unknown	
AQUA.P213	AQUA.P213	US	14/961,810				Pending	
AQUA.P214	AQUA.P214	US	14/995,141				Unknown	
AQUA.P215	AQUA.P215	US	15/011,384				Unknown	
AQUA.P216	AQUA.P216	US	15/065,753				Unknown	
AQUA.P216	AQUA.P216C1	US	15/853,504				Pending	
AQUA.P217	AQUA.P217	US	15/063,468				Unknown	
AQUA.P218	AQUA.P218	US	15/083,041				Pending	
AQUA.P219	AQUA.P219	US	14/858,918				Unknown	
AQUA.P219	AQUA.P219CIP1	US	15/276,250				Unknown	
AQUA.P219	AQUA.P219CIP2	US	16/057,609				Pending	
AQUA.P221	AQUA.P221	US	14/858,902				Unknown	
AQUA.P222	AQUA.P222C1	US	16/112,583				Pending	
AQUA.P222	AQUA.P222CIP1	US	16/365,510				Pending	
AQUA.P224	AQUA.P224	US	14/961,802				Unknown	

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
AQUA.P225	AQUA.P225	US	14/961,806				Pending	
AQUA.P226	AQUA.P226	US	15/222,742				Pending	
AQUA.P227	AQUA.P227	US	15/063,474				Unknown	
AQUA.P228	AQUA.P228	US	15/478,046				Unknown	
AQUA.P229	AQUA.P229	US	15/398,583				Unknown	
AQUA.P230	AQUA.P230	US	15/669,717				Pending	
AQUA.P231	AQUA.P231	US	15/451,920				Pending	
AQUA.P232	AQUA.P232	US	15/620,618				Pending	
AQUA.P233	AQUA.P233	US	15/478,051				Pending	
AQUA.P235	AQUA.P235	US	16/354,512				Pending	
AQUA.P236	AQUA.P236	US	16/354,525				Pending	
AQUA.P237	AQUA.P237	US	16/354,542				Pending	
AQUA.P238	AQUA.P238	US	16/419,585				Pending	
AQUA.P239	AQUA.P239	US	16/419,625				Pending	
AQUA.P240	AQUA.P240	US	16/537,008				Pending	
AQUA.P241	AQUA.P241	US	16/514,844				Pending	
AQUA.P242	AQUA.P242	US	16/171,298				Pending	
AQUA.P243	AQUA.P243	US	16/362,479				Pending	
AQUA.P244	AQUA.P244	US	16/362,494				Pending	
AQUA.P245	AQUA.P245	US	16/362,500				Pending	
AQUA.P246	AQUA.P246	US	16/143,254				Pending	
AQUA.P247	AQUA.P247	US	16/162,751				Pending	
AQUA.P248	AQUA.P248	US	16/419,643				Pending	
AQUA.P249	AQUA.P249	US	16/554,000				Pending	
AQUA.P250	AQUA.P250	US	16/554,023				Pending	
AQUA.P251	AQUA.P251	US	16/554,036				Pending	
AQUA.P252	AQUA.P252P	US	62/868778				Pending	
ARC920030015	ARC920030015EP1	Europe	4766141.8		1644834	12/15/2010	Not Provide	Autonomic Parity Exchange
ARC920030015	ARC920030015TW1	Taiwan	93120449		I308689	4/11/2009	Not Provide	Method and System for Increasing Failure Tolerance of a Storage Syst
ARC920030015	ARC920030015US1	US	10/619,649	7/14/2003	7281177	10/9/2007		Autonomic Parity Exchange
ARC920030040	ARC920030040CN1	China	2.0048002039e-		ZL2004800203	6/3/2009	Not Provide	RAID 3+3
ARC920030040	ARC920030040US1	US	10/619,648	7/14/2003	7254754	8/7/2007		Raid 3+3
AUS919970289	AUS919970289US1	US	08/873,830	6/12/1997	5949272	9/7/1999		
AUS9199990403	AUS919990403US1	US	09/422,367	10/21/1999	6552589	4/22/2003		

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AUS919990403	AUS919990403US2	US	10/116,813	4/5/2002	6701444	3/2/2004		Method And Apparatus For Process Independent Clock Signal Distrib
AUS920000013	AUS920000013US1	US	09/615,767	7/13/2000	6643772	11/4/2003		Universal Boot Code For A Computer Network
AUS920000159	AUS920000159US1	US	09/574,186	5/18/2000	6829677	12/7/2004		Method And Apparatus For Preserving The Contents Of Synchronou
AUS920010256	AUS920010256US1	US	09/870,623	5/31/2001	6933752	8/23/2005		Method And Apparatus For Interface Signaling Using Single-Ended /
AUS920010615	AUS920010615US1	US	09/974,986	10/11/2001	6529084	3/4/2003		Interleaved Feedforward Vco And Pll
AUS920030695	AUS920030695US1	US	11/002,559	12/2/2004	7656237	2/2/2010		Method To Gate Off Plls In A Deep Power Saving State Without Sep
AUS920030707	AUS920030707US1	US	10/670,823	9/25/2003	7523157	4/21/2009		Managing A Plurality Of Processors As Devices
AUS920040151	AUS920040151US1	US	10/840,561	5/6/2004	6980038	12/27/2005		Circuit For Compensating Charge Leakage In A Low Pass Filter Capa
AUS920040361	AUS920040361US1	US	10/896,504	7/22/2004	7129859	10/31/2006		Method And Apparatus For Minimizing Threshold Variation From B
AUS920040682	AUS920040682US1	US	11/082,973	3/17/2005	7350095	3/25/2008		Digital Circuit To Measure And/Or Correct Duty Cycles
AUS920040682	AUS920040682US2	US	12/014,501	1/15/2008	7917795	3/29/2011		Digital Circuit To Measure And/Or Correct Duty Cycles
AUS920050120	AUS920050120US1	US	11/168,692	6/28/2005	7336105	2/26/2008		Dual Gate Transistor Keeper Dynamic Logic
AUS920050298	AUS920050298US1	US	11/168,691	6/28/2005	7411425	8/12/2008		Method For Power Consumption Reduction In A Limited-Switch Dyr
AUS920050346	AUS920050346US1	US	11/250,019	10/13/2005	7376001	5/20/2008		Row Circuit Ring Oscillator Method For Evaluating Memory Cell Perfo
AUS920050383	AUS920050383US1	US	11/381,373	5/3/2006	8037893	10/18/2011		Optimizing Thermal Performance Using Thermal Flow Analysis
AUS920050391	AUS920050391US1	US	11/381,391	5/3/2006	7596430	9/29/2009		Selection Of Processor Cores For Optimal Thermal Performance
AUS920050410	AUS920050410US1	US	11/137,234	5/25/2005	7444525	10/28/2008		Methods And Apparatus For Reducing Leakage Current In A Disabled
AUS920050827	AUS920050827US1	US	11/360,907	2/23/2006	7340361	3/4/2008		Method And Apparatus For Analyzing Computer System Performance
AUS920060507	AUS920060507US2	US	12/347,947	12/31/2008	8214660	7/3/2012		Structure For An Apparatus For Monitoring And Controlling Heat Gen
AUS920060770	AUS920060770CN1	China	2.0071018674e-		ZL2007101867	5/4/2011	Not Provide	Programmable Local Clock Buffer Capable of Varying Initial Settings
AUS920060770	AUS920060770US1	US	11/609,403	12/12/2006	7613944	11/3/2009		Programmable Local Clock Buffer Capable Of Varying Initial Settings
AUS920070137	AUS920070137US1	US	12/023,337	1/31/2008	8117579	2/14/2012		Lssd Compatibility For Gsd Unified Local Clock Buffers
AUS920070148	AUS920070148US1	US	11/877,898	10/24/2007	7414436	8/19/2008		Limited Switch Dynamic Logic Cell Based Register
AUS920070155	AUS920070155US1	US	11/759,426	6/7/2007	7466164	12/16/2008		Method And Apparatus For A Configurable Low Power High Fan-In M
AUS920070250	AUS920070250US1	US	11/772,592	7/2/2007	7474574	1/6/2009		Shift Register Latch With Embedded Dynamic Random Access Memor
AUS920070440	AUS920070440US2	US	12/174,572	7/16/2008	8086989	12/27/2011		Structure For Glitchless Clock Multiplexer Optimized For Synchronous
AUS920080274	AUS920080274CN1	China	2.0098013888e-		ZL2009801388	2/20/2013	Not Provide	OPTIMIZING SRAM PERFORMANCE OVER EXTENDED VOLTAGE OR PR
AUS920080274	AUS920080274DE1	Germa	20090783647		2345036	5/30/2012	Not Provide	OPTIMIZING SRAM PERFORMANCE OVER EXTENDED VOLTAGE OR PR
AUS920080274	AUS920080274EP1	Europe	9783647.2		2345036	5/30/2012	Not Provide	OPTIMIZING SRAM PERFORMANCE OVER EXTENDED VOLTAGE OR PR
AUS920090052	AUS920090052US1	US	12/508,235	7/23/2009	8212537	7/3/2012		Integratable Efficient Switching Down Converter
AUS920090076	AUS920090076US1	US	12/569,421	9/29/2009	8336008	12/18/2012		Characterization Of Long Range Variability
AUS920100102	AUS920100102US1	US	12/828,238	6/30/2010	8183920	5/22/2012		Variable Gain Amplifier With Reduced Power Consumption
AUS920100248	AUS920100248US1	US	12/827,693	6/30/2010	8338948	12/25/2012		Ball Grid Array With Improved Single-Ended And Differential Signal Pe
AUS920100248	AUS920100248US3	US	13/687,218	11/28/2012	8742565	6/3/2014		Ball Grid Array With Improved Single-Ended And Differential Signal Pe

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AUS920120203	AUS920120203US1	US	13/689,044	11/29/2012	9057766	6/16/2015		Isolating Failing Latches Using A Logic Built-In Self-Test
AUS920120226	AUS920120226CN1	China	2.0131059077e-		ZL2013105907	3/1/2017	Not Provide	CHANGING RESONANT CLOCK MODES
AUS920120226	AUS920120226US1	US	13/719,408	12/19/2012	8736342	5/27/2014		Changing Resonant Clock Modes
AUS920120246	AUS920120246CN1	China	2.0141004309e-		ZL2014100430	4/12/2017	Not Provide	WIDE BANDWIDTH RESONANT GLOBAL CLOCK DISTRIBUTION
AUS920140069	AUS920140069US1	US	14/294,219	6/3/2014	9455976	9/27/2016		Multi-Factor Secure Appliance Decommissioning
AUS920140151	AUS920140151US1	US	14/506,331	10/3/2014	9582388	2/28/2017		Dynamic Multi Purpose External Access Points Connected To Core I
AUS920140213	AUS920140213US1	US	14/449,666	8/1/2014	9575822	2/21/2017		Tracking A Relative Arrival Order Of Events Being Stored In Multiple
AUS920140214	AUS920140214US1	US	14/521,739	10/23/2014	9424173	8/23/2016		Performing Secure Address Relocation Within A Multi Processor Sys
BUR820150009	BUR820150009-CN-	China	2.0161007629e-				Not Provide	Electrostatic Discharge Protection Structures for Efuses
BUR820150009	BUR820150009-TW	Taiwan	105100696				Not Provide	Electrostatic Discharge Protection Structures for Efuses
BUR820150009	BUR820150009-US-	US	14/971,644	12/16/2015	9940986	4/10/2018		Electrostatic Discharge Protection Structures For Efuses
BUR820150017	BUR820150017-US-	US	14/945,530	11/19/2015	9767240	9/19/2017		Temperature-Aware Integrated Circuit Design Methods And System
BUR820150019	BUR820150019-US-	US	15/002,550	1/21/2016	9852259	12/26/2017		Area And/Or Power Optimization Through Post-Layout Modificatio
BUR820150044	BUR820150044-CN-	China	2.0161097842e-				Not Provide	CONTENT-ADDRESSABLE MEMORY HAVING MULTIPLE REFERENCE
BUR820150044	BUR820150044-US-	US	14/990,125	1/7/2016	9704575	7/11/2017		Content-Addressable Memory Having Multiple Reference Matchlines
BUR820150046	BUR820150046-CN-	China	2.0161071082e-				Not Provide	VOLTAGE-AWARE ADAPTIVE STATIC RANDOM ACCESS MEMORY (SR
BUR820150046	BUR820150046-TW	Taiwan	105112406		I 630617	7/21/2018	Not Provide	VOLTAGE-AWARE ADAPTIVE STATIC RANDOM ACCESS MEMORY (SR
BUR820150046	BUR820150046-US-	US	15/009,132	1/28/2016	9508420	11/29/2016		Voltage-Aware Adaptive Static Random Access Memory (Sram) Write
BUR820150051	BUR820150051-US-	US	15/015,535	2/4/2016	9653330	5/16/2017		Threshold Voltage (Vt)-Type Transistor Sensitive And/Or Fan-Out Sen
BUR820150056	BUR820150056-US-	US	15/001,763	1/20/2016	9967825	5/8/2018		Environmentally Aware Mobile Computing Devices
BUR820150061	BUR820150061-US-	US	15/019,590	2/9/2016				Memory Built-In Self-Test (Mbist) Test Reduction
BUR820150093	BUR820150093-CN-	China	201710063508.0				Not Provide	GIMBAL ASSEMBLY TEST SYSTEM AND METHOD
BUR820150093	BUR820150093-TW	Taiwan	106103210		I 639007	10/21/2018	Not Provide	GIMBAL ASSEMBLY TEST SYSTEM AND METHOD
BUR820150093	BUR820150093-US-	US	15/933,443	3/23/2018				Gimbal Assembly Test System And Method
BUR820150093	BUR820150093-US-	US	15/014,479	2/3/2016	10041976	8/7/2018		Gimbal Assembly Test System And Method
BUR820150094	BUR820150094-US-	US	14/992,426	1/11/2016	9654086	5/16/2017		Operational Amplifier With Current-Controlled Up Or Down Hysteres
BUR820150098	BUR820150098-US-	US	15/083,692	3/29/2016	9865486	1/9/2018		Timing/Power Risk Optimized Selective Voltage Binning Using Non- Li
BUR820150115	BUR820150115-US-	US	15/142,511	4/29/2016	9996648	6/12/2018		Netlist Editing Of Graphical Data
BUR820150121	BUR820150121-US-	US	15/012,331	2/1/2016	9760673	9/12/2017		Application Specific Integrated Circuit (Asic) Test Screens And Selecti
BUR919960066	BUR919960066US1	US	08/705,871	8/28/1996	5768152	6/16/1998		
BUR919960079	BUR919960079JP1	Japan	19970294010		4008989	9/7/2007	Not Provide	SELF REGULATING TEMPERATURE/PERFORMANCE/VOLTAGE SCHEM
BUR919960079	BUR919960079US1	US	08/772,706	12/23/1996	5832284	11/3/1998		
BUR919960155	BUR919960155US1	US	08/853,963	5/9/1997	6118318	9/12/2000		
BUR919960177	BUR919960177US1	US	08/803,056	2/19/1997	5757693	5/26/1998		

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BUR919970028	BUR919970028US1	US	08/833,367	4/4/1997	6070262	5/30/2000		
BUR919970057	BUR919970057US1	US	08/947,762	10/9/1997	5898623	4/27/1999		
BUR919970107	BUR919970107US1	US	09/056,903	4/7/1998	5945707	8/31/1999		
BUR919970149	BUR919970149US1	US	09/105,739	6/26/1998	6767789	7/27/2004		
BUR919970164	BUR919970164DE1	Germa	99303394.3		957490	3/16/2005	Not Provide	HIGH BANDWIDTH NARROW I/O MEMORY DEVICE WITH COMMAN
BUR919970164	BUR919970164EP1	Europe	99303394.3		957490	3/16/2005	Not Provide	HIGH BANDWIDTH NARROW I/O MEMORY DEVICE WITH COMMAN
BUR919970164	BUR919970164US1	US	09/079,572	5/15/1998	6065093	5/16/2000		
BUR919970169	BUR919970169US1	US	09/081,639	5/20/1998	6118719	9/12/2000		
BUR919970202	BUR919970202US1	US	09/121,933	7/24/1998	6178517	1/23/2001		
BUR919970219	BUR919970219TW	Taiwan	88102478		NI-128941	7/13/2001	Not Provide	DEVICE AND METHOD TO REDUCE POWER CONSUMPTION IN INTEC
BUR919970219	BUR919970219US1	US	09/159,861	9/24/1998	6097243	8/1/2000		
BUR919970220	BUR919970220US1	US	09/120,211	7/21/1998	6011383	1/4/2000		
BUR919970220	BUR919970220US2	US	09/159,898	9/24/1998	6097241	8/1/2000		
BUR919970231	BUR919970231US1	US	09/055,002	4/3/1998	6097655	8/1/2000		
BUR919970232	BUR919970232US1	US	09/092,412	6/5/1998	6057719	5/2/2000		
BUR919980122	BUR919980122US1	US	09/283,960	4/1/1999	6177817	1/23/2001		
BUR919980123	BUR919980123US1	US	09/224,766	1/4/1999	6157530	12/5/2000		
BUR919980148	BUR919980148US1	US	09/302,902	4/30/1999	6141245	10/31/2000		
BUR919990140	BUR919990140US1	US	09/524,661	3/13/2000	6282144	8/28/2001		Multi-Ported Memory With Asynchronous And Synchronous Protocol
BUR919990218	BUR919990218TW	Taiwan	89124582		NI-155053	8/27/2002	Not Provide	MACRO DESIGN TECHNIQUES TO ACCOMMODATE CHIP LEVEL WIRIN
BUR919990218	BUR919990218US1	US	09/526,198	3/15/2000	6543040	4/1/2003		Macro Design Techniques To Accommodate Chip Level Wiring And Ci
BUR919990240	BUR919990240CN1	China	00133852.8		ZL00133852.8	4/27/2005	Not Provide	WAFER INTEGRATED RIGID SUPPORT RING
BUR919990240	BUR919990240TW	Taiwan	89121973		NI-164171	2/19/2003	Not Provide	WAFER INTEGRATED RIGID SUPPORT RING
BUR919990276	BUR919990276US1	US	09/588,202	6/6/2000	6496037	12/17/2002		Automatic Off-Chip Driver Adjustment Based On Load Characteristics
BUR919990287	BUR919990287US1	US	09/669,117	9/25/2000	6658610	12/2/2003		Compilable Address Magnitude Comparator For Memory Array Self-T
BUR919990296	BUR919990296US1	US	09/757,267	1/9/2001	6420925	7/16/2002		Programmable Latch Device With Integrated Programmable Element
BUR920000036	BUR920000036US1	US	09/737,012	12/14/2000	6523154	2/18/2003		Method For Supply Voltage Drop Analysis During Placement Phase Of
BUR920000102	BUR920000102US1	US	09/733,295	12/8/2000	6496432	12/17/2002		Method And Apparatus For Testing A Write Function Of A Dual-Port S
BUR920000103	BUR920000103US1	US	09/768,122	1/23/2001	6708298	3/16/2004		Method For Guaranteeing A Minimum Data Strobe Valid Window An
BUR920000106	BUR920000106US1	US	09/690,674	10/17/2000	6555891	4/29/2003		Soi Hybrid Structure With
BUR920000109	BUR920000109US1	US	09/862,427	5/21/2001	6832361	12/14/2004		System And Method For Analyzing Power Distribution Using Static Ti
BUR920000111	BUR920000111CN1	China	2001142599		ZL01142599.7	2/9/2005	Not Provide	METHOD AND APPARATUS FOR INITIALIZING AN INTEGRATED CIRCUI
BUR920000111	BUR920000111TW	Taiwan	90129655		NI-177947	9/9/2003	Not Provide	METHOD AND APPARATUS FOR INITIALIZING AN INTEGRATED CIRCUI
BUR920000111	BUR920000111US1	US	09/731,147	12/5/2000	6577156	6/10/2003		Method And Apparatus For Initializing An Integrated Circuit Using Co

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BUR920000126	BUR920000126US1	US	09/810,763	3/16/2001	6470476	10/22/2002		Substitution Of Non-Minimum Groundrule Cells For Non-Critical Mini
BUR920000152	BUR920000152US1	US	09/791,003	2/22/2001	6754864	6/22/2004		System And Method To Predetermine A Bitmap Of A Self-Tested Em
BUR920010028	BUR920010028US1	US	09/683,808	2/19/2002	6621324	9/16/2003		Redundant Antifuse Segments For Improved Programming Efficienc
BUR920010115	BUR920010115US1	US	09/682,638	10/1/2001	6430072	8/6/2002		Embedded Cam Test Structure For Fully Testing All Matchlines
BUR920010121	BUR920010121US1	US	10/063,394	4/18/2002	6990644	1/24/2006		On Chip Timing Adjustment In Multi-Channel Fast Data Transfer
BUR920010125	BUR920010125US1	US	10/065,475	10/22/2002	6930506	8/16/2005		Terminating Resistor Driver For High Speed Data Communication
BUR920010182	BUR920010182US1	US	10/063,212	3/29/2002	6778419	8/17/2004		Complementary Two Transistor Rom Cell
BUR920010192	BUR920010192US1	US	10/063,427	4/23/2002	6823496	11/23/2004		Physical Design Characterization System
BUR920010202	BUR920010202US1	US	10/063,497	4/30/2002	7149941	12/12/2006		Optimized Ecc/Redundancy Fault Recovery
BUR920010204	BUR920010204US1	US	10/064,921	8/29/2002	6794901	9/21/2004		Apparatus For Reducing Soft Errors In Dynamic Circuits
BUR920010216	BUR920010216TW	Taiwan	92109727		I222078	10/11/2004	Not Provide	Testing of ECC Memories
BUR920010216	BUR920010216US1	US	10/063,495	4/30/2002	7308621	12/11/2007		Testing Of Ecc Memories
BUR920020022	BUR920020022US1	US	10/063,504	5/1/2002	6731154	5/4/2004		Global Voltage Buffer For Voltage Islands
BUR920020025	BUR920020025EP1-	Germa	02795913.9		1573553	4/1/2016	Not Provide	PACE - Dynamic Variable Line Width Cache
BUR920020025	BUR920020025US1	US	11/160,184	8/19/2005	7406579	7/29/2008		Selectively Changeable Line Width Memory
BUR920020046	BUR920020046US1	US	10/249,311	3/31/2003	6731134	5/4/2004		Tri-State Delay Boost
BUR920020065	BUR920020065US1	US	10/063,859	5/20/2002	7020857	3/28/2006		Method And Apparatus For Providing Noise Suppression In An Integr
BUR920020079	BUR920020079US1	US	10/249,684	4/30/2003	6825711	11/30/2004		Power Reduction By Stage In Integrated Circuit
BUR920020080	BUR920020080US1	US	10/265,591	10/7/2002	6768694	7/27/2004		Method Of Electrically Blowing Fuses Under Control Of An On-Chip T
BUR920020085	BUR920020085CN1	China	2.0041009122e-	11/17/2004	ZL2004100912	9/14/2011	Not Provide	MEMORY DEVICE WITH PROGRAMMABLE RECEIVERS TO IMPROVE P
BUR920020085	BUR920020085US1	US	10/707,053	11/18/2003	7646649	1/12/2010		Memory Device With Programmable Receivers To Improve Performa
BUR920020086	BUR920020086US1	US	10/249,273	3/27/2003	6826113	11/30/2004		Synchronous Dynamic Random Access Memory Device Having Memo
BUR920020088	BUR920020088EP1	Europe	2797402.1		1620857	11/11/2009	Not Provide	Enabling Memory Redundancy During Testing
BUR920020088	BUR920020088US1	US	11/160,268	6/16/2005	7304901	12/4/2007		Enabling Memory Redundancy During Testing
BUR920020092	BUR920020092CN1	China	2.0048001924e-		ZL2004800192	2/24/2010	Not Provide	Nested Voltage Island Architecture
BUR920020092	BUR920020092EP1	Europe	20040777801		1649493	3/16/2016	Not Provide	PACE - Nested Voltage Island Architecture
BUR920020092	BUR920020092US1	US	10/604,277	7/8/2003	7131074	10/31/2006		Nested Voltage Island Architecture
BUR920020097	BUR920020097US1	US	10/707,963	1/28/2004	7061304	6/13/2006		Fuse Latch With Compensated Programmable Resistive Trip Point
BUR920020137	BUR920020137US1	US	10/248,302	1/7/2003	6986116	1/10/2006		Signal Balancing Between Voltage Domains
BUR920020148	BUR920020148US1	US	10/604,186	6/30/2003	6897674	5/24/2005		Adaptive Integrated Circuit Based On Transistor Current Measureme
BUR920030013	BUR920030013US1	US	10/604,095	6/26/2003	6882230	4/19/2005		System And Method For Control Parameter Re-Centering In A Control
BUR920030017	BUR920030017US1	US	10/249,331	4/1/2003	7480774	1/20/2009		Method For Performing A Command Cancel Function In A Dram
BUR920030099	BUR920030099US1	US	10/729,751	12/4/2003	7287177	10/23/2007		Digital Reliability Monitor Having Autonomic Repair And Notification
BUR920030103	BUR920030103US1	US	10/651,874	8/29/2003	7305600	12/4/2007		Partial Good Integrated Circuit And Method Of Testing Same

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BUR920030134	BUR920030134US1	US	10/707,121	11/21/2003	6940324	9/6/2005		Variation Of Effective Filter Capacitance In Phase Lock Loop Circuit Lo
BUR920030147	BUR920030147CN1	China	2.0051005483e-		ZL2005100548	7/2/2008	Not Provide	Phase Change Memory Cell on Silicon-On Insulator Substrate
BUR920030147	BUR920030147US1	US	10/708,667	3/18/2004	7005665	2/28/2006		Phase Change Memory Cell On Silicon-On Insulator Substrate
BUR920030171	BUR920030171US1	US	10/708,317	2/24/2004	7120732	10/10/2006		Content Addressable Memory Structure
BUR920030171	BUR920030171US2	US	11/533,125	9/19/2006	7337268	2/26/2008		Content Addressable Memory Structure
BUR920030171	BUR920030171US3	US	11/850,840	9/6/2007	7464217	12/9/2008		Design Structure For Content Addressable Memory
BUR920040001	BUR920040001US1	US	10/709,754	5/26/2004	7373567	5/13/2008		System And Method Of Providing Error Detection And Correction Ca
BUR920040011	BUR920040011CN1	China	2.0051011562e-		ZL2005101156	7/23/2008	Not Provide	METHOD AND APPARATUS FOR CONVERTING GLOBALLY CLOCK-GA
BUR920040013	BUR920040013CN1	China	2.0058002936e-		ZL2005800293	2/29/2012	Not Provide	LOW VOLTAGE PROGRAMMABLE eFUSE WITH DIFFERENTIAL SENSII
BUR920040013	BUR920040013DE1	Germa	20050794850		1800323	11/16/2011	Not Provide	LOW VOLTAGE PROGRAMMABLE eFUSE WITH DIFFERENTIAL SENSII
BUR920040013	BUR920040013EP1	Europe	5794850.7		1800323	11/16/2011	Not Provide	LOW VOLTAGE PROGRAMMABLE eFUSE WITH DIFFERENTIAL SENSII
BUR920040013	BUR920040013TW	Taiwan	94129638		I392076	4/1/2013	Not Provide	LOW VOLTAGE PROGRAMMABLE eFUSE WITH DIFFERENTIAL SENSII
BUR920040013	BUR920040013US1	US	10/711,205	9/1/2004	7098721	8/29/2006		Low Voltage Programmable Efuse With Differential Sensing Scheme
BUR920040024	BUR920040024CN1	China	2.0051005629e-		ZL2005100562	3/25/2009	Not Provide	Low Leakage Monotonic CMOS Logic
BUR920040024	BUR920040024TW	Taiwan	94123273		I348273	9/1/2011	Not Provide	Low Leakage Monotonic CMOS Logic
BUR920040024	BUR920040024US1	US	10/710,453	7/13/2004	7084667	8/1/2006		Low Leakage Monotonic Cmos Logic
BUR920040027	BUR920040027CN1	China	2.005100653e+:		ZL2005100652	2/18/2009	Not Provide	Programmable Frequency Divider With Symmetrical Output
BUR920040027	BUR920040027US1	US	10/709,804	5/28/2004	6961403	11/1/2005		Programmable Frequency Divider With Symmetrical Output
BUR920040045	BUR920040045US1	US	10/710,681	7/28/2004	7106110	9/12/2006		Clock Dithering System And Method During Frequency Scaling
BUR920040074	BUR920040074US1	US	10/709,362	4/29/2004	7280939	10/9/2007		System And Method Of Analyzing Timing Effects Of Spatial Distributio
BUR920040077	BUR920040077US1	US	10/710,745	7/30/2004	7053712	5/30/2006		Method And Apparatus For Controlling Common-Mode Output Volta
BUR920040078	BUR920040078US1	US	10/906,508	2/23/2005	8595557	11/26/2013		Method And Apparatus For Verifying Memory Testing Software
BUR920040133	BUR920040133US1	US	10/904,309	11/3/2004	7401307	7/15/2008		Slack Sensitivity To Parameter Variation Based Timing Analysis
BUR920040183	BUR920040183US1	US	10/906,343	2/15/2005	7404114	7/22/2008		System And Method For Balancing Delay Of Signal Communication Pa
BUR920040195	BUR920040195US1	US	10/906,147	2/4/2005	7404125	7/22/2008		Compilable Memory Structure And Test Methodology For Both Asic A
BUR920040209	BUR920040209US1	US	10/906,407	2/18/2005	7240266	7/3/2007		Clock Control Circuit For Test That Facilitates An At Speed Structural
BUR920040268	BUR920040268US1	US	10/908,033	4/26/2005	7251756	7/31/2007		Method And Apparatus For Increasing Fuse Programming Yield Throu
BUR920040268	BUR920040268US2	US	11/775,531	7/10/2007	7895028	2/22/2011		Structure For Increasing Fuse Programming Yield
BUR920040273	BUR920040273US1	US	11/161,742	8/15/2005	7352609	4/1/2008		Voltage Controlled Static Random Access Memory
BUR920040273	BUR920040273US2	US	11/763,687	6/15/2007	7466582	12/16/2008		Voltage Controlled Static Random Access Memory
BUR920050034	BUR920050034US1	US	10/908,084	4/27/2005	7348210	3/25/2008		Post Bump Passivation For Soft Error Protection
BUR920050037	BUR920050037CN1	China	2.0068003559e-		ZL2006800355	6/16/2010	Not Provide	CIRCUIT DESIGN VERIFICATION USING CHECKPOINTING
BUR920050037	BUR920050037US1	US	11/162,846	9/26/2005	7308663	12/11/2007		Circuit Design Verification Using Checkpointing
BUR920050081	BUR920050081US1	US	11/163,800	10/31/2005	7286425	10/23/2007		System And Method For Capacitive Mis-Match Bit-Line Sensing

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BUR920050082	BUR920050082US1	US	11/623,112	1/15/2007	7573300	8/11/2009		Current Control Mechanism For Dynamic Logic Keeper Circuits In An I
BUR920050087	BUR920050087US1	US	11/095,327	3/31/2005	7142991	11/28/2006		Voltage Dependent Parameter Analysis
BUR920050118	BUR920050118US1	US	11/162,997	9/30/2005	7282949	10/16/2007		Fpga Powerup To A Known Functional State
BUR920050120	BUR920050120US1	US	11/275,035	12/5/2005	7444258	10/28/2008		Automated Simulation Testbench Generation For Serializer / Deseri
BUR920050127	BUR920050127US1	US	11/276,413	2/28/2006	7490280	2/10/2009		Microcontroller For Logic Built-In Self Test (Lbist)
BUR920050149	BUR920050149US1	US	11/276,451	2/28/2006	7449942	11/11/2008		Intrinsic Rc Power Distribution For Noise Filtering Of Analog Supplie
BUR920050173	BUR920050173US1	US	12/183,099	7/31/2008	8077534	12/13/2011		Adaptive Noise Suppression Using A Noise Look-Up Table
BUR920050180	BUR920050180US3	US	11/946,096	11/28/2007	8037337	10/11/2011		Structures Including Circuits For Noise Reduction In Digital Systems
BUR920050200	BUR920050200US1	US	11/424,961	6/19/2006	7459958	12/2/2008		Circuits To Reduce Threshold Voltage Tolerance And Skew In Multi-'
BUR920050206	BUR920050206US1	US	11/276,380	2/27/2006	7256503	8/14/2007		Chip Underfill In Flip-Chip Technologies
BUR920050214	BUR920050214US1	US	11/275,482	1/9/2006	7470959	12/30/2008		Integrated Circuit Structures For Preventing Charging Damage
BUR920050226	BUR920050226US1	US	11/277,315	3/23/2006	7403061	7/22/2008		Method Of Improving Fuse State Detection And Yield In Semicondu
BUR920050234	BUR920050234US1	US	12/135,249	6/9/2008	7821294	10/26/2010		Integrated Circuit Containing Multi-State Restore Circuitry For Resto
BUR920050235	BUR920050235US1	US	11/277,385	3/24/2006	7404163	7/22/2008		Static Timing Slacks Analysis And Modification
BUR920050237	BUR920050237US2	US	11/851,138	9/6/2007	7839206	11/23/2010		Design Structure For Low Voltage Applications In An Integrated Circui
BUR920050246	BUR920050246US1	US	11/278,169	3/31/2006	7307457	12/11/2007		Apparatus For Implementing Dynamic Data Path With Interlocked Ke
BUR920050271	BUR920050271CN1	China	2.0071013996e-		ZL2007101399	12/9/2009	Not Provide	CONFIGURABLE SRAM SYSTEM AND METHOD
BUR920050271	BUR920050271US1	US	11/463,917	8/11/2006	7450413	11/11/2008		Configurable Sram System And Method
BUR920050271	BUR920050271US2	US	11/947,092	11/29/2007	7602635	10/13/2009		Structure For A Configurable Sram System And Method
BUR920050293	BUR920050293US2	US	11/869,216	10/9/2007	8086832	12/27/2011		Structure For Dynamically Adjusting Pipelined Data Paths For Improv
BUR920050304	BUR920050304CN1	China	2.0071008982e-		ZL2007100898	7/15/2009	Not Provide	DUAL-SIDED CHIP ATTACHED MODULES
BUR920050304	BUR920050304US1	US	11/383,595	5/16/2006	7462509	12/9/2008		Dual-Sided Chip Attached Modules
BUR920060018	BUR920060018US1	US	11/457,477	7/14/2006	7724559	5/25/2010		Self-Referenced Match-Line Sense Amplifier For Content Addressable
BUR920060018	BUR920060018US2	US	11/763,669	6/15/2007	7751218	7/6/2010		Self-Referenced Match-Line Sense Amplifier For Content Addressable
BUR920060040	BUR920060040CN1	China	2.0098011554e-		ZL2009801155	8/6/2014	Not Provide	Method and Apparatus for Implementing Self-Referencing Read Oper
BUR920060040	BUR920060040TW	Taiwan	98113933		I462098	11/21/2014	Not Provide	%MPG% METHOD AND APPARATUS FOR IMPLEMENTING SELF-REFER
BUR920060040	BUR920060040US1	US	12/112,454	4/30/2008	7660152	2/9/2010		Method And Apparatus For Implementing Self-Referencing Read Ope
BUR920060052	BUR920060052US1	US	12/135,250	6/9/2008	8239791	8/7/2012		Method Of Designing Multi-State Restore Circuitry For Restoring Stat
BUR920060053	BUR920060053US1	US	11/279,639	4/13/2006	7397718	7/8/2008		Determining Relative Amount Of Usage Of Data Retaining Device Bas
BUR920060067	BUR920060067CN1	China	2.0071013902e-		ZL2007101390	8/17/2011	Not Provide	(e-Docket)A SYSTEM FOR ACQUIRING DEVICE PARAMETERS
BUR920060067	BUR920060067US1	US	11/459,367	7/24/2006	7382149	6/3/2008		System For Acquiring Device Parameters
BUR920060072	BUR920060072CN1	China	2.0071016942e-		ZL2007101694	6/23/2010	Not Provide	TUNABLE CAPACITOR
BUR920060097	BUR920060097US1	US	11/550,573	10/18/2006	7732949	6/8/2010		System For Method Of Predicting Power Events In An Intermittent Po
BUR920060127	BUR920060127US1	US	11/684,655	3/12/2007	7692954	4/6/2010		Apparatus And Method For Integrating Nonvolatile Memory Capabilit

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BUR920060129	BUR920060129US1	US	11/743,686	5/3/2007	7408800	8/5/2008		Apparatus And Method For Improved Sram Device Performance Thro
BUR920060136	BUR920060136US1	US	11/621,248	1/9/2007	7432730	10/7/2008		Time Based Driver Output Transition (Slew) Rate Compensation
BUR920060136	BUR920060136US2	US	12/053,946	3/24/2008	8115508	2/14/2012		Structure For Time Based Driver Output Transition (Slew) Rate Com
BUR920060143	BUR920060143US1	US	11/621,175	1/9/2007	7545165	6/9/2009		System Architectures For And Methods Of Scheduling On-Chip And
BUR920060144	BUR920060144US1	US	11/670,537	2/2/2007	7403039	7/22/2008		Flexible Multimode Logic Element For Use In A Configurable Mixed-
BUR920060144	BUR920060144US2	US	11/848,470	8/31/2007	7429877	9/30/2008		Design Structure For A Flexible Multimode Logic Element For Use In
BUR920060186	BUR920060186US1	US	11/613,516	12/20/2006	7362138	4/22/2008		Flexible Multimode Logic Element For Use In A Configurable Mixed-
BUR920060186	BUR920060186US3	US	12/494,642	6/30/2009	8302037	10/30/2012		Skewed Double Differential Pair Circuit For Offset Cancellation
BUR920060190	BUR920060190US1	US	11/949,066	12/3/2007	8196088	6/5/2012		Method And Structure For Screening Nfet-To-Pfet Device Performa
BUR920060211	BUR920060211US1	US	11/949,063	12/3/2007	7848128	12/7/2010		Apparatus And Method For Implementing Matrix-Based Search Cap
BUR920060211	BUR920060211US2	US	12/110,375	4/28/2008	7859878	12/28/2010		Design Structure For Implementing Matrix-Based Search Capability
BUR920060214	BUR920060214US1	US	11/760,856	6/11/2007	7932641	4/26/2011		Low Voltage Head Room Detection For Reliable Start-Up Of Self-Bia
BUR920070010	BUR920070010US2	US	11/937,111	11/8/2007	8132136	3/6/2012		Dynamic Critical Path Detector For Digital Logic Circuit Paths
BUR920070029	BUR920070029US1	US	11/941,994	11/19/2007	7609569	10/27/2009		System And Method For Implementing Row Redundancy With Reduc
BUR920070045	BUR920070045US1	US	11/949,904	12/4/2007	8024513	9/20/2011		Method And System For Implementing Dynamic Refresh Protocols Fo
BUR920070045	BUR920070045US2	US	12/126,499	5/23/2008	8108609	1/31/2012		Structure For Implementing Dynamic Refresh Protocols For Dram Bas
BUR920070058	BUR920070058US1	US	11/779,432	7/18/2007	8055925	11/8/2011		Structure And Method To Optimize Computational Efficiency In Low-
BUR920070058	BUR920070058US2	US	11/870,575	10/11/2007	8122273	2/21/2012		Structure And Method To Optimize Computational Efficiency In Low-
BUR920070080	BUR920070080US1	US	12/032,798	2/18/2008	7643357	1/5/2010		System And Method For Integrating Dynamic Leakage Reduction Wit
BUR920070088	BUR920070088US1	US	11/869,179	10/9/2007	8291357	10/16/2012		On-Chip Identification Circuit Incorporating Pairs Of Conductors, Each
BUR920070109	BUR920070109US1	US	11/949,068	12/3/2007	7646648	1/12/2010		Apparatus And Method For Implementing Memory Array Device Wit
BUR920070109	BUR920070109US2	US	12/110,456	4/28/2008	8117567	2/14/2012		Structure For Implementing Memory Array Device With Built In Com
BUR920070114	BUR920070114US1	US	11/850,488	9/5/2007	7825469	11/2/2010		Threshold Voltage Compensation For Pixel Design Of Cmos Image Se
BUR920070137	BUR920070137US1	US	12/014,240	1/15/2008	8181148	5/15/2012		Method For Identifying And Implementing Flexible Logic Block Logic F
BUR920070137	BUR920070137US2	US	12/054,835	3/25/2008	8141028	3/20/2012		Structure For Identifying And Implementing Flexible Logic Block Logic
BUR920070157	BUR920070157US1	US	12/048,237	3/14/2008	8233342	7/31/2012		Apparatus And Method For Implementing Write Assist For Static Ran
BUR920070167	BUR920070167US1	US	12/030,903	2/14/2008	8108817	1/31/2012		Semiconductor Structure And Method Of Designing Semiconductor S
BUR920070202	BUR920070202US1	US	12/109,379	4/25/2008	7903493	3/8/2011		Design Structure For Estimating And/Or Predicting Power Cycle Lengt
BUR920070204	BUR920070204US1	US	12/057,686	3/28/2008	8035452	10/11/2011		Structure For Transforming An Input Voltage To Obtain Linearity Bet
BUR920070235	BUR920070235US1	US	12/055,336	3/26/2008	8193893	6/5/2012		Inductor Having Opening Enclosed Within Conductive Line And Relat
BUR920070241	BUR920070241US1	US	12/053,705	3/24/2008	8239811	8/7/2012		System And Method For Wireless And Dynamic Intra-Process Measur
BUR920070276	BUR920070276US1	US	12/342,228	12/23/2008	8039868	10/18/2011		Structure And Method For An Electrostatic Discharge (Esd) Silicon Co
BUR920070276	BUR920070276US3	US	13/686,422	11/27/2012	8637900	1/28/2014		Electrostatic Discharge (Esd) Silicon Controlled Rectifier (Scr) Structur
BUR920070276	BUR920070276US4	US	14/079,048	11/13/2013	9029206	5/12/2015		Electrostatic Discharge (Esd) Silicon Controlled Rectifier (Scr) Structur

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
BUR920080006	BUR920080006US1	US	12/147,670	6/27/2008	8214699	7/3/2012		Circuit Structure And Method For Digital Integrated Circuit Performan
BUR920080015	BUR920080015US1	US	12/135,229	6/9/2008	7859921	12/28/2010		Apparatus And Method For Low Power Sensing In A Multi-Port Sran
BUR920080016	BUR920080016US1	US	12/135,237	6/9/2008	7830727	11/9/2010		Apparatus And Method For Low Power, Single-Ended Sensing In A M
BUR920080031	BUR920080031US1	US	12/098,038	4/4/2008	7529962	5/5/2009		System For Expanding A Window Of Valid Data
BUR920080032	BUR920080032US1	US	12/034,161	2/20/2008	8103998	1/24/2012		Verifying Non-Deterministic Behavior Of A Design Under Test
BUR920080078	BUR920080078US1	US	12/192,537	8/15/2008	7781283	8/24/2010		Split-Gate Dram With Mugfet, Design Structure, And Method Of Ma
BUR920080078	BUR920080078US2	US	12/192,554	8/15/2008	7979836	7/12/2011		Split-Gate Dram With Mugfet, Design Structure, And Method Of Ma
BUR920080079	BUR920080079US3	US	12/538,196	8/10/2009	8184472	5/22/2012		Split-Gate Dram With Lateral Control-Gate Mugfet
BUR920080090	BUR920080090US1	US	12/046,608	3/12/2008	7958477	6/7/2011		Structure, Failure Analysis Tool And Method Of Determining White
BUR920080110	BUR920080110US1	US	12/144,686	6/24/2008	8239715	8/7/2012		Method And Apparatus For A Robust Embedded Interface
BUR920080111	BUR920080111US2	US	12/193,058	8/18/2008	8095902	1/10/2012		Design Structure For Couple Noise Characterization Using A Single C
BUR920080199	BUR920080199US1	US	12/344,711	12/29/2008	8916464	12/23/2014		Structures And Methods For Improving Solder Bump Connections Ir
BUR920080199	BUR920080199US2	US	14/522,664	10/24/2014	9087754	7/21/2015		Structures And Methods For Improving Solder Bump Connections Ir
BUR920080208	BUR920080208US1	US	12/192,571	8/15/2008	8189723	5/29/2012		Method, Circuit, And Design Structure For Capturing Data Across A Ps
BUR920080208	BUR920080208US2	US	12/192,586	8/15/2008	8300752	10/30/2012		Method, Circuit, And Design Structure For Capturing Data Across A Ps
BUR920080231	BUR920080231US2	US	14/200,613	3/7/2014	9269683	2/23/2016		Integrated Circuit Chip With Pyramid Or Cone-Shaped Conductive Pa
BUR920080245	BUR920080245US2	US	12/632,015	12/7/2009	8363367	1/29/2013		Electrical Overstress Protection Circuit
BUR920080324	BUR920080324US1	US	12/639,454	12/16/2009	8027207	9/27/2011		Leakage Compensated Reference Voltage Generation System
BUR920080326	BUR920080326US1	US	12/882,425	9/15/2010	8188765	5/29/2012		Circuit And Method For Asynchronous Pipeline Processing With Varia
BUR920080358	BUR920080358US1	US	12/862,310	8/24/2010	8521500	8/27/2013		Method And Device For Measuring Integrated Circuit Power Supply N
BUR920080447	BUR920080447US1	US	12/779,608	5/13/2010	8279687	10/2/2012		Single Supply Sub Vdd Bit-Line Precharge Sram And Method For Level
BUR920090025	BUR920090025KR1	Korea	2010-0100230		1625232	5/23/2016	Not Provide	DYNAMICALLY RECONFIGURABLE SELF-MONITORING CIRCUIT
BUR920090025	BUR920090025US1	US	12/605,417	10/26/2009	8407633	3/26/2013		Dynamically Reconfigurable Self-Monitoring Circuit
BUR920090028	BUR920090028US1	US	12/579,089	10/14/2009	8218378	7/10/2012		Word-Line Level Shift Circuit
BUR920090029	BUR920090029CN1	China	201080047089.1		ZL2010800470	8/3/2015	Not Provide	SRAM DELAY CIRCUIT THAT TRACKS BITCELL CHARACTERISTICS
BUR920090029	BUR920090029DE1	Germa	20101103722T		1.1201000372	2/19/2015	Not Provide	Sram - units with a delay circuit, in order to represent characteristics
BUR920090029	BUR920090029US1	US	12/581,440	10/19/2009	8233337	7/31/2012		Sram Delay Circuit That Tracks Bitcell Characteristics
BUR920090034	BUR920090034US1	US	12/576,275	10/9/2009	8130525	3/6/2012		Method And Apparatus For Configuring A Content-Addressable Mem
BUR920090039	BUR920090039US2	US	14/151,866	1/10/2014	9646125	5/9/2017		Method For Conversion Of Commercial Microprocessor To Radiation-
BUR920090040	BUR920090040US1	US	12/560,593	9/16/2009	7920003	4/5/2011		Delay Circuit With Delay Equal To Percentage Of Input Pulse Width
BUR920090041	BUR920090041US1	US	12/686,457	1/13/2010	8560990	10/15/2013		Method Of Managing Electro Migration In Logic Design And Design St
BUR920090042	BUR920090042US1	US	12/615,856	11/10/2009	8381052	2/19/2013		Circuit And Method For Efficient Memory Repair
BUR920090042	BUR920090042US2	US	13/750,497	1/25/2013	8612813	12/17/2013		Circuit And Method For Efficient Memory Repair
BUR920090043	BUR920090043US1	US	12/686,013	1/12/2010	8300489	10/30/2012		Charge Pump System And Method Utilizing Adjustable Output Charge

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BUR920090068	BUR920090068US1	US	12/782,359	5/18/2010	8302063	10/30/2012		Method And System To Optimize Semiconductor Products For Power
BUR920090069	BUR920090069US1	US	12/770,976	4/30/2010	8238168	8/7/2012		VDD Pre-Set Of Direct Sense Dram
BUR920090076	BUR920090076US1	US	12/894,286	9/30/2010	8768679	7/1/2014		System And Method For Efficient Modeling Of Npskew Effects On St
BUR920090080	BUR920090080US1	US	12/845,390	7/28/2010	8415999	4/9/2013		High Frequency Quadrature Pll Circuit And Method
BUR920090080	BUR920090080US2	US	13/761,358	2/7/2013	8581648	11/12/2013		High Frequency Quadrature Pll Circuit And Method
BUR920090083	BUR920090083US1	US	12/877,628	9/8/2010	8233345	7/31/2012		Phase Change Memory Cycle Timer And Method
BUR920090085	BUR920090085US1	US	13/835,358	3/15/2013	9257352	2/9/2016		Semiconductor Test Wafer And Methods For Use Thereof
BUR920100023	BUR920100023US1	US	12/862,844	8/25/2010	8586982	11/19/2013		Semiconductor Test Chip Device To Mimic Field Thermal Mini-Cycle
BUR920100047	BUR920100047US1	US	12/892,160	9/28/2010	8228713	7/24/2012		Sram Having Wordline Up-Level Voltage Adjustable To Assist Bitcell
BUR920100064	BUR920100064DE1	Germa	112012000256.:				Not Provide	CIRCUIT FOR DETECTING STRUCTURAL DEFECTS IN AN INTEGRATED
BUR920100064	BUR920100064US2	US	14/713,626	5/15/2015	9599664	3/21/2017		Circuit For Detecting Structural Defects In An Integrated Circuit Chip
BUR920100081	BUR920100081CN1	China	2.0118005749e-		ZL2011800574	7/8/2015	Not Provide	STATIC RANDOM ACCESS MEMORY (SRAM) WRITE ASSIST CIRCUIT \
BUR920100081	BUR920100081US1	US	12/959,883	12/3/2010	8363453	1/29/2013		Static Random Access Memory (Sram) Write Assist Circuit With Leal
BUR920100109	BUR920100109US1	US	12/892,191	9/28/2010	8582351	11/12/2013		Methods And Systems For Adjusting Wordline Up-Level Voltage To I
BUR920110003	BUR920110003US1	US	13/080,055	4/5/2011	8239818	8/7/2012		Data Structure For Describing Mbist Architecture
BUR920110015	BUR920110015US1	US	13/076,781	3/31/2011	8415763	4/9/2013		Tunable Semiconductor Device
BUR920110025	BUR920110025US1	US	13/115,492	5/25/2011	8634174	1/21/2014		Gate Dielectric Breakdown Protection During Esd Events
BUR920110050	BUR920110050US1	US	13/198,894	8/5/2011	8467260	6/18/2013		Structure And Method For Storing Multiple Repair Pass Data Into A F
BUR920110059	BUR920110059US1	US	13/204,929	8/8/2011	8484543	7/9/2013		Fusebay Controller Structure, System, And Method
BUR920110060	BUR920110060US1	US	13/227,028	9/7/2011	8493250	7/23/2013		Comparator Offset Cancellation In A Successive Approximation Analo
BUR920110062	BUR920110062US1	US	13/223,949	9/1/2011	8537627	9/17/2013		Determining Fusebay Storage Element Usage
BUR920110063	BUR920110063US1	US	13/195,114	8/1/2011	8611164	12/17/2013		Device And Method For Detecting Resistive Defect
BUR920110066	BUR920110066US1	US	13/241,496	9/23/2011	8539404	9/17/2013		Functional Simulation Redundancy Reduction By State Comparison A
BUR920110086	BUR920110086US1	US	13/289,140	11/4/2011	8381161	2/19/2013		Method For Providing A Secure Gray Box" View Proprietary Ip"
BUR920110115	BUR920110115US1	US	13/288,269	11/3/2011	8421495	4/16/2013		Speed Binning For Dynamic And Adaptive Power Control
BUR920110139	BUR920110139CN1	China	2.0131000556e-		ZL 201310005!	3/9/2016	Not Provide	OFF-LINE GAIN CALIBRATION IN A TIME-INTERLEAVED ANALOG-TO-D
BUR920110139	BUR920110139US1	US	13/346,242	1/9/2012	8587464	11/19/2013		Off-Line Gain Calibration In A Time-Interleaved Analog-To-Digital Con
BUR920110141	BUR920110141US1	US	13/327,847	12/16/2011	8612815	12/17/2013		Asynchronous Circuit With An At-Speed Built-In Self-Test (Bist) Archit
BUR920110142	BUR920110142US1	US	13/403,252	2/23/2012	8654594	2/18/2014		Vdiff Max Limiter In Srams For Improved Yield And Power
BUR920110145	BUR920110145US1	US	13/311,832	12/6/2011	8560989	10/15/2013		Statistical Clock Cycle Computation
BUR920110148	BUR920110148US1	US	13/353,383	1/19/2012	8471595	6/25/2013		Selectable Dynamic/Static Latch With Embedded Logic
BUR920110157	BUR920110157US1	US	13/348,850	1/12/2012	8656325	2/18/2014		Integrted Circuit Design Method And System
BUR920110158	BUR920110158CN1	China	2.0121048345e-		ZL2012104834	1/27/2016	Not Provide	FINE GRANULARITY POWER GATING
BUR920110158	BUR920110158DE1	Germa	102012217578.4		102012217578	4/12/2016	Not Provide	FINE GRANULARITY POWER GATING

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BUR920110158	BUR920110158US1	US	13/315,604	12/9/2011	8611169	12/17/2013		Fine Granularity Power Gating
BUR920110159	BUR920110159DE1	Germa	102012221806.I		10 2012 221 8(	7/27/2018	Not Provide	DUAL POWER SUPPLY MEMORY ARRAY HAVING A CONTROL CIRCUI"
BUR920110159	BUR920110159US1	US	13/307,245	11/30/2011	8630139	1/14/2014		Dual Power Supply Memory Array Having A Control Circuit That Dyr
BUR920110186	BUR920110186US1	US	13/445,187	4/12/2012	8839054	9/16/2014		Read Only Memory (Rom) With Redundancy
BUR920110186	BUR920110186US2	US	14/453,779	8/7/2014	9460811	10/4/2016		Read Only Memory (Rom) With Redundancy
BUR920110187	BUR920110187US1	US	13/558,498	7/26/2012	8754696	6/17/2014		Ring Oscillator
BUR920110189	BUR920110189US1	US	13/426,776	3/22/2012	8575964	11/5/2013		Inactivity Triggered Self Clocking Logic Family
BUR920110195	BUR920110195US2	US	13/948,567	7/23/2013	9172371	10/27/2015		Majority Dominant Power Scheme For Repeated Structures And Str
BUR920110198	BUR920110198US1	US	13/468,268	5/10/2012	8648634	2/11/2014		Input Jitter Filter For A Phase-Locked Loop (Pll)
BUR920110212	BUR920110212CN1	China	2.0131005352e-		ZL 201310053!	3/30/2016	Not Provide	REFERENCE GENERATOR WITH PROGRAMMABLE M AND B PARAME
BUR920110212	BUR920110212US1	US	13/401,368	2/21/2012	8773920	7/8/2014		Reference Generator With Programmable M And B Parameters And
BUR920110228	BUR920110228US1	US	13/567,127	8/6/2012	8661399	2/25/2014		Built-In-Self-Test (Bist) Organizational File Generation
BUR920120012	BUR920120012US1	US	13/464,131	5/4/2012	8643987	2/4/2014		Current Leakage In Rc Esd Clamps
BUR920120035	BUR920120035US1	US	13/567,214	8/6/2012	8638149	1/28/2014		Equalized Rise And Fall Slew Rates For A Buffer
BUR920120042	BUR920120042US1	US	13/614,564	9/13/2012	9157956	10/13/2015		Adaptive Power Control Using Timing Canonicals
BUR920120045	BUR920120045US1	US	13/553,986	7/20/2012	9557378	1/31/2017		Method And Structure For Multi-Core Chip Product Test And Selectiv
BUR920120049	BUR920120049US1	US	13/564,337	8/1/2012	9429619	8/30/2016		Reliability Test Screen Optimization
BUR920120067	BUR920120067US1	US	13/676,063	11/13/2012	9188643	11/17/2015		Flexible Performance Screen Ring Oscillator Within A Scan Chain
BUR920120073	BUR920120073CN1	China	2.0138002691e-		ZL 201380026!	7/21/2017	Not Provide	CONTENT ADDRESSABLE MEMORY EARLY-PREDICT LATE-CORRECT SI
BUR920120073	BUR920120073DE1	Germa	20131102065T		112013002065	6/1/2017	Not Provide	Content addressable memory (content addressable memory) with pr
BUR920120073	BUR920120073IN1	India	8323/CHENP/20				Not Provide	CONTENT ADDRESSABLE MEMORY EARLY-PREDICT LATE-CORRECT SI
BUR920120073	BUR920120073US1	US	13/482,166	5/29/2012	9384835	7/5/2016		Content Addressable Memory Early-Predict Late-Correct Single Ende
BUR920120075	BUR920120075US1	US	13/732,713	1/2/2013	9270268	2/23/2016		Compensated Impedance Calibration Circuit
BUR920120075	BUR920120075US2	US	14/519,630	10/21/2014	9236863	1/12/2016		Compensated Impedance Calibration Circuit
BUR920120093	BUR920120093US1	US	13/626,040	9/25/2012	9310426	4/12/2016		On-Going Reliability Monitoring Of Integrated Circuit Chips In The Fiel
BUR920120105	BUR920120105CN1	China	2.0131044969e-		ZL 201310449(	9/22/2017	Not Provide	FERROELECTRIC RANDOM ACCESS MEMORY WITH OPTIMIZED HARD
BUR920120105	BUR920120105US1	US	13/628,715	9/27/2012	8796044	8/5/2014		Ferroelectric Random Access Memory With Optimized Hardmask
BUR920120138	BUR920120138CN1	China	201410002967.:		ZL 2014100025	6/1/2017	Not Provide	DESIGN STRUCTURE FOR AN INDUCTOR-CAPACITOR VOLTAGE-CONT
BUR920120138	BUR920120138US1	US	13/734,364	1/4/2013	8912854	12/16/2014		Structure For An Inductor-Capacitor Voltage-Controlled Oscillator
BUR920120138	BUR920120138US2	US	14/491,037	9/19/2014	9281779	3/8/2016		Structure For An Inductor-Capacitor Voltage-Controlled Oscillator
BUR920120177	BUR920120177CN1	China	2.0141003105e-		ZL 201410031(	1/5/2018	Not Provide	COMPLEMENTARY METAL-OXIDE-SEMICONDUCTOR (CMOS) DYNAM
BUR920120177	BUR920120177US1	US	13/747,529	1/23/2013	8934286	1/13/2015		Complementary Metal-Oxide-Semiconductor (Cmos) Dynamic Rando
BUR920120185	BUR920120185US1	US	13/888,108	5/6/2013	9177646	11/3/2015		Implementing Computational Memory From Content-Addressable M
BUR920120191	BUR920120191US2	US	14/515,597	10/16/2014	9245854	1/26/2016		Organic Module Emi Shielding Structures And Methods

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BUR920130013	BUR920130013CN1	China	2.0141011759e-		ZL2014101175	10/5/2016	Not Provide	SIGNAL MARGIN CENTERING FOR SINGLE-ENDED eDRAM SENSE AMP
BUR920130013	BUR920130013US1	US	13/851,202	3/27/2013	9093175	7/28/2015		Signal Margin Centering For Single-Ended Edram Sense Amplifier
BUR920130041	BUR920130041US1	US	13/955,401	7/31/2013	9286181	3/15/2016		Apparatus For Capturing Results Of Memory Testing
BUR920130052	BUR920130052US1	US	13/887,508	5/6/2013	9117547	8/25/2015		Reduced Stress High Voltage Word Line Driver
BUR920130054	BUR920130054US1	US	13/972,149	8/21/2013	9542981	1/10/2017		Self-Timed, Single-Ended Sense Amplifier
BUR920130057	BUR920130057US1	US	13/967,491	8/15/2013	9255962	2/9/2016		Determining Intra-Die Variation Of An Integrated Circuit
BUR920130074	BUR920130074US1	US	13/963,438	8/9/2013	9372520	6/21/2016		Reverse Performance Binning
BUR920130078	BUR920130078US1	US	13/958,754	8/5/2013	8879295	11/4/2014		Electronic Circuit For Remapping Faulty Memory Arrays Of Variable
BUR920130079	BUR920130079CN1	China	2.0141045235e-		ZL 201410452	8/18/2017	Not Provide	VERIFYING PARTIAL GOOD VOLTAGE ISLAND STRUCTURES
BUR920130079	BUR920130079US1	US	14/019,957	9/6/2013	9172373	10/27/2015		Verifying Partial Good Voltage Island Structures
BUR920130081	BUR920130081US2	US	14/886,177	10/19/2015	9331037	5/3/2016		Preventing Misshaped Solder Balls
BUR920130098	BUR920130098US1	US	14/059,486	10/22/2013	9190318	11/17/2015		Method Of Forming An Integrated Crackstop
BUR920130098	BUR920130098US2	US	14/876,889	10/7/2015	9397054	7/19/2016		Semiconductor Structure With An Interconnect Level Having A Conc
BUR920130110	BUR920130110US1	US	14/190,723	2/26/2014	9171125	10/27/2015		Limiting Skew Between Different Device Types To Meet Performance
BUR920130130	BUR920130130US1	US	14/026,158	9/13/2013	9159696	10/13/2015		Plug Via Formation By Patterned Plating And Polishing
BUR920130138	BUR920130138US2	US	14/708,755	5/11/2015	9185807	11/10/2015		Integrated Circuit Structures Having Off-Axis In-Hole Capacitor
BUR920130141	BUR920130141US1	US	14/185,079	2/20/2014	9262569	2/16/2016		Balancing Sensitivities With Respect To Timing Closure For Integrated
BUR920130145	BUR920130145US1	US	14/152,345	1/10/2014	9286980	3/15/2016		Converting An Xy Tcam To A Value Tcam
BUR920130146	BUR920130146US1	US	14/146,793	1/3/2014	9281023	3/8/2016		Single-Ended Sensing Circuits For Signal Lines
BUR920130163	BUR920130163US1	US	14/178,955	2/12/2014	9472269	10/18/2016		Stress Balancing Of Circuits
BUR920130177	BUR920130177US1	US	14/202,590	3/10/2014	9224091	12/29/2015		Learning Artificial Neural Network Using Ternary Content Addressabl
BUR920130183	BUR920130183US1	US	14/295,794	6/4/2014	9252717	2/2/2016		Phase Noise Reduction In Lc-Vco
BUR920140012	BUR920140012US1	US	14/546,318	11/18/2014				Scoped Search Engine
BUR920140019	BUR920140019US1	US	14/468,999	8/26/2014	9514844	12/6/2016		Fast Auto Shift Of Failing Memory Diagnostics Data Using Pattern Det
BUR920140037	BUR920140037US1	US	14/577,113	12/19/2014	9251890	2/2/2016		Bias Temperature Instability State Detection And Correction
BUR920140101	BUR920140101US1	US	14/620,273	2/12/2015	9618566	4/11/2017		Systems And Methods To Prevent Incorporation Of A Used Integrate
BUR920140102	BUR920140102US1	US	14/624,907	2/18/2015	9625325	4/18/2017		System And Method For Identifying Operating Temperatures And Mo
BUR920140104	BUR920140104US1	US	14/614,470	2/5/2015	9269407	2/23/2016		System And Method For Managing Circuit Performance And Power C
BUR920150001	BUR920150001US1	US	14/695,091	4/24/2015	9552447	1/24/2017		Systems And Methods For Controlling Integrated Circuit Chip Temper
BUR920150014	BUR920150014US1	US	14/819,784	8/6/2015	9437282	9/6/2016		High Performance Sense Amplifier
BUR920150022	BUR920150022US1	US	14/700,402	4/30/2015	9791502	10/17/2017		On-Chip Usable Life Depletion Meter And Associated Method
BUR920150023	BUR920150023US1	US	14/695,112	4/24/2015	9759767	9/12/2017		Pre-Test Power-Optimized Bin Reassignment Following Selective Volt
BUR920150024	BUR920150024US1	US	14/734,411	6/9/2015	9501607	11/22/2016		Composite Views For Ip Blocks In Asic Designs
BUR920150028	BUR920150028US1	US	14/734,504	6/9/2015	9601200	3/21/2017		Tcam Structures With Reduced Power Supply Noise

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BUR920150032	BUR920150032US1	US	14/832,127	8/21/2015	9570156	2/14/2017		Data Aware Write Scheme For Sram
BUR920150057	BUR920150057US1	US	14/918,149	10/20/2015	9761329	9/12/2017		Built-In Self-Test (Bist) Circuit And Associated Bist Method For Embe
BUR920150062	BUR920150062US1	US	14/751,222	6/26/2015	9519747	12/13/2016		Dynamic And Adaptive Timing Sensitivity During Static Timing Analy
BUR920150064	BUR920150064US1	US	14/922,323	10/26/2015	9390769	7/12/2016		Sense Amplifiers And Multiplexed Latches
BUR920150069	BUR920150069US1	US	14/742,801	6/18/2015	9639645	5/2/2017		Integrated Circuit Chip Reliability Using Reliability-Optimized Failure
BUR920150075	BUR920150075US1	US	14/922,256	10/26/2015	9740815	8/22/2017		Electromigration-Aware Integrated Circuit Design Methods And Sys
BUR920150079	BUR920150079US1	US	15/076,139	3/21/2016	10020047	7/10/2018		Static Random Access Memory (Sram) Write Assist Circuit With Imp
BUR920150080	BUR920150080US1	US	15/136,404	4/22/2016	10014074	7/3/2018		Failure Analysis And Repair Register Sharing For Memory Bist
BUR920150090	BUR920150090US1	US	14/862,652	9/23/2015	9619609	4/11/2017		Integrated Circuit Chip Design Methods And Systems Using Process
CH920030065U	CH920030065US1	US	10/720,974	11/24/2003	7295604	11/13/2007		Method For Determining Jitter Of A Signal In A Serial Link And High
CH920040024U	CH920040024US1	US	11/214,161	8/29/2005	7522687	4/21/2009		Clock And Data Recovery System And Method For Clock And Data R
CH920120050U	CH920120050US1	US	13/606,940	9/7/2012	8674737	3/18/2014		Clock Feathered Slew Rate Control System
CHQ919990017	CHQ919990017EP1	Europe	101146.9		1024599	4/5/2006	Not Provide	DELAY-LOCKED-LOOP (DLL) HAVING SYMMETRICAL RISING AND FAL
CHQ919990017	CHQ919990017US1	US	09/239,487	1/28/1999	6127866	10/3/2000		
CN820150610-	CN820150610-CN-N	China	2.0161110145e-				Not Provide	Ethernet Physical Layer Device Having An Integrated Physical Coding
CN820150610-	CN820150610-US-N	US	15/336,974	10/28/2016	10411832	9/10/2019		Ethernet Physical Layer Device Having Integrated Physical Coding An
CN920090009C	CN920090009CN1	China	2.0091013697e-		ZL2009101369	7/25/2012	Not Provide	Method for detecting contradictory timing constraints
CN920090009U	CN920090009US1	US	12/767,068	4/26/2010	8302048	10/30/2012		Method For Detecting Contradictory Timing Constraint Conflicts
CN920100115C	CN920100115CN1	China	2.0101057702e-		ZL2010105770	6/11/2014	Not Provide	Method and Apparatus for Timing Improvement based on Delay Vari
CN920100115U	CN920100115US1	US	13/296,555	11/15/2011	8769470	7/1/2014		Timing Closure In Chip Design
CN920100119C	CN920100119CN1	China	2.0101061233e-		ZL2010106123	7/9/2014	Not Provide	Delay Weighted Clock Tree Branching Optimization Method
CN920100119U	CN920100119US1	US	13/325,102	12/14/2011	8484604	7/9/2013		Constructing A Clock Tree For An Integrated Circuit Design
CN920130127C	CN920130127CN1	China	2.013105302e+(		ZL 201310530(	9/18/2018	Not Provide	METHOD AND APPARATUS FOR SIMULATING A DIGITAL CIRCUIT
CN920130127U	CN920130127US1	US	14/524,413	10/27/2014	10146895	12/4/2018		Method And Apparatus For Simulating A Digital Circuit
CN920130180U	CN920130180US1	US	14/633,477	2/27/2015	9727684	8/8/2017		Method And Apparatus For Physical-Aware Hold Violation Fixing
CN920150040U	CN920150040US1	US	14/755,621	6/30/2015	9820248	11/14/2017		Network Clock Synchronization
DE920080004U	DE920080004US2	US	12/236,551	9/24/2008	8090929	1/3/2012		Generating Clock Signals For Coupled Asic Chips In Processor Interfac
END919970039	END919970039US1	US	08/936,032	9/23/1997	6074895	6/13/2000		
END919970148	END919970148US1	US	09/018,698	2/4/1998	6326696	12/4/2001		
END919990128	END919990128US1	US	09/501,393	2/9/2000	6229754	5/8/2001		Write Through Function For A Memory
END920000025	END920000025US1	US	09/795,610	2/27/2001	6531410	3/11/2003		Intrinsic Dual Gate Oxide Mosfet Using A Damascene Gate Process
END920010026	END920010026CN1	China	02812341.7		ZL02812341.7	9/13/2006	Not Provide	EXTENSION OF FATIGUE LIFE FOR C4 SOLDER BALL TO CHIP CONNECT
END920010026	END920010026TW	Taiwan	91112657		567595	12/21/2003	Not Provide	EXTENSION OF FATIGUE LIFE FOR C4 SOLDER BALL TO CHIP CONNECT
END920010026	END920010026US1	US	09/885,853	6/20/2001	7067916	6/27/2006		Extension Of Fatigue Life For C4 Solder Ball To Chip Connection

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END920030035	END920030035US1	US	10/661,050	9/11/2003	6917662	7/12/2005		Programmable Low-Power High-Frequency Divider
END920030035	END920030035US3	US	11/325,786	1/5/2006	7342429	3/11/2008		Programmable Low-Power High-Frequency Divider
FIS820140038	FIS820140038-US-N	US	15/048,066	2/19/2016	9754071	9/5/2017		Integrated Circuit (ic) Design Analysis And Feature Extraction
FIS820150197	FIS820150197-US-N	US	15/176,598	6/8/2016	9805977	10/31/2017		Integrated Circuit Structure Having Through-Silicon Via And Method
FIS919950073	FIS919950073US1	US	08/536,901	9/29/1995	5745734	4/28/1998		
FIS919950075	FIS919950075US1	US	08/570,850	12/12/1995	5734582	3/31/1998		
FIS919950077	FIS919950077CN1	China	96121505.4		ZL96121505.4	8/6/2003	Not Provide	FIELD-PROGRAMMABLE MEMORY ARRAY
FIS919950077	FIS919950077US1	US	08/575,312	12/20/1995	5914906	6/22/1999		
FIS919950077	FIS919950077US4	US	09/189,750	11/10/1998	6118707	9/12/2000		
FIS919990172	FIS919990172US1	US	09/458,877	12/10/1999	6217989	4/17/2001		Conductive Line Features For Enhanced Reliability Of Multi-Layer Ce
FIS920000200	FIS920000200US1	US	09/765,035	1/17/2001	6373771	4/16/2002		Integrated Fuse Latch And Shift Register For Efficient Programming
FIS920000242	FIS920000242US1	US	09/805,027	3/12/2001	7087997	8/8/2006		Copper To Aluminum Interlayer Interconnect Using Stud And Via Lir
FIS920000302	FIS920000302US1	US	09/757,107	1/8/2001	6466100	10/15/2002		Linear Voltage Controlled Oscillator Transconductor With Gain Com
FIS920000363	FIS920000363TW1	Taiwan	91111143		NI-190151	3/5/2004	Not Provide	Single Bitline Direct Sensing Architecture For High Speed Memory De
FIS920000363	FIS920000363US1	US	09/870,755	5/31/2001	6552944	4/22/2003		Single Bitline Direct Sensing Architecture For High Speed Memory De
FIS920000364	FIS920000364TW1	Taiwan	91111303		556222	10/1/2003	Not Provide	Stabilized Direct Sensing Memory Architecture
FIS920000364	FIS920000364US1	US	09/870,559	5/31/2001	6438051	8/20/2002		Stabilized Direct Sensing Memory Architecture
FIS920000380	FIS920000380US1	US	09/907,387	7/17/2001	6834365	12/21/2004		Integrated Real Time Data Tracing With Low Pin Count Output
FIS920000399	FIS920000399US1	US	09/892,396	6/27/2001	6552920	4/22/2003		Saving Content Addressable Memory Power Through Conditional Co
FIS920010028	FIS920010028US1	US	09/887,792	6/22/2001	6785832	8/31/2004		Process Independent Source Synchronous Data Capture Apparatus A
FIS920010235	FIS920010235US1	US	10/199,788	7/19/2002	6920525	7/19/2005		Method And Apparatus Of Local Word-Line Redundancy In Cam
FIS920010285	FIS920010285JP1	Japan	2003-019203		3954974	5/11/2007	Not Provide	Unique Feature Design Enabling Structural Integrity for Advanced Lo
FIS920010285	FIS920010285US1	US	10/078,174	2/15/2002	6650010	11/18/2003		Unique Feature Design Enabling Structural Integrity For Advanced Lo
FIS920010287	FIS920010287US1	US	10/059,863	1/30/2002	6650561	11/18/2003		High Reliability Content-Addressable Memory Using Shadow Content
FIS920010292	FIS920010292US1	US	10/235,435	9/4/2002	6821026	11/23/2004		Redundant Configurable Vcsel Laser Array Optical Light Source
FIS920010312	FIS920010312US1	US	11/160,307	6/17/2005	7325213	1/29/2008		Nested Design Approach
FIS920020148	FIS920020148US1	US	10/342,420	1/14/2003	6800530	10/5/2004		Triple Layer Hard Mask For Gate Patterning To Fabricate Scaled Cmos
FIS920030022	FIS920030022US1	US	10/249,347	4/2/2003	6747890	6/8/2004		Gain Cell Structure With Deep Trench Capacitor
FIS920030048	FIS920030048US1	US	10/250,043	5/30/2003	6937054	8/30/2005		Programmable Peaking Receiver And Method
FIS920030050	FIS920030050US1	US	10/249,545	4/17/2003	6891357	5/10/2005		Reference Current Generation System And Method
FIS920030090	FIS920030090CN1	China	2.0048001496e-		ZL2004800149	12/16/2009	Not Provide	Low Power Manager for Standby Operation
FIS920030090	FIS920030090DE1	Germa	04741612.8		112011100118	1/7/2009	Not Provide	Low Power Manager for Standby Operation
FIS920030090	FIS920030090EP1	Europe	04741612.8		1639602	1/7/2009	Not Provide	Low Power Manager for Standby Operation
FIS920030090	FIS920030090US1	US	10/250,233	6/16/2003	7046572	5/16/2006		Low Power Manager For Standby Operation Of Memory System

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FIS920030111	FIS920030111US1	US	10/604,025	6/23/2003	7352815	4/1/2008		Data Transceiver And Method For Equalizing The Data Eye Of A Differ
FIS920030121	FIS920030121US1	US	10/604,799	8/18/2003	6897712	5/24/2005		Apparatus And Method For Detecting Loss Of High-Speed Signal
FIS920030154	FIS920030154US1	US	10/604,419	7/18/2003	7113749	9/26/2006		System And Method For Measuring A High Speed Signal
FIS920030314	FIS920030314US1	US	10/708,233	2/18/2004	6949981	9/27/2005		Dynamic Threshold For Vco Calibration
FIS920030409	FIS920030409CN1	China	200510002155.0		100485806	5/6/2009	Not Provide	Concurrent Refresh Mode with Distributed Row Address Counters I
FIS920030409	FIS920030409JP1	Japan	2005-007728		4524194	6/4/2010	Not Provide	Concurrent Refresh Mode with Distributed Row Address Counters I
FIS920030409	FIS920030409US1	US	10/757,846	1/15/2004	6967885	11/22/2005		Concurrent Refresh Mode With Distributed Row Address Counters I
FIS920040082	FIS920040082US1	US	10/711,713	9/30/2004	7409019	8/5/2008		High Speed Multi-Mode Receiver With Adaptive Receiver Equalizati
FIS920040132	FIS920040132US1	US	10/875,699	6/24/2004	7278459	10/9/2007		Common Carrier
FIS920040304	FIS920040304US1	US	10/904,460	11/11/2004	7106096	9/12/2006		Circuit And Method Of Controlling Integrated Circuit Power Consum
FIS920040400	FIS920040400US1	US	10/908,959	6/2/2005	8040813	10/18/2011		Apparatus And Method For Reduced Loading Of Signal Transmissior
FIS920050261	FIS920050261US1	US	11/308,167	3/9/2006	7381590	6/3/2008		Method And Device Including Reworkable Alpha Particle Barrier An
FIS920050269	FIS920050269CN1	China	2.0081000408e-		ZL2008100040	8/3/2011	Not Provide	Mult- Anode System For Uniform Plating of Alloys
FIS920050269	FIS920050269US1	US	11/627,494	1/26/2007	8177945	5/15/2012		Mult- Anode System For Uniform Plating Of Alloys
FIS920050336	FIS920050336US1	US	11/307,785	2/22/2006	7254078	8/7/2007		System And Method For Increasing Reliability Of Electrical Fuse Progr
FIS920050379	FIS920050379CN1	China	2.0078001133e-		ZL2007800113	11/3/2010	Not Provide	Method and Structure for Eliminating Aluminum Terminal Pad Materi
FIS920050379	FIS920050379TW1	Taiwan	96111680		I438852	5/21/2014	Not Provide	METHOD FOR FAR BACK END OF LINE (FBEOL) SEMICONDUCTOR DEV
FIS920050379	FIS920050379US1	US	11/308,539	4/4/2006	7375021	5/20/2008		Method And Structure For Eliminating Aluminum Terminal Pad Mater
FIS920050387	FIS920050387US1	US	11/307,894	2/27/2006	7441213	10/21/2008		Method For Testing The Validity Of Initial-Condition Statements In Cir
FIS920060116	FIS920060116US1	US	11/683,068	3/7/2007	7674691	3/9/2010		Method Of Manufacturing An Electrical Antifuse
FIS920060118	FIS920060118US1	US	11/380,515	4/27/2006	7397261	7/8/2008		Monitoring System For Detecting And Characterizing Classes Of Leaka
FIS920060132	FIS920060132US1	US	11/559,130	11/13/2006	7573115	8/11/2009		Structure And Method For Enhancing Resistance To Fracture Of Bond
FIS920060140	FIS920060140US1	US	11/461,208	7/31/2006	7470985	12/30/2008		Solder Connector Structure And Method
FIS920060279	FIS920060279US1	US	11/548,482	10/11/2006	8445362	5/21/2013		Apparatus And Method For Programming An Electronically Program
FIS920070026	FIS920070026US1	US	11/759,396	6/7/2007	7974304	7/5/2011		Out Of Band Signaling Enhancement For High Speed Serial Driver
FIS920070026	FIS920070026US2	US	12/154,796	5/27/2008	8144726	3/27/2012		Structure For Out Of Band Signaling Enhancement For High Speed Ser
FIS920070030	FIS920070030US1	US	11/769,128	6/27/2007	8219040	7/10/2012		Transmitter Bandwidth Optimization Circuit
FIS920070030	FIS920070030US2	US	11/985,963	11/19/2007	8219041	7/10/2012		Design Structure For Transmitter Bandwidth Optimization Circuit
FIS920070092	FIS920070092US1	US	11/949,426	12/3/2007	7432755	10/7/2008		Programming Current Stabilized Electrical Fuse Programming Circuit
FIS920070097	FIS920070097US1	US	11/766,268	6/21/2007	7855563	12/21/2010		Robust Cable Connectivity Test Receiver For High-Speed Data Receive
FIS920070278	FIS920070278US1	US	11/940,531	11/15/2007	8492846	7/23/2013		Stress Generating Shallow Trench Isolation Structure Having Dual Co
FIS920070278	FIS920070278US3	US	13/947,677	7/22/2013	9013001	4/21/2015		Stress-Generating Shallow Trench Isolation Structure Having Dual Co
FIS920070281	FIS920070281US1	US	12/365,919	2/5/2009	8183949	5/22/2012		Compensation Of Vco Gain Curve Offsets Using Auto-Calibration
FIS920070308	FIS920070308US1	US	12/698,629	2/2/2010	8401135	3/19/2013		Post-Equalization Amplitude Latch-Based Channel Characterization M

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FIS920070383	FIS920070383US1	US	11/947,832	11/30/2007	8120175	2/21/2012		Soft Error Rate Mitigation By Novel Interconnect Structure
FIS920070483	FIS920070483US1	US	12/036,320	2/25/2008	7908534	3/15/2011		Diagnosable General Purpose Test Registers Scan Chain Design
FIS920080171	FIS920080171CN1	China	2.0101021537e-		ZL2010102153	11/6/2013	Not Provide	Electrically Programmable Fuse Using Anisometric Contacts and Fab
FIS920080171	FIS920080171US1	US	12/493,616	6/29/2009	8519507	8/27/2013		Electrically Programmable Fuse Using Anisometric Contacts And Fal
FIS920080224	FIS920080224US1	US	12/365,921	2/5/2009	8183950	5/22/2012		Auto-Calibration For Ring Oscillator Vco
FIS920090065	FIS920090065US1	US	12/492,198	6/26/2009	8312404	11/13/2012		Multi-Segments Modeling Bond Wire Interconnects With 2d Simula
FIS920100066	FIS920100066US1	US	12/835,764	7/14/2010	8350264	1/8/2013		Secure Anti-Fuse With Low-Voltage Programming Through Localizec
FIS920100108	FIS920100108US1	US	12/878,579	9/9/2010	8378498	2/19/2013		Chip Assembly With A Coreless Substrate Employing A Patterned Ad
FIS920110074	FIS920110074US1	US	13/345,290	1/6/2012	8901706	12/2/2014		Thermally Stale High-K Tetragonal Hfo2 Layer Within High Aspect Ra
FIS920110157	FIS920110157US1	US	13/342,453	1/3/2012	8410835	4/2/2013		Leakage Tolerant Phase Locked Loop Circuit Device
FIS920110180	FIS920110180US1	US	13/295,351	11/14/2011	8493117	7/23/2013		Leakage Tolerant Delay Locked Loop Circuit Device
FIS920110221	FIS920110221CN1	China	2.0128005732e-		ZL2012800573	8/24/2016	Not Provide	Retention Based Intrinsic Fingerprint Identification Featuring a Fuzz
FIS920110221	FIS920110221DE1	Germa	201211044397		11 2012 004 4.	11/24/2016	Not Provide	Memory supported intrinsic fingerprint - identification with a fuzzy
FIS920110221	FIS920110221IN1	India	2203/CHENP/20				Not Provide	Retention Based Intrinsic Fingerprint Identification Featuring a Fuzzy
FIS920110221	FIS920110221US1	US	13/302,314	11/22/2011	8590010	11/19/2013		Retention Based Intrinsic Fingerprint Identification Featuring A Fuzzy
FIS920120068	FIS920120068US1	US	13/465,159	5/7/2012	9041119	5/26/2015		Forming Cmos With Close Proximity Stressors
FIS920120128	FIS920120128US1	US	13/552,106	7/18/2012	8841208	9/23/2014		Method Of Forming Vertical Electronic Fuse Interconnect Structures I
FIS920120200	FIS920120200US1	US	13/661,062	10/26/2012	8754690	6/17/2014		Programmable Duty Cycle Setter Employing Time To Voltage Domain
FIS920120203	FIS920120203US1	US	13/648,292	10/10/2012	9202040	12/1/2015		Chip Authentication Using Multi-Domain Intrinsic Identifiers
FIS920120234	FIS920120234US1	US	13/688,595	11/29/2012	8667448	3/4/2014		Integrated Circuit Having Local Maximum Operating Voltage
FIS920120249	FIS920120249US1	US	13/737,110	1/9/2013	8692600	4/8/2014		Multi-Protocol Driver Slew Rate Calibration System For Calibrating Sle
FIS920130003	FIS920130003US1	US	13/903,199	5/28/2013	9184948	11/10/2015		Decision Feedback Equalizer ('dfe') With A Plurality Of Independently
FIS920130088	FIS920130088PCT1	Patent	US2014/040462				Not Provide	Electronic Module Assembly With Patterned Adhesive Array
FIS920130088	FIS920130088US2	US	14/567,052	12/11/2014	9293439	3/22/2016		Electronic Module Assembly With Patterned Adhesive Array
FIS920130196	FIS920130196CN1	China	201410681506.:		ZL2014106815	7/27/2017	Not Provide	(FDC)Unclonable ID Based Chip-to-Chip Communication
FIS920130196	FIS920130196US1	US	14/102,607	12/11/2013	9219722	12/22/2015		Unclonable Id Based Chip-To-Chip Communication
FIS920130232	FIS920130232US1	US	14/172,550	2/4/2014	9374098	6/21/2016		Transmitter Serializer Latency Trim
FIS920130233	FIS920130233US1	US	14/172,618	2/4/2014	9473172	10/18/2016		Receiver Deserializer Latency Trim
FIS920140166	FIS920140166US1	US	14/467,191	8/25/2014	9396991	7/19/2016		Multilayered Contact Structure Having Nickel, Copper, And Nickel-Iro
FIS920140185	FIS920140185US1	US	14/525,267	10/28/2014	9728440	8/8/2017		Non-Transparent Microelectronic Grade Glass As A Substrate, Tempo
FIS920140280	FIS920140280US1	US	14/669,775	3/26/2015	9401315	7/26/2016		Thermal Hot Spot Cooling For Semiconductor Devices
FIS920150135	FIS920150135US1	US	14/964,327	12/9/2015	9571111	2/14/2017		System And Method To Speed Up Pll Lock Time On Subsequent Calibr
FIS920150135	FIS920150135US1-C	China	2.0161113183e-				Not Provide	SYSTEM AND METHOD TO SPEED UP PLL LOCK TIME ON SUBSEQUEN
FIS920150135	FIS920150135US1-T	Taiwan	105138372		I 620416	4/1/2018	Not Provide	SYSTEM AND METHOD TO SPEED UP PLL LOCK TIME ON SUBSEQUEN

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FIS920150139	FIS920150139US1	US	14/961,484	12/7/2015	9659604	5/23/2017		Dual-Bit 3-T High Density Mtprom Array
FIS920150139	FIS920150139US1-C	China	2.0161111372e-				Not Provide	DUAL-BIT 3-T HIGH DENSITY MTPROM ARRAY
FIS920150139	FIS920150139US1-	Germa	201610123654				Not Provide	DUAL-BIT 3-T HIGH DENSITY MTPROM ARRAY
FIS920150139	FIS920150139US1-T	Taiwan	105138376		I 646538	1/1/2019	Not Provide	DUAL-BIT 3-T HIGH DENSITY MTPROM ARRAY
FIS920150139	FIS920150139US1-	US	15/478,820	4/4/2017	9786333	10/10/2017		Dual-Bit 3-T High Density Mtprom Array
GU083	GU083-US-NP	US	14/165,039	1/27/2014	9337143	5/10/2016		E-Fuse Structure With Methods Of Fusing The Same And Monitoring
HAUS001	HAUS001-CN-NP	China	201711274097.!				Not Provide	Digitally controlled varactor structure for high resolution DCO
HAUS001	HAUS001-TW-NP	Taiwan	106125710		I665867	7/11/2019	Not Provide	Digitally controlled varactor structure for high resolution DCO
HAUS001	HAUS001-US-NP	US	15/370,004	12/6/2016	10332960	6/25/2019		Digitally Controlled Varactor Structure For High Resolution Dco
HAUS002	HAUS002-US-NP	US	15/277,344	9/27/2016	9774302	9/26/2017		Amplifier Circuit With Single-Ended Input And Differential Outputs
HAUS003	HAUS003-CN-NP	China	2.0171058867e-				Not Provide	SWITCHED CAPACITOR CIRCUIT STRUCTURE WITH METHOD OF CON
HAUS003	HAUS003-DE-NP	Germa	10 2017 212 101				Not Provide	SWITCHED CAPACITOR CIRCUIT STRUCTURE WITH METHOD OF CON
HAUS003	HAUS003-TW-NP	Taiwan	106118674				Not Provide	SWITCHED CAPACITOR CIRCUIT STRUCTURE WITH METHOD OF CON
HAUS003	HAUS003-US-NP	US	15/213,529	7/19/2016	10348243	7/9/2019		Switched Capacitor Circuit Structure With Method Of Controlling Sou
HAUS004	HAUS004-CN-NP	China	201711308359.!				Not Provide	DIGITAL FREQUENCY MULTIPLIER TO GENERATE A LOCAL OSCILLATO
HAUS004	HAUS004-TW-NP	Taiwan	106127461				Not Provide	DIGITAL FREQUENCY MULTIPLIER TO GENERATE A LOCAL OSCILLATO
HAUS004	HAUS004-US-NP	US	15/373,791	12/9/2016	9806701	10/31/2017		Digital Frequency Multiplier To Generate A Local Oscillator Signal In F
HBUR001	HBUR001-US-NP	US	15/140,016	4/27/2016	9870163	1/16/2018		Double Bandwidth Algorithmic Memory Array
HBUR007	HBUR007-CN-NP	China	2.0171007264e-				Not Provide	MATCHLINE PRECHARGE ARCHITECTURE FOR SELF-REFERENCE MATC
HBUR007	HBUR007-DE-NP	Germa	102016213092.'				Not Provide	MATCHLINE PRECHARGE ARCHITECTURE FOR SELF-REFERENCE MATC
HBUR007	HBUR007-US-NP	US	15/164,325	5/25/2016	9583192	2/28/2017		Matchline Precharge Architecture For Self-Reference Matchline Sensi
HBUR008	HBUR008-CN-NP	China	2.0171042222e-				Not Provide	Self Pre-Charging Memory Circuits
HBUR008	HBUR008-DE-NP	Germa	102016215292				Not Provide	Self Pre-Charging Memory Circuits
HBUR008	HBUR008-TW-NP	Taiwan	105122590		I 654617	3/21/2019	Not Provide	Self Pre-Charging Memory Circuits
HBUR008	HBUR008-US-NP	US	15/175,466	6/7/2016	9886998	2/6/2018		Self Pre-Charging Memory Circuits
HBUR012	HBUR012-CN-NP	China	2.0171045793e-				Not Provide	ALGORITHMIC N SEARCH/M WRITE TERNARY CONTENT ADDRESSABL
HBUR012	HBUR012-DE-NP	Germa	10 2016 220 328				Not Provide	ALGORITHMIC N SEARCH/M WRITE TERNARY CONTENT ADDRESSABL
HBUR012	HBUR012-TW-NP	Taiwan	105126730		I636458	9/21/2018	Not Provide	ALGORITHMIC N SEARCH/M WRITE TERNARY CONTENT ADDRESSABL
HBUR012	HBUR012-US-NP	US	15/185,956	6/17/2016	9859006	1/2/2018		Algorithmic N Search/M Write Ternary Content Addressable Memory
HD066	HD066-US-DIV	US	15/428,509	2/9/2017				Memory Device Structure
HD066	HD066-US-NP	US	14/918,736	10/21/2015	9614003	4/4/2017		Method Of Forming A Memory Device Structure And Memory Device
HD066-C	HD066-CN-DIV	China	201910430187.:				Not Provide	Method For Forming Memory Device Structure And Memory Device
HD066-C	HD066-CN-NP	China	2.0161092047e-		ZL 2016 1 0920	6/14/2019	Not Provide	Method For Forming Memory Device Structure And Memory Device
HD066-T	HD066-TW-NP	Taiwan	105133538		I620277	4/1/2018	Not Provide	Method of Forming a Memory Device Structure and Memory Device

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HFIS008	HFIS008-US-DIV	US	16/033,932	7/12/2018				Ic Structure Including Tsv Having Metal Resistant To High Temperatur
HFIS008	HFIS008-US-NP	US	15/292,721	10/13/2016	10049979	8/14/2018		Ic Structure Including Tsv Having Metal Resistant To High Temperat
HFIS010	HFIS010-CN-NP	China	2.0171071041e-				Not Provide	IC structure on two sides of substrate and method of forming
HFIS010	HFIS010-TW-NP	Taiwan	106121256		I657559	4/21/2019	Not Provide	IC STRUCTURE ON TWO SIDES OF SUBSTRATE AND METHOD OF FOI
HFIS010	HFIS010-US-NP	US	15/239,976	8/18/2016	10068899	9/4/2018		Ic Structure On Two Sides Of Substrate And Method Of Forming
HIND007	HIND007-CN-NP	China	201610517254.(				Not Provide	HIGH PERFORMANCE MULTIPLEXED LATCHES
HIND007	HIND007-TW-NP	Taiwan	105104246		I649758	2/1/2019	Not Provide	High Performance Multiplexed Latches
HIND007	HIND007-US-NP	US	15/003,598	1/21/2016	9787292	10/10/2017		High Performance Multiplexed Latches
HIND008	HIND008-CN-NP	China	2.0171009598e-				Not Provide	SENSE AMPLIFIER AND LATCHNG SCHEME
HIND008	HIND008-TW-NP	Taiwan	105108787		I645413	12/21/2018	Not Provide	Sense Amplifier And Latching Scheme
HIND008	HIND008-US-NP	US	15/054,553	2/26/2016	9761285	9/12/2017		Sense Amplifier Latching Scheme
HMLT052	HMLT052-CN-NP	China	2.0171006357e-		ZL 201710063!	12/21/2018	Not Provide	METHOD, APPARATUS AND SYSTEM FOR VOLTAGE COMPENSATION
HMLT052	HMLT052-TW-NP	Taiwan	106103445		I637252	10/1/2018	Not Provide	METHOD, APPARATUS AND SYSTEM FOR VOLTAGE COMPENSATION
HMLT052	HMLT052-US-NP	US	15/013,956	2/2/2016	10069490	9/4/2018		Method, Apparatus And System For Voltage Compensation In A Semi
HSG098	HSG098-US-NP	US	15/227,979	8/4/2016	9843326	12/12/2017		Wide Range Level Shifter For Low Voltage Input Applications
HSG099	HSG099-US-NP	US	15/431,796	2/14/2017	10153279	12/11/2018		Compact And Reliable Changeable Negative Voltage Transmission Cir
HSG108	HSG108-US-NP	US	15/383,083	12/19/2016	10115453	10/30/2018		Integrated Circuits With Sram Devices Having Read Assist Circuits And
IBUR005	IBUR005-CN-NP	China	2.0171050643e-				Not Provide	TAMPER DETECTION FOR A CHIP PACKAGE
IBUR005	IBUR005-TW-NP	Taiwan	106112180		I660184	5/21/2019	Not Provide	TAMPER DETECTION FOR A CHIP PACKAGE
IBUR005	IBUR005-US-NP	US	15/195,029	6/28/2016				Tamper Detection For A Chip Package
IBUR008	IBUR008-US-NP	US	15/276,182	9/26/2016	9875956	1/23/2018		Integrated Interface Structure
IBUR038	IBUR038-US-NP	US	15/464,397	3/21/2017	9893765	2/13/2018		Transmission System Having Duplicate Transmission Systems For Indi
IBUR050	IBUR050-CN-NP	China	2.0171105726e-				Not Provide	Ternary Content Addressable Memory (TCAM) for Multi Bit Miss Dete
IBUR050	IBUR050-TW-NP	Taiwan	106113438		I 634560	9/1/2018	Not Provide	Ternary Content Addressable Memory (TCAM) for Multi Bit Miss Dete
IBUR050	IBUR050-US-NP	US	15/340,579	11/1/2016	9916896	3/13/2018		Ternary Content Addressable Memory (Tcam) For Multi Bit Miss Dete
IBUR055	IBUR055-CN-NP	China	2.0171065503e-				Not Provide	ZERO TEST TIME MEMORY USING BACKGROUND BUILT-IN SELF-TEST
IBUR055	IBUR055-TW-NP	Taiwan	106122580		I644321	12/11/2018	Not Provide	ZERO TEST TIME MEMORY USING BACKGROUND BUILT-IN SELF-TEST
IBUR055	IBUR055-US-NP	US	15/478,666	4/4/2017				Zero Test Time Memory Using Background Built-In Self-Test
ICIS-CH	ICIS-CHRT-0017&a	Singapo	10201408366W				Not Provide	Ip Protection
ICIS-CHRT-0017&20	ICIS-CHRT-0017&20	US	13/161,514	6/16/2011	9069923	6/30/2015		
ICIS-CHRT-0017&a	ICIS-CHRT-0017&a	US	14/751,170	6/26/2015	9947645	4/17/2018		
ICNA003	ICNA003-US-NP	US	15/406,350	1/13/2017	10298345	5/21/2019		Clock Synchronizaton Using Codeword Marker
ICNA004	ICNA004-CN-NP	China	2.0171105814e-				Not Provide	NANOSECOND ACCURACY UNDER PRECISION TIME PROTOCOL FOR E
ICNA004	ICNA004-TW-NP	Taiwan	106124111		I651983	2/21/2019	Not Provide	NANOSECOND ACCURACY UNDER PRECISION TIME PROTOCOL FOR E

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ICNA004	ICNA004-US-NP	US	15/397,028	1/3/2017	10104148	10/16/2018		Nanosecond Accuracy Under Precision Time Protocol For Ethernet By
IFIS045	IFIS045A-US-NP	US	15/467,610	3/23/2017	10033334	7/24/2018		Peaking Amplifier Frequency Tuning
IFIS045	IFIS045B-US-NP	US	15/467,617	3/23/2017	9960738	5/1/2018		Peaking Amplifier Frequency Tuning
IFIS045	IFIS045C-US-CNT	US	15/790,144	10/23/2017	10211790	2/19/2019		Peaking Amplifier Frequency Tuning
IFIS045	IFIS045C-US-NP	US	15/467,589	3/23/2017	9853612	12/26/2017		Peaking Amplifier Frequency Tuning
IFIS051	IFIS051-US-NP	US	15/460,914	3/16/2017	10014364	7/3/2018		On-Chip Resistors With A Tunable Temperature Coefficient Of Resis
IIND002	IIND002-US-NP	US	15/266,261	9/15/2016	9721628	8/1/2017		Address Based Memory Data Path Programming Scheme
IIND009	IIND009-CN-NP	China	2.0171084564e-				Not Provide	Integrated Level Translator And Latch For Fence Architecture
IIND009	IIND009-TW-NP	Taiwan	105144158		I 655846	4/1/2019	Not Provide	Integrated Level Translator And Latch For Fence Architecture
IIND009	IIND009-US-NP	US	15/269,139	9/19/2016	10020809	7/10/2018		Integrated Level Translator And Latch For Fence Architecture
IIND012	IIND012-US-NP	US	15/363,056	11/29/2016	9721673	8/1/2017		Distributed Current Source/Sink Using Inactive Memory Elements
IIND014	IIND014-CN-NP	China	2.0171108861e-				Not Provide	Bending Circuit For Static Random Access Memory (SRAM) Self-Tim
IIND014	IIND014-TW-NP	Taiwan	106110780		I 637398	10/1/2018	Not Provide	Bending Circuit For Static Random Access Memory (SRAM) Self-Tim
IIND014	IIND014-US-NP	US	15/345,544	11/8/2016	10217507	2/26/2019		Bending Circuit For Static Random Access Memory (Sram) Self-Timer
IIND015	IIND015-US-NP	US	15/446,091	3/1/2017	9881669	1/30/2018		Wordline Driver With Integrated Voltage Level Shift Function
IL920140066	IL920140066US1	US	14/548,929	11/20/2014	9465689	10/11/2016		Forward Error Correction Synchronization
IN920110081	IN920110081US1	US	13/316,067	12/9/2011	8797096	8/5/2014		Crosstalk Compensation For High Speed, Reduced Swing Circuits
IN920110082	IN920110082US1	US	13/269,942	10/10/2011	8593861	11/26/2013		Asymmetric Memory Cells
IN920110193	IN920110193US1	US	13/296,724	11/15/2011	8427896	4/23/2013		Dynamic Wordline Assist Scheme To Improve Performance Tradeoff I
IN920120023	IN920120023US1	US	13/563,960	8/1/2012	9047980	6/2/2015		Sense Amplifier For Static Random Access Memory With A Pair Of Co
IN920130090	IN920130090US1	US	14/018,451	9/5/2013	9177709	11/3/2015		Structure And Method For Realizing High Performance Multi-Port Ind
IN920130092	IN920130092US1	US	14/018,452	9/5/2013	9054069	6/9/2015		Variable Capacitance Integrated Circuit
IN920140013	IN920140013US1	US	14/317,806	6/27/2014	9286969	3/15/2016		Low Power Sense Amplifier For Static Random Access Memory
IN920140041	IN920140041US1	US	14/331,276	7/15/2014	9542354	1/10/2017		Generating A Parallel Data Signal By Converting Serial Data Of A Serial
IN920140046	IN920140046US1	US	14/466,385	8/22/2014	9208859	12/8/2015		Low Power Static Random Access Memory (Sram) Read Data Path
IN920140201	IN920140201US1	US	14/734,525	6/9/2015	9570155	2/14/2017		Circuit To Improve Sram Stability
IN920140204	IN920140204US1	US	14/669,043	3/26/2015	9236116	1/12/2016		Memory Cells With Read Access Schemes
IN920150072	IN920150072US1	US	14/886,739	10/19/2015				Auto Test Grouping/Clock Sequencing For At-Speed Test
IUS001	IUS001-US-NP	US	15/412,193	1/23/2017	9871531	1/16/2018		Non-Geometric Scaling Current Steering Digital To Analog Converter
IUS012	IUS012-US-NP	US	15/451,470	3/7/2017	9911474	3/6/2018		Feedback Circuit At Wordline Ends
IUS013	IUS013-CN-NP	China	201810642737.:				Not Provide	PHASE ROTATOR APPARATUS
IUS013	IUS013-TW-NP	Taiwan	107100641		I 647916	1/11/2019	Not Provide	PHASE ROTATOR APPARATUS
IUS013	IUS013-US-NP	US	15/629,900	6/22/2017	10075174	9/11/2018		Phase Rotator Apparatus
IUS016	IUS016-US-NP	US	15/659,352	7/25/2017	10014828	7/3/2018		Transmitter System And Method Of Calibration

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JBUR004	JBUR004-US-NP	US	15/684,492	8/23/2017				Sense-Line Muxing Scheme
JBUR010	JBUR010-CN-NP	China	201810982972.:				Not Provide	CHARGE PUMP CIRCUIT WITH BUILT-IN RETRY
JBUR010	JBUR010-DE-NP	Germa	102018218550.(				Not Provide	CHARGE PUMP CIRCUIT WITH BUILT-IN RETRY
JBUR010	JBUR010-TW-NP	Taiwan	107121316				Not Provide	CHARGE PUMP CIRCUIT WITH BUILT-IN RETRY
JBUR010	JBUR010-US-NP	US	15/822,318	11/27/2017				Charge Pump Circuit With Built-In Retry
JBUR023	JBUR023-CN-NP	China	201810796399.:				Not Provide	DIFFERENTIAL VOLTAGE GENERATOR
JBUR023	JBUR023-TW-NP	Taiwan	107114680				Not Provide	DIFFERENTIAL VOLTAGE GENERATOR
JBUR023	JBUR023-US-NP	US	15/788,289	10/19/2017	10192590	1/29/2019		Differential Voltage Generator
JBUR038	JBUR038-US-NP	US	15/866,999	1/10/2018				Electrostatic Discharge Clamp With Reduced Off-State Power Consu
JBUR045	JBUR045-CN-NP	China	201910063350.!				Not Provide	ON-CHIP RELIABILITY MONITOR AND METHOD
JBUR045	JBUR045-DE-NP	Germa	10 2019 200 21(				Not Provide	ON-CHIP RELIABILITY MONITOR AND METHOD
JBUR045	JBUR045-TW-NP	Taiwan	108102551				Not Provide	ON-CHIP RELIABILITY MONITOR AND METHOD
JBUR045	JBUR045-US-NP	US	15/903,231	2/23/2018				On-Chip Reliability Monitor And Method
JBUR047	JBUR047-CN-NP	China	201910131291				Not Provide	DYNAMIC POWER ANALYSIS WITH PER-MEMORY INSTANCE ACTIVITY
JBUR047	JBUR047-DE-NP	Germa	10 2019 202 01!				Not Provide	DYNAMIC POWER ANALYSIS WITH PER-MEMORY INSTANCE ACTIVITY
JBUR047	JBUR047-TW-NP	Taiwan	108105231				Not Provide	DYNAMIC POWER ANALYSIS WITH PER-MEMORY INSTANCE ACTIVITY
JBUR047	JBUR047-US-NP	US	15/928,587	3/22/2018				Dynamic Power Analysis With Per-Memory Instance Activity Customi
JCNA001	JCNA001-US-NP	US	15/636,793	6/29/2017	10365328	7/30/2019		Register Array Having Groups Of Latches With Single Test Latch Testa
JCNA005	JCNA005-CN-NP	China	201910061789.4				Not Provide	WRITE SCHEME FOR A STATIC RANDOM ACCESS MEMORY (SRAM)
JCNA005	JCNA005-DE-NP	Germa	10 2019 200 51:				Not Provide	WRITE SCHEME FOR A STATIC RANDOM ACCESS MEMORY (SRAM)
JCNA005	JCNA005-TW-NP	Taiwan	108102267				Not Provide	WRITE SCHEME FOR A STATIC RANDOM ACCESS MEMORY (SRAM)
JCNA005	JCNA005-US-NP	US	15/903,826	2/23/2018				Write Scheme For A Static Random Access Memory (Sram)
JFIS006	JFIS006-CN-NP	China	201810454031.:				Not Provide	FLEXIBLE HEAT SPREADER LID
JFIS006	JFIS006-DE-NP	Germa	10 2018 207 34!				Not Provide	FLEXIBLE HEAT SPREADER LID
JFIS006	JFIS006-TW-NP	Taiwan	107108259				Not Provide	FLEXIBLE HEAT SPREADER LID
JFIS006	JFIS006-US-NP	US	15/593,969	5/12/2017	10224262	3/5/2019		Flexible Heat Spreader Lid
JIND001	JIND001-US-NP	US	15/668,362	8/3/2017	10416226	9/17/2019		Test Response Compaction Scheme
JIND002	JIND002-CN-NP	China	201711440928.:				Not Provide	BIT LINE STRAPPING SCHEME FOR HIGH DENSITY SRAM
JIND002	JIND002-TW-NP	Taiwan	106138448		I642064	11/21/2018	Not Provide	BIT LINE STRAPPING SCHEME FOR HIGH DENSITY SRAM
JIND002	JIND002-US-NP	US	15/693,637	9/1/2017	10199095	2/5/2019		Bit Line Strapping Scheme For High Density Sram
JIND003	JIND003-CN-NP	China	201810311333.4				Not Provide	Intracycle Bitline Restore in High Performance Memory
JIND003	JIND003-TW-NP	Taiwan	107107003		I 654606	3/21/2019	Not Provide	Intracycle Bitline Restore in High Performance Memory
JIND003	JIND003-US-NP	US	15/814,969	11/16/2017				Intracycle Bitline Restore In High Performance Memory
JIND004	JIND004-CN-NP	China	201810755948.!				Not Provide	HYBRID STACK WRITE DRIVER

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JIND004	JIND004-TW-NP	Taiwan	107113834				Not Provide	HYBRID STACK WRITE DRIVER
JIND004	JIND004-US-NP	US	15/730,850	10/12/2017	10186312	1/22/2019		Hybrid Stack Write Driver
JIND009	JIND009-CN-NP	China	201910078311.3				Not Provide	COMMON BOOSTED ASSIST
JIND009	JIND009-DE-NP	Germa	10 2019 200 67(				Not Provide	COMMON BOOSTED ASSIST
JIND009	JIND009-TW-NP	Taiwan	108102940				Not Provide	COMMON BOOSTED ASSIST
JIND009	JIND009-US-NP	US	15/906,588	2/27/2018	10381054	8/13/2019		Common Boosted Assist
JIND016	JIND016-CN-NP	China	201910032455.4				Not Provide	SENSE AMPLIFIER LATCH CIRCUIT AND SENSE AMPLIFIER MULTIPLE)
JIND016	JIND016-DE-NP	Germa	10 2019 200 85:				Not Provide	SENSE AMPLIFIER LATCH CIRCUIT AND SENSE AMPLIFIER MULTIPLE)
JIND016	JIND016-TW-NP	Taiwan	108101188				Not Provide	SENSE AMPLIFIER LATCH CIRCUIT AND SENSE AMPLIFIER MULTIPLE)
JIND016	JIND016-US-NP	US	15/895,001	2/13/2018	10170164	1/1/2019		Sense Amplifier Latch Circuit And Sense Amplifier Multiplexed Latch
JIND018	JIND018-DE-NP	Germa	10 2019 201 83(				Not Provide	INTEGRATED LEVEL TRANSLATOR
JIND018	JIND018-TW-NP	Taiwan	108105659				Not Provide	INTEGRATED LEVEL TRANSLATOR
JIND018	JIND018-US-NP	US	15/926,272	3/20/2018	10395700	8/27/2019		Integrated Level Translator
JMLT128	JMLT128-CN-NP	China	201711257865.(				Not Provide	INTERCONNECT STRUCTURE
JMLT128	JMLT128-TW-NP	Taiwan	106137196		I 645525	12/21/2018	Not Provide	INTERCONNECT STRUCTURE
JMLT128	JMLT128-US-NP	US	15/664,484	7/31/2017	10381304	8/13/2019		Interconnect Structure
JMLT289	JMLT289-US-NP	US	15/801,380	11/2/2017				Testing Monolithic Three Dimensional Integrated Circuits
JSC011	JSC011-CN-NP	China	201810311075.3				Not Provide	INJECTION LOCKED OSCILLATOR SYSTEM AND PROCESSES
JSC011	JSC011-DE-NP	Germa	102018203378.7				Not Provide	INJECTION LOCKED OSCILLATOR SYSTEM AND PROCESSES
JSC011	JSC011-TW-NP	Taiwan	107106797		I 653832	3/11/2019	Not Provide	INJECTION LOCKED OSCILLATOR SYSTEM AND PROCESSES
JSC011	JSC011-US-NP	US	15/801,735	11/2/2017				Injection Locked Oscillator System And Processes
JSC013	JSC013-US-NP	US	15/886,322	2/1/2018				Controlling Current Flow Between Nodes With Adjustable Back-Gate
JSC025	JSC025-US-NP	US	15/951,426	4/12/2018				Column Multiplexor Decoding
JSG125	JSG125-CN-NP	China	201811581716.3				Not Provide	OVERHANG MODEL FOR REDUCING PASSIVATION STRESS AND METH
JSG125	JSG125-DE-NP	Germa	10 2019 202 08:				Not Provide	OVERHANG MODEL FOR REDUCING PASSIVATION STRESS AND METH
JSG125	JSG125-TW-NP	Taiwan	107113836				Not Provide	OVERHANG MODEL FOR REDUCING PASSIVATION STRESS AND METH
JSG125	JSG125-US-NP	US	15/909,684	3/1/2018				Overhang Model For Reducing Passivation Stress And Method For Pr
JUS005	JUS005-CN-NP	China	201810974380.7				Not Provide	MANAGING DATA FLOW IN VCSEL-BASED OPTICAL COMMUNICATIO
JUS005	JUS005-TW-NP	Taiwan	107125708				Not Provide	MANAGING DATA FLOW IN VCSEL-BASED OPTICAL COMMUNICATIO
JUS005	JUS005-US-NP	US	15/686,304	8/25/2017				Managing Data Flow In Vcsel-Based Optical Communications System
JUS018	JUS018-US-NP	US	15/982,459	5/17/2018	10250010	4/2/2019		Control Of Vcsel-Based Optical Communications System
KBUR029	KBUR029-TW-NP	Taiwan	108136679	10/9/2019			Not Provide	ULTRA-LOW VOLTAGE LEVEL SHIFTER
KBUR042	KBUR042-DE-NP	Germa	10 2019 216 38:	10/24/2019			Not Provide	PARALLEL-PREFIX ADDER & METHOD
KBUR042	KBUR042-TW-NP	Taiwan	108138626	10/25/2019			Not Provide	PARALLEL-PREFIX ADDER & METHOD

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KIND003	KIND003-US-NP	US	16/216,248	12/11/2018	10432436	10/1/2019		Feed Forward Equalizer With Power-Optimized Distributed Arithmeti
KIND018	KIND018-US-NP	US	16/266,707	2/4/2019	10447510	10/15/2019		On-Demand Feed Forward Equalizer With Distributed Arithmetic Ar
KSC001	KSC001-CN-NP	China	201910312721.5				Not Provide	MERGED WRITE DRIVER BASED ON LOCAL SOURCE LINE MRAM ARC
KSC001	KSC001-DE-NP	Germa	10 2019 205 647				Not Provide	MERGED WRITE DRIVER BASED ON LOCAL SOURCE LINE MRAM ARC
KSC001	KSC001-TW-NP	Taiwan	108113231				Not Provide	MERGED WRITE DRIVER BASED ON LOCAL SOURCE LINE MRAM ARC
KUS001	KUS001-CN-NP	China	201910852860	9/10/2019			Not Provide	LINEAR FEEDBACK SHIFT REGISTER-BASED CLOCK SIGNAL GENERATO
KUS001	KUS001-DE-NP	Germa	10 2019 215 455	10/9/2019			Not Provide	LINEAR FEEDBACK SHIFT REGISTER-BASED CLOCK SIGNAL GENERATO
KUS001	KUS001-TW-NP	Taiwan	108132584	9/10/2019			Not Provide	LINEAR FEEDBACK SHIFT REGISTER-BASED CLOCK SIGNAL GENERATO
KUS001	KUS001-US-NP	US	16/156,460	10/10/2018	10432209	10/1/2019		Linear Feedback Shift Register-Based Clock Signal Generator, Time D
KUS002	KUS002-US-NP	US	16/248,290	1/15/2019	10396811	8/27/2019		Temperature Compensation For Reference Voltages In An Analog-To
LFIS003	LFIS003-US-NP	US	16/726,331	12/24/2019			Pending	Apparatus and Related Method for Synchronizing Operation of Seri
LIND008	LIND008-US-NP	US	16/724,554	12/23/2019			Pending	
MP0001	MP0001	US	08/493,799	6/22/1995	5576647	11/19/1996	In Force	CHARGE PUMP FOR PHASE LOCK LOOP
MP0001	MP0001D1	US	08/660,006	6/3/1996	5635879	6/3/1997	In Force	VOLTAGE CONTROLLED OSCILLATOR FORMED OF TWO DIFFERENTIA
MP0001	MP0001D2	US	08/657,087	6/3/1996	5686867	11/11/1997	In Force	REGULATED SUPPLY FOR VOLTAGE CONTROLLED OSCILLATOR
MP0003	MP0003	US	08/827,030	3/25/1997	5869988	2/9/1999	In Force	High Speed Write Driver For Inductive Heads
MP0004	MP0004	US	08/847,912	4/28/1997	5861829	1/19/1999	In Force	HIGH-SPEED, LOW POWER, MEDIUM RESOLUTION ANALOG-TO-DIGI
MP0004	MP0004RE	US	09/760,705	1/17/2001	RE37716	5/28/2002	In Force	High-Speed, Low Power, Medium Resolution Analog-To-Digital Conve
MP0005	MP0005	US	08/848,549	4/28/1997	5805006	9/8/1998	In Force	CONTROLLABLE INTEGRATOR
MP0005	MP0005RE	US	09/609,007	6/22/2000	RE37739	6/11/2002	In Force	Controllable Integrator
MP0005	MP0005REC1	US	09/950,086	12/9/2001	RE38,455	9/3/2004	In Force	CONTROLLABLE INTEGRATOR
MP0005	MP0005REC2	US	10/614,084	7/8/2003	RE41792	10/5/2010	In Force	Controllable Integrator
MP0006	MP0006	US	08/847,933	4/28/1997	5764718	6/9/1998	In Force	RIPPLE CARRY LOGIC ASND METHOD
MP0006	MP0006RE	US	09/585,343	6/2/2000	RE37335	8/21/2001	In Force	Ripple Carry Logic And Method
MP0007	MP0007	US	08/848,525	4/28/1997	5831456	11/3/1998	In Force	APPARATUS AND METHOD FOR TRANSIENT SUPPRESSION IN SYNCHR
MP0008	MP0008	US	09/015,733	1/29/1998	6219195	4/17/2001	In Force	Low-Noise Magneto-Resistive Amplifier Using CMOS Technology
MP0009	MP0009	US	09/049,830	3/27/1998	6125154	9/26/2000	In Force	DIGITAL SERVO CHANNEL FOR RECORDING APPARATUS
MP0009	MP0009C1	US	09/605,133	6/27/2000	6430238	8/6/2002	In Force	Digital Servo Channel For Recording Apparatus
MP0009	MP0009C2	US	10/114,578	4/2/2002	6606358	8/12/2003	In Force	Digital Servo Channel For Recording Apparatus
MP0009	MP0009C3	US	10/441,761	5/19/2003	6775338	8/10/2004	In Force	Digital Servo Channel For Recording Apparatus
MP0009	MP0009D1	US	10/441,605	5/19/2003	6985543	1/10/2006	In Force	Digital Servo Channel For Recording Apparatus
MP0010	MP0010	US	09/072,729	5/4/1998	6011437	1/4/2000	In Force	HIGH PRECISION, HIGH BANDWIDH VARIABLE GAIN AMPLIFIER AND
MP0011	MP0011	US	09/082,424	5/20/1998	6133861	10/17/2000	In Force	Selectable Delay Circuit Having Immunity To Variations In Fabrication
MP0011	MP0011C1	US	09/528,659	3/20/2000	6243031	6/5/2001	In Force	Selectable Delay Method And Circuit

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MP0012	MP0012	US	09/208,211	12/9/1998	6507449	1/14/2003	In Force	Digital Servo Demodulator With Synchronous Dithering And Method
MP0013	MP0013	US	09/434,207	11/4/1999	6427220	7/30/2002	In Force	METHOD AND APPARATUS FOR PRML DETECTION INCORPORATING
MP0014	MP0014	US	09/465,228	12/17/1999	6934387	8/23/2005	In Force	Method And Apparatus For Digital Near-End Echo/Near-End Crossta
MP0015	MP0015	US	09/470,170	12/22/1999	6526530	2/25/2003	In Force	Method And Apparatus For Encoding Data Incorporating Check Bits
MP0016	MP0016aaaa	US	13/230,769	9/12/2011	8619571	12/31/2013	In Force	Chip-To-Chip Interface For 1000BASE-T Gigabit Physical Layer Devic
MP0016	MP0016aaC1	US	12/723,915	3/15/2010	8018962	9/13/2011	In Force	Chip-To-Chip Interface For 1000 Base T Gigabit Physical Layer Devic
MP0016	MP0016C1	US	10/892,356	7/16/2004	7173942	2/6/2007	In Force	Chip-To-Chip Interface For 1000 Base T Gigabit Physical Layer Devic
MP0016	MP0016C2	US	11/516,359	9/6/2006	7680146	3/16/2010	In Force	Chip-To-Chip Interface For 1000 Base T Gigabit Physical Layer Devic
MP0016	MP0016Zaaa	US	14/138,569	12/23/2013	9407562	8/2/2016	In Force	Systems And Methods For Operating According To An Auto-Negotia
MP0016	MP0016ZZaa	US	15/224,902	8/1/2016	9614776	4/4/2017	In Force	Systems And Methods For Transmitting Data According To An Auto-
MP0016	MP0016ZZZa	US	15/477,767	4/3/2017	9961006	5/1/2018	In Force	Network Switch For Transmitting Data According To An Auto-Negot
MP0017	MP0017	US	09/501,557	2/9/2000	6232834	5/15/2001	In Force	Calibrated compensation for an operational amplifier
MP0018	MP0018	US	09/699,772	10/30/2000	6750727	6/15/2004	In Force	Low Phase Noise MOS LC Oscillator
MP0019	MP0019	US	09/746,687	12/22/2000	6380772	4/30/2002	In Force	SELF-LIMITING PAD DRIVER
MP0020	MP0020	US	09/737,743	12/18/2000	7194037	3/20/2007	In Force	Active Replica Transformer Hybrid
MP0020	MP0020C1	US	11/030,205	7/1/2005	7729429	1/6/2010	In Force	ACTIVE REPLICA TRANSFORMER HYBRID
MP0021	MP0021	US	09/607,904	6/30/2000	6456208	9/24/2002	In Force	NOVEL TECHNIQUE TO CONSTRUCT 32/33 AND OTHER RLL CODES
MP0022	MP0022	US	09/556,758	4/21/2000	6384713	5/7/2002	In Force	SERIAL COMPARATOR
MP0023	MP0023	US	09/572,155	5/17/2000	7167516	1/23/2007	In Force	Circuit And Method For Finding The Sampling Phase And Canceling Pr
MP0023	MP0023C1	US	11/656,729	1/23/2007	8767813	1/7/2014	In Force	Circuit and method for finding the sampling phase and canceling inte
MP0023	MP0023C1C1	US	14/313,196	6/24/2014	9264264	2/16/2016	In Force	Systems And Methods For Filtering A Received Signal To Remove Inte
MP0024	MP0024	US	09/536,120	3/27/2000	6856790	2/15/2005	In Force	Receiver With Dual D.C. Noise Cancellation Circuits
MP0024	MP0024C1C1	US	11/820,115	6/18/2007	7813702	10/12/2010	In Force	Receiver With Dual D.C. Noise Cancellation Circuits
MP0025	MP0025	US	09/546,039	4/10/2000	6429987	8/6/2002	In Force	HIGH SPEED INDUCTOR CURRENT DRIVER WITH MINIMUM OVERSH
MP0027	MP0027	US	09/717,240	11/22/2000	6804695	10/12/2004	In Force	Method And Apparatus For Constraining Tap Coefficients In An Adapt
MP0028	MP0028	US	09/589,417	6/7/2000	6246268	6/12/2001	In Force	CMOS integrated signal detection circuit with high efficiency and perf
MP0030	MP0030	US	09/587,991	6/6/2000	6411233	6/25/2002	In Force	Method and apparatus for direct RAM analog-to-digital converter cali
MP0030	MP0030C1	US	10/035,761	10/23/2001	6563446	5/13/2003	In Force	Method And Apparatus For Direct RAM Analog-To-Digital Converter C
MP0031	MP0031	US	09/599,907	6/23/2000	6359499	3/19/2002	In Force	Temperature And Process Independent CMOS Circuit
MP0032	MP0032	US	09/578,886	5/26/2000	6711225	3/23/2004	In Force	Method And Apparatus For Synchronization Of A Bit Stream For Long
MP0033	MP0033	US	09/713,199	11/16/2000	6606048	8/12/2003	In Force	METHOD AND APPARATUS FOR EQUALIZING THE DIGITAL PERFORM
MP0033	MP0033C1	US	10/460,120	6/12/2003	6762708	7/13/2004	In Force	Method And Apparatus For Equalizing The Digital Performance Of Mu
MP0034	MP0034	US	09/698,236	10/30/2000	6489827	12/3/2002	In Force	Reduction Of Offset Voltage In Current Mirror Circuit
MP0035	MP0035	US	09/737,474	12/18/2000	6462688	10/8/2002	In Force	DIRECT DRIVE PROGRAMMABLE HIGH SPEED POWER DIGITAL-TO-AN

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MP0035	MP0035RE	US	11/220,304	9/6/2005	RE40971	11/17/2009	In Force	Direct Drive Programmable High Speed Power Digital-To-Analog Conv
MP0036	MP0036	US	09/528,314	3/20/2000	6366168	4/2/2002	In Force	Efficient Ground Noise And Common-Mode Suppression Network
MP0037	MP0037	US	09/566,861	5/8/2000	6331803	12/18/2001	In Force	Constant Bandwidth, Variable Gain Amplifier, And Method
MP0038	MP0038	US	09/559,186	4/27/2000	6888897	5/3/2005	In Force	Multi-Mode Iterative Detector
MP0038	MP0038C1	US	10/927,325	8/27/2004	7340003	3/4/2008	In Force	Multi-Mode Iterative Detector
MP0038	MP0038C1C1	US	11/973,500	10/9/2007	8136005	3/13/2012	In Force	Multi-Mode Iterative Detector
MP0039	MP0039	US	09/629,092	7/31/2000	6775529	8/10/2004	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039aAC1	US	11/821,637	6/25/2007	7536162	5/19/2009	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039aBa	US	12/156,984	6/6/2008	8050645	11/1/2011	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039C1	US	10/786,010	2/26/2004	7466971	12/16/2008	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039C1D1	US	11/523,169	9/19/2006	7327995	2/5/2008	In Force	Active Resistance Summer For A Transformer Hybrid
MP0039	MP0039CIP	US	09/920,240	8/1/2001	7606547	10/20/2009	In Force	Active Resistance Summer For A Transformer Hybrid
MP0039	MP0039Faaa	US	13/958,915	8/5/2013	8880017	11/4/2014	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039I1aa	US	13/279,873	10/24/2011	8503961	8/6/2013	In Force	Active Resistive Summer For A Transformer Hybrid
MP0039	MP0039I1C1	US	12/581,415	10/19/2009	8045946	10/25/2011	In Force	Active Resistive Summer For A Transformer Hybrid
MP0040	MP0040	US	09/629,095	7/31/2000	6577114	6/10/2003	In Force	Calibration Circuit
MP0040	MP0040D1	US	10/379,116	3/4/2003	6737851	5/18/2004	In Force	Calibration Circuit
MP0040	MP0040D2	US	10/379,260	3/4/2003	6885181	4/26/2005	In Force	Calibration Circuit
MP0041	MP0041	US	09/564,711	5/3/2000	6515506	2/4/2003	In Force	Circuit For Reducing Pin Count Of A Semiconductor Chip And Method
MP0041	MP0041C1	US	10/287,527	11/5/2002	6831479	12/14/2004	In Force	Circuit For Reducing Pin Count Of A Semiconductor Chip And Method
MP0041	MP0041D1	US	10/287,528	11/5/2002	6741097	5/25/2004	In Force	Circuit For Reducing Pin Count Of A Semiconductor Chip And Method
MP0042	MP0042	US	09/678,728	10/4/2000	7127481	10/24/2006	In Force	Movable Tap Finite Impulse Response Filter
MP0042	MP0042C1	US	11/500,603	8/8/2006	7831647	11/9/2010	In Force	MOVABLE TAP FINITE IMPLUSE RESPONSE FILTER
MP0042	MP0042CIP	US	09/761,190	1/18/2001	7120656	10/10/2006	In Force	Movable Tap Finite Impulse Response Filter
MP0042	MP0042D2aa	US	13/917,165	6/13/2013	9093983	7/28/2015	In Force	Movable Tap Finite Impulse Response Filter
MP0042	MP0042D2C1	US	13/012,700	1/24/2011	8468188	6/18/2013	In Force	Movable Tap Finite Impulse Response Filter
MP0042	MP0042I1C1	US	11/501,185	8/8/2006	7584236	9/1/2009	In Force	Movable Tap Finite Impulse Response Filter
MP0042	MP0042ID1	US	11/496,667	7/31/2006	7831646	11/9/2010	In Force	Movable Tap Finite Impulse Response Filter
MP0043	MP0043	US	09/661,912	9/14/2000	6871251	3/22/2005	In Force	High Latency Interface Between Hardware Components
MP0043	MP0043baaa	US	13/404,510	2/24/2012	9519598	12/13/2016	In Force	High Latency Interface Between Hardware Components
MP0043	MP0043C1	US	10/962,321	10/12/2004	7171507	1/30/2007	In Force	High Latency Interface Between Hardware Components
MP0043	MP0043C2	US	11/311,848	12/19/2005	7389374	6/17/2008	In Force	High Latency Interface Between Hardware Components
MP0043	MP0043C2aa	US	12/858,457	8/18/2010	8127067	2/28/2012	In Force	High Latency Interface Between Hardware Components
MP0044	MP0044	US	09/603,188	6/26/2000	6473010	10/29/2002	In Force	Method And Apparatus For Determining Error Correction Code Failur

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0045	MP0045	US	09/620,545	7/20/2000	6859399	2/22/2005	In Force	Memory Architecture And System And Multiport Interface Protocol
MP0045	MP0045D1	US	10/778,592	2/17/2004	6868470	3/15/2005	In Force	Interface Protocol For A Disk Drive, SRAM And DRAM
MP0045	MP0045D2	US	11/014,777	12/20/2004	7405980	7/29/2008	In Force	Shared Terminal Memory Interface
MP0045	MP0045D2C1	US	12/220,607	7/25/2008	8386735	2/26/2013	In Force	Memory Architecture And System, And Interface Protocol
MP0046	MP0046	US	09/699,513	10/31/2000	6504493	1/7/2003	In Force	METHOD AND APPARATUS FOR ENCODING/DECODING DATA
MP0048	MP0048	US	10/118,504	4/8/2002	6868517	3/15/2005	In Force	Method And Apparatus For Checking Read Errors With Two Cyclic R
MP0048	MP0048C1	US	11/049,753	4/2/2005	7310765	12/18/2007	In Force	METHOD AND APPARATUS FOR CHECKING READ ERRORS WITH TW
MP0049	MP0049	US	09/615,062	7/12/2000	6633441	10/14/2003	In Force	Method And Apparatus For Measuring An Output Signal Of A Floati
MP0050	MP0050	US	09/583,316	5/30/2000	6920132	7/19/2005	In Force	Reduced Pin Gigabit Media Independent Interface
MP0050	MP0050baa	US	12/837,152	7/15/2010	7983259	7/19/2011	In Force	Reduced Pin Gigabit Media Independent Interface
MP0050	MP0050baaa	US	13/185,133	7/18/2011	8144635	3/27/2012	In Force	Reduced Pin Gigabit Media Independent Interface
MP0050	MP0050C1	US	11/072,323	3/7/2005	7065075	6/20/2006	In Force	Reduced Pin Gigabit Media Independent Interface
MP0050	MP0050C2	US	11/447,351	6/6/2006	7415013	8/19/2008	In Force	Reduced Pin Gigabit Media Independent Interface
MP0050	MP0050C2C1	US	12/228,960	8/18/2008	7760725	7/20/2010	In Force	Reduced Pin Gigabit Media Independent Interface
MP0051	MP0051	US	09/615,063	7/12/2000	6721913	4/13/2004	In Force	Method And Apparatus For Testing An Interface Between Separate H
MP0052	MP0052	US	09/874,949	6/5/2001	6721114	4/13/2004	In Force	Precompensation Circuit For Magnetic Recording
MP0052	MP0052C1	US	10/810,893	3/29/2004	6956708	10/18/2005	In Force	Precompensation Circuit For Magnetic Recording
MP0052	MP0052C2	US	11/250,373	10/17/2005	7184231	2/27/2007	In Force	Precompensation Circuit For Magnetic Recording
MP0052	MP0052C2C1	US	11/711,475	2/27/2007	7515368	4/7/2009	In Force	Precompensation Circuit For Magnetic Recording
MP0053	MP0053	US	09/725,818	11/30/2000	6732286	5/4/2004	In Force	High Latency Timing Circuit
MP0053	MP0053C1	US	10/831,100	4/26/2004	7049896	5/23/2006	In Force	High Latency Timing Circuit
MP0053	MP0053C2C1	US	11/507,916	8/22/2006	7304545	12/4/2007	In Force	High Latency Timing Circuit
MP0054	MP0054	US	09/697,714	10/27/2000	6633197	10/14/2003	In Force	Gate Capacitor Stress Reduction In CMOS/BICMOS Circuit
MP0055	MP0055	US	09/660,392	9/12/2000	6594098	7/15/2003	In Force	Acquistion Timing Loop For Read Channel
MP0055	MP0055C1	US	10/385,977	3/11/2003	6801380	10/5/2004	In Force	Acquisition Timing Loop For Read Channel
MP0056	MP0056	US	09/660,929	9/13/2000	6369967	4/9/2002	In Force	RACE AVOIDANCE IN DISK HEAD READ CIRCUIT
MP0056	MP0056C1aa	US	13/043,236	3/8/2011	8184391	5/22/2012	In Force	Phase-Adjustment Of Divided Clock In Disk Head Read Circuit
MP0057	MP0057	US	09/643,819	8/22/2000	6441765	8/27/2002	In Force	Analog To Digital Converter With Enhanced Differential Non-Linearity
MP0057	MP0057C1	US	10/176,409	6/19/2002	6545628	8/4/2003	In Force	ANALOG-TO-DIGITAL CONVERTER WITH ENHANCED DIFFERENTIAL N
MP0058	MP0058	US	09/648,770	8/28/2000	6400214	6/4/2002	In Force	Switched Capacitor Filter For Reference Voltages In Analog To Digital
MP0059	MP0059	US	09/648,462	8/28/2000	6417725	7/9/2002	In Force	High Speed Reference Buffer
MP0060	MP0060	US	09/654,392	9/1/2000	6369554	4/9/2002	In Force	A LINEAR REGULATOR WHICH PROVIDES STABILIZED CURRENT FLOW
MP0060	MP0060CN	CN	11311037	8/30/2001	ZL2001011311	5/18/2005	In Force	Frequency Compensation For A Linear Regulator
MP0060	MP0060JP	JP	2001-265940	9/3/2001	4680447	2/10/2011	In Force	Frequency Compensation for a Linear Regulator

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MP0061	MP0061	US	09/648,464	8/28/2000	6396334	5/28/2002	In Force	Charge Pump For Reference Voltages In Analog To Digital Converter
MP0062	MP0062I1	US	10/184,505	6/26/2002	7546172	6/9/2009	In Force	Apparatus, Method, And Computer Program Product For Recording
MP0062	MP0062I1C1	US	11/981,728	10/31/2007	8145331	3/27/2012	In Force	Apparatus, Method, And Computer Program For Recording And Rep
MP0062	MP0062I1C2	US	12/157,909	6/13/2008	9141619	9/22/2015	In Force	Apparatus, Method, And Computer Program Product For Recording
MP0062	MP0062I2	US	10/184,302	6/26/2002	7457676	11/25/2008	In Force	Vehicle For Recording And Reproducing Digital Data
MP0062	MP0062I2C1	US	11/981,601	10/31/2007	8374710	2/12/2013	In Force	Vehicle For Recording And Reproducing Digital Data
MP0062	MP0062I2C2	US	12/157,860	6/13/2008	8145332	3/27/2012	In Force	Vehicle For Recording And Reproducing Digital Data
MP0062	MP0062I3	US	10/184,299	6/26/2002	7315764	1/1/2008	In Force	Integrated Circuit, Method, And Computer Program Product For Re
MP0062	MP0062I4	US	10/690,022	10/20/2003	7577247	8/18/2009	In Force	Apparatus And Method For Telephone, Intercom, And Clock
MP0062	MP0062I5	US	10/692,644	10/24/2003	7778736	8/17/2010	In Force	Apparatus, Method, And Computer Program For Sprinkler Control
MP0062	MP0062I5C2	US	12/855,142	8/12/2010	8019482	9/13/2011	In Force	Method And Apparatus For Controlling A Sprinkler System
MP0062	MP0062I6	US	10/703,034	11/5/2003	7298252	11/20/2007	In Force	Apparatus, Method, And Computer Program For An Alarm System
MP0062	MP0062I6C1	US	11/981,729	10/31/2007	7522039	4/21/2009	In Force	Apparatus, Method, And Computer Program For An Alarm System
MP0063	MP0063	US	09/730,597	12/7/2000	6965652	11/15/2005	In Force	Address Generator For LDPC Encoder And Decoder And Method Ther
MP0063	MP0063D1	US	11/217,409	2/9/2005	7580485	8/25/2009	In Force	ADDRESS GENERATOR FOR LDPC ENCODER AND DECODER AND MET
MP0063	MP0063D1D1	US	11/595,179	11/9/2006	7760822	7/20/2010	In Force	Address Generator For LDPC Encoder And Decoder And Method Ther
MP0064	MP0064	US	09/730,752	12/7/2000	7072417	7/4/2006	In Force	LDPC Encoder And Method Thereof
MP0064	MP0064C1	US	11/343,775	1/31/2006	7583751	9/1/2009	In Force	LDPC Encoder Method Thereof
MP0065	MP0065	US	09/730,603	12/7/2000	7184486	2/27/2007	In Force	LDPC Encoder And Decoder And Method Thereof
MP0065	MP0065C1	US	11/397,318	4/4/2006	7453960	11/18/2008	In Force	LDPC Encoder And Encoder And Method Thereof
MP0065	MP0065C1C1	US	12/273,100	11/18/2008	7751505	7/6/2010	In Force	LDPC Encoder And Encoder And Method Thereof
MP0066	MP0066	US	09/881,474	6/14/2001	6570781	5/27/2003	In Force	Logic Process DRAM
MP0066	MP0066D1aB	US	11/449,957	6/9/2006	7609538	10/27/2009	In Force	Logic Process DRAM
MP0066	MP0066D1C1	US	10/734,060	12/12/2003	6947324	9/20/2005	In Force	Logic Process Dram
MP0067	MP0067	US	09/710,256	11/10/2000	6404290	6/11/2002	In Force	Fast Change Charge Pump Having Switchable Boost Function
MP0067	MP0067D1	US	10/135,646	4/29/2002	6566846	5/20/2003	In Force	Cascode Regulator With Plural Outputs
MP0068	MP0068	US	09/710,255	11/10/2000	6320796	11/20/2001	In Force	Variable Slope Charge Pump Control
MP0069	MP0069	US	09/730,598	12/7/2000	7000177	2/14/2006	In Force	Parity Check Matrix And Method Of Forming Thereof
MP0071	MP0071	US	09/759,151	1/16/2001	7281065	10/9/2007	In Force	Long Latency Interface Protocol
MP0071	MP0071aZaa	US	14/059,522	10/22/2013	9576606	2/21/2017	In Force	Long Latency Interface Protocol
MP0071	MP0071C1	US	11/973,633	10/9/2007	7487268	2/3/2009	In Force	Long Latency Interface Protocol
MP0071	MP0071C1aa	US	12/876,715	9/7/2010	8180946	5/15/2012	In Force	Long Latency Interface Protocol
MP0071	MP0071C1Za	US	13/470,930	5/14/2012	8566499	10/22/2013	In Force	Long Latency Interface Protocol
MP0072	MP0072C1	US	11/011,178	12/15/2004	7426236	9/16/2008	In Force	FEEDFORWARD EQUALIZER FOR DFE BASED DETECTOR

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MP0072	MP0072C1aa	US	13/252,098	10/3/2011	8315301	11/20/2012	In Force	Feedforward Equalizer For DFE Based Detector
MP0072	MP0072C1C1	US	12/283,664	9/15/2008	8031765	10/4/2011	In Force	Feedforward Equalizer For DFE Based Detector
MP0073	MP0073	US	09/738,715	12/15/2000	6388506	5/14/2002	In Force	Regulator With Leakage Compensation
MP0074	MP0074	US	09/765,200	1/18/2001	6625006	9/23/2003	In Force	Fringing Capacitor Structure
MP0074	MP0074D1	US	10/372,617	2/21/2003	6784050	8/31/2004	In Force	Fringing Capacitor Structure
MP0074	MP0074D2	US	10/836,389	4/30/2004	6974744	12/13/2005	In Force	Fringing Capacitor Structure
MP0077	MP0077	US	09/901,507	7/9/2001	7099411	8/29/2006	In Force	Soft-Output Decoding Method And Apparatus For Controlled Inters
MP0077	MP0077C1	US	11/217,408	9/2/2005	7319726	1/15/2008	In Force	Soft-Output Decoding Method And Apparatus For Controlled Inters
MP0078	MP0078	US	10/054,595	1/22/2002	7257125	8/14/2007	In Force	Quality Of Service Half-Duplex Media Access Controller
MP0078	MP0078C1	US	11/893,104	8/14/2007	8139600	3/20/2012	In Force	Quality Of Service Half-Duplex Media Access Controller
MP0078	MP0078C1C1	US	12/074,802	3/6/2008	7684429	3/23/2010	In Force	Quality Of Service Half-Duplex Media Access Controller
MP0078	MP0078C3	US	13/421,170	3/15/2012	8693492	4/8/2014	In Force	Quality Of Service Half-Duplex Media Access Controller
MP0079	MP0079C1I	US	10/754,325	1/9/2004	7286311	10/23/2007	In Force	Thermal Asperity Compensation In Perpendicular Recording
MP0082	MP0082	US	09/991,284	11/21/2001	7570657	8/4/2009	In Force	Autonegotiation Between 1000BASE-X and 1000BASE-T
MP0083	MP0083	US	09/990,137	11/21/2001	6993667	1/31/2006	In Force	Apparatus For Automatic Energy Savings Mode For Ethernet Transcei
MP0083	MP0083D1	US	11/254,986	10/20/2005	7278039	10/2/2007	In Force	PHYSICAL LAYER ENERGY SAVING TECHNIQUES FOR TRANSCEIVERS
MP0084	MP0084	US	09/880,599	6/13/2001	6459246	10/1/2002	In Force	Voltage Regulator
MP0085	MP0085C1	US	10/695,531	10/28/2003	6917314	7/12/2005	In Force	Method And Apparatus For DC-Level Constrained Coding
MP0085	MP0085C2	US	11/166,425	6/24/2005	7286065	10/23/2007	In Force	Method And Apparatus For DC-Level Constrained Coding
MP0086	MP0086	US	09/991,043	11/21/2001	7624197	11/24/2009	In Force	Apparatus And Method For Automatic Speed Downshift For A Two Pa
MP0086	MP0086C1	US	12/624,522	11/24/2009	7979587	7/12/2011	In Force	Apparatus And Method For Automatic Speed Downshift For A Two Pa
MP0087	MP0087	US	10/184,334	6/26/2002	7073099	7/4/2006	In Force	Method And Apparatus For Improving Memory Operation And Yield
MP0087	MP0087BFaa	US	13/632,547	10/1/2012	8745450	6/3/2014	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087BFay	TW	96128368	8/2/2007	I453751	9/21/2014	In Force	FULLY-BUFFERED DUAL IN-LINE MEMORY MODULE WITH FAULT COR
MP0087	MP0087BFba	US	12/908,376	10/20/2010	8103921	1/24/2012	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087Bfby	TW	96128369	8/2/2007	I462112	11/21/2014	In Force	FULLY-BUFFERED DUAL IN-LINE MEMORY MODULE WITH FAULT COR
MP0087	MP0087BFTW	TW	96128366	8/2/2007	I432957	4/1/2014	In Force	MEMORY MODULE AND HOST DEVICE WITH FAULT CORRECTION
MP0087	MP0087BFZa	US	14/292,020	5/30/2014	9158620	10/13/2015	In Force	Systems And Methods For Testing Pages Of Data Stored In A Memory
MP0087	MP0087Bi1a	US	11/655,449	1/19/2007	7814382	10/12/2010	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087Bi1b	US	11/655,451	1/19/2007	7823030	10/26/2010	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087Bi1e	US	11/655,603	1/19/2007	7870331	1/11/2011	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087C1	US	11/327,933	1/9/2006	7809998	10/5/2010	In Force	Method And Apparatus For Improving Memory Operation And Yield
MP0087	MP0087D2	US	11/328,373	1/9/2006	7962809	6/14/2011	In Force	Method And Apparatus For Improving Memory Operation And Yield
MP0087	MP0087D2D1	US	11/589,368	10/30/2006	7818636	10/19/2010	In Force	Method And Apparatus For Improving Memory Operation And Yield

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MP0087	MP0087D2Fa	US	12/903,606	10/13/2010	8281191	10/2/2012	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087D2I1	US	11/584,946	10/23/2006	7818639	10/19/2010	In Force	Fully-Buffered Dual In-Line Memory Module With Fault Correction
MP0087	MP0087D2I1WO	WO	PCT/US2007/01	7/24/2007			Pending	Fully Buffered Dual In-Line Memory Module With Fault Correction
MP0088	MP0088	US	09/920,241	8/1/2001	7433665	10/7/2008	In Force	Apparatus And Method For Converting Single-Ended Signals To A Di
MP0088	MP0088C1	US	12/075,231	3/10/2008	7761076	7/20/2010	In Force	Apparatus And Method For Converting Single-Ended Signals To A Di
MP0088	MP0088I	US	10/191,924	7/8/2002	6844837	1/18/2005	In Force	Class B Driver
MP0088	MP0088IC1	US	10/972,143	10/25/2004	7280060	10/9/2007	In Force	Communication Driver
MP0088	MP0088IC4	US	11/432,886	5/12/2006	7312739	12/25/2007	In Force	Communication Driver
MP0088	MP0088ICAa	US	12/004,200	12/20/2007	7649483	1/19/2010	In Force	Communication Driver
MP0088	MP0088IGaa	US	12/689,066	1/18/2010	8009073	8/30/2011	In Force	Method And Apparatus For Generating An Analog Signal Having A P
MP0088	MP0088IRE	US	11/284,395	11/21/2005	RE41831	10/19/2010	In Force	Class B Driver
MP0090	MP0090C1	US	10/921,810	8/20/2004	6987634	1/17/2006	In Force	WRITE CHANNEL HAVING A PREAMPLIFIER AND A NON-UNIFORM T
MP0091	MP0091	US	10/067,556	2/4/2002	7218691	5/15/2007	In Force	Method And Apparatus For Estimation Of Orthogonal Frequency Di
MP0091	MP0091D1	US	11/801,032	5/8/2007	7532693	5/12/2009	In Force	Method And Apparatus For Acquistion And Tracking Of Orthogonal Fr
MP0091	MP0091D1AA	US	13/660,580	10/25/2012	8619922	12/31/2013	In Force	Method And Apparatus For Acquisition And Tracking Of Orthogonal F
MP0091	MP0091D1D1	US	12/393,547	2/26/2009	8300743	10/30/2012	In Force	Method And Apparatus For Acquisition And Tracking Of Orthogonal F
MP0094	MP0094	US	10/072,843	2/6/2002	6762644	7/13/2004	In Force	Apparatus And Method For A Nested Transimpedance Amplifier
MP0094	MP0094C1	US	10/838,040	5/3/2004	6836182	12/28/2004	In Force	Nested Transimpedance Amplifier
MP0094	MP0094fAaa	US	12/857,949	8/17/2010	8159293	4/17/2012	In Force	Nested Transimpendance Amplifier
MP0094	MP0094fAC1	US	12/627,432	11/30/2009	7808311	10/5/2010	In Force	Nested Transimpedance Amplifier
MP0094	MP0094faJP	JP	2012037544	1/18/2007	5367857	9/20/2013	In Force	Nested Transimpedance Amplifier
MP0094	MP0094faSG	SG	2007002272	1/17/2007	134251	7/31/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094faU6	EP	07000516.0	1/11/2007	1811652	3/27/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094faU6DE	DE	07000516.0	1/11/2007	602007057935	3/27/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094faU6FR	FR	07000516.0	1/11/2007	1811652	3/27/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094faU6GB	GB	07000516.0	1/11/2007	1811652	3/27/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094fay6	TW	96101880	1/18/2007	I420804	12/21/2013	In Force	NESTED TRANSIMPEDANCE AMPLIFIER
MP0094	MP0094fbCN	CN	200710002413.:	1/17/2007	ZL2007100024	4/18/2012	In Force	NESTED TRANSIMPEDANCE AMPLIFIER
MP0094	MP0094fbJP	JP	2007009334	1/18/2007	4866745	11/18/2011	In Force	Nested Transimpedance Amplifier
MP0094	MP0094feCN	CN	200710002414.3	1/17/2007	ZL2007100024	4/18/2012	In Force	NESTED TRANSIMPEDANCE AMPLIFIER
MP0094	MP0094feSG	SG	2007002280	1/17/2007	134252	8/31/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094feU8	EP	EP07000517.8	1/11/2007			Pending	Nested Transimpedance Amplifier
MP0094	MP0094fey8	TW	96101882	1/18/2007	I433452	4/1/2014	In Force	NESTED TRANSIMPEDANCE AMPLIFIER
MP0094	MP0094fSG4	SG	2007002264	1/17/2007	134250	8/31/2009	In Force	Nested Transimpedance Amplifier

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MP0094	MP0094I	US	10/459,731	6/11/2003	7276965	10/2/2007	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2	US	11/495,813	7/28/2006	7551024	6/23/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2C1	US	11/643,089	12/21/2006	7616057	11/10/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2C1HK	HK	81000803	1/17/2007			Pending	Nested Transimpedance Amplifier
MP0094	MP0094I2C2	US	11/643,432	12/21/2006	7405616	7/29/2008	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2C3	US	11/643,557	12/21/2006	7605649	10/20/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2C3HK	HK	81000812	1/17/2007			Pending	Nested Transimpedance Amplifier
MP0094	MP0094I2C3JP	JP	2007009330	1/18/2007	5049020	7/27/2012	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2CN	CN	200710002412.5	1/17/2007	ZL2007100024	9/26/2012	In Force	NESTED TRANSIMPEDANCE AMPLIFIER
MP0094	MP0094I2D1	US	12/217,800	7/9/2008	7626453	12/1/2009	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2EP	EP	EP07000520.2	1/11/2007	1811657	3/13/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2EPDE	DE	EP07000520.2	1/11/2007	602007057817	3/13/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2EPFR	FR	EP07000520.2	1/11/2007	1811657	3/13/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2EPGB	GB	EP07000520.2	1/11/2007	1811657	3/13/2019	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2HK	HK	81000885	1/17/2007			Pending	Nested Transimpedance Amplifier
MP0094	MP0094I2JP	JP	2007009337	1/18/2007	5010293	6/8/2012	In Force	Nested Transimpedance Amplifier
MP0094	MP0094I2TW	TW	96101878	1/18/2007	I416864	11/21/2013	In Force	Nested Transimpedance Amplifier
MP0094	MP0094IC1	US	11/455,362	6/19/2006	7173486	2/6/2007	In Force	Nested Transimpedance Amplifier With Capacitive Cancellation Of In
MP0094	MP0094ID1	US	11/106,248	4/14/2005	7304536	12/4/2007	In Force	NESTED TRANSIMPENDANCE AMPLIFIER
MP0095	MP009S	US	10/059,516	1/28/2002	6775091	8/10/2004	In Force	Repeatable Run-Out Compensation For Disk Drive
MP0096	MP0096	US	10/074,747	2/11/2002	7149955	12/12/2006	In Force	Encoding And Decoding Apparatus And Method With Hamming Weig
MP0096	MP0096D1	US	11/606,678	11/30/2006	7783937	8/24/2010	In Force	Encoding And Decoding Apparatus And Method With Hamming Weig
MP0096	MP0096D2	US	11/606,510	11/30/2006	8359498	1/22/2013	In Force	Encoding And Decoding Apparatus And Method With Hamming Weig
MP0096	MP0096D3	US	11/606,677	11/30/2006	7802151	9/21/2010	In Force	Encoding And Decoding Apparatus And Method With Hamming Weig
MP0113	MP0113	US	10/098,865	3/15/2002	7203851	4/10/2007	In Force	Method And Apparatus For Detecting And Supplying Power By A First
MP0113	MP0113C1	US	11/732,010	4/2/2007	7552348	6/23/2009	In Force	Method And Apparatus For Detecting And Supplying Power By A First
MP0115	MP0115	US	09/966,914	9/27/2001	6979894	12/27/2005	In Force	Integrated Chip Package Having Intermediate Substrate
MP0115	MP0115D1	US	10/346,428	1/16/2003	6995463	2/7/2006	In Force	Integrated Chip Package Having Intermediate Substrate And Multiple
MP0115	MP0115D2	US	10/346,771	1/16/2003	6911724	6/28/2005	In Force	IMPROVED INTEGRATED CHIP PACKAGE HAVING INTERMEDIATE SUB
MP0115	MP0115D2C1	US	11/133,358	5/20/2005	8525317	9/3/2013	In Force	Integrated Chip Package Having Intermediate Substrate With Capacit
MP0116	MP0116	US	10/051,965	1/16/2002	6770982	8/3/2004	In Force	SEMICONDUCTOR DEVICE POWER DISTRIBUTION SYSTEM AND MET
MP0116	MP0116AFAA	US	14/163,429	1/24/2014	9240372	1/19/2016	In Force	Semiconductor Die Having Lead Wires Formed Over A Circuit In A Shi
MP0116	MP0116D1FA	US	13/601,442	8/31/2012	8637975	1/28/2014	In Force	Semiconductor Device Having Lead Wires Connecting Bonding Pads F
MP0116	MP0116D1I1	US	11/024,309	12/28/2004	8258616	9/4/2012	In Force	SEMICONDUCTOR DICE HAVING A SHIELDED AREA CREATED UNDER

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MP0117	MP0117	US	10/071,417	2/6/2002	7035273	4/25/2006	In Force	Quality Of Service Queueing System For A Network Switch
MP0118	MP0118	US	10/086,084	2/26/2002	7054398	5/30/2006	In Force	Method For Selecting Sync Marks And Sync Mark Detectors
MP0120	MP0120	US	10/085,683	2/26/2002	7248568	7/24/2007	In Force	Apparatus For Detecting Hidden Nodes In A Wireless Network
MP0120	MP0120C1	US	11/880,772	7/24/2007	8159984	4/17/2012	In Force	Apparatus And Method For Detecting Hidden Nodes In A Wireless N
MP0120	MP0120C2	US	11/880,771	7/24/2007	7965669	6/21/2011	In Force	Apparatus And Method For Detecting Hidden Nodes In A Wireless N
MP0121	MP0121	US	09/991,046	11/21/2001	7054309	5/30/2006	In Force	Ethernet Automatic Fiber/Copper Media Selection Logic
MP0121	MP0121C1	US	11/400,457	4/7/2006	7639675	12/29/2009	In Force	Ethernet Automatic Media Selection Logic
MP0121	MP0121C1C1	US	12/644,483	12/22/2009	7907605	3/15/2011	In Force	Method And Apparatus For Selecting A Media For Communication I
MP0121	MP0121Faa	US	12/748,946	3/29/2010	8018923	9/13/2011	In Force	Method and Apparatus For Establishing A Link Over A Preferred Me
MP0121	MP0121I	US	10/435,301	5/9/2003	7324507	1/29/2008	In Force	Ethernet Automatic Fiber/Copper Media Selection Logic
MP0121	MP0121IC1	US	12/011,532	1/28/2008	7688812	3/30/2010	In Force	Ethernet Automatic Media Selection Logic With Preferred Medium
MP0122	MP0122	US	10/010,732	12/5/2001	7042893	5/9/2006	In Force	Serial Media Independent Interface With Double Data Rate
MP0122	MP0122C1	US	11/378,551	3/17/2006	7672326	3/2/2010	In Force	Serial Media Independent Interface With Double Data Rate
MP0122	MP0122C1C1	US	12/714,746	3/1/2010	8094668	1/10/2012	In Force	Physical Layer Device Including A Serial Media Independent Interface
MP0123	MP0123	US	10/106,720	3/26/2002	7292596	11/6/2007	In Force	Method And Apparatus For Automatic Crossover And Parallel Detect
MP0126	MP0126	US	09/987,824	11/16/2001	6898655	5/24/2005	In Force	Multiport High Speed Communications Integrated Circuit
MP0126	MP0126C1	US	11/072,325	3/7/2005	7099982	8/29/2006	In Force	Multi-Port Communications Integrated Circuit
MP0126	MP0126C1C1	US	11/506,555	8/18/2006	7308521	12/11/2007	In Force	Multi-Port Communications Integrated Circuit And Method For Facilit
MP0132	MP0132	US	10/096,211	3/8/2002	7272114	9/18/2007	In Force	Physical Layer And Physical Layer Diagnostic System With Reversed L
MP0132	MP0132C1	US	11/901,694	9/18/2007	7701861	4/20/2010	In Force	Physical Layer And Physical Layer Diagnostic System With Reversed L
MP0133	MP0133	US	10/080,454	2/21/2002	7239648	7/3/2007	In Force	Extension Mode For Wireless LANS Complying With Short Interframe
MP0133	MP0133aaaa	US	14/032,694	9/20/2013	9231737	1/5/2016	In Force	Method And Apparatus For Remotely Increasing Available Processing
MP0133	MP0133C1	US	11/824,993	7/3/2007	7468990	12/23/2008	In Force	Extension Mode For Wireless LANS Complying With Short Interframe
MP0133	MP0133C1aa	US	13/180,260	7/11/2011	8542703	9/24/2013	In Force	Extension Mode For Wireless LANs Complying With Short Interframe
MP0133	MP0133C1C1	US	12/151,185	5/5/2008	7978675	7/12/2011	In Force	Extension Mode For Wireless LANS Complying With Short Interframe
MP0133	MP0133C2	US	11/824,781	7/3/2007	7468989	12/23/2008	In Force	EXTENSION MODE FOR WIRELESS LANS COMPYING WITH SHORT INT
MP0135	MP0135	US	10/099,050	3/15/2002	7161911	1/9/2007	In Force	Method And Apparatus For Autonegotiation Between Network Devic
MP0135	MP0135C1	US	11/650,052	1/5/2007	7724692	5/25/2010	In Force	Method And Apparatus For Autonegotiation Between Network Devic
MP0136	MP0136C1	US	10/189,385	7/5/2002	7577085	8/18/2009	In Force	MULTICARRIER TRANSMIT DIVERSITY
MP0136	MP0136C1AA	US	14/320,515	6/30/2014	9614605	4/4/2017	In Force	MULTICARRIER TRANSMIT DIVERSITY
MP0136	MP0136C1C1	US	12/507,418	7/22/2009	8767523	7/1/2014	In Force	MULTICARRIER TRANSMIT DIVERSITY
MP0137	MP0137	US	10/077,042	2/14/2002	8244916	8/14/2012	In Force	Method And Apparatus For Enabling A Network Interface To Support
MP0137	MP0137C1	US	13/584,341	8/13/2012	8447887	5/21/2013	In Force	Method And Apparatus For Enabling A Network Interface To Support
MP0137	MP0137C1C1	US	13/894,876	5/15/2013	8886839	11/11/2014	In Force	Method And Apparatus For Enabling A Network Interface To Support

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MP0138	MP0138	US	10/253,183	9/23/2002	7286528	10/23/2007	In Force	Multiple Address Databases In A Switch Without The Need For Extra
MP0138	MP0138C1	US	11/975,981	10/23/2007	7995581	8/9/2011	In Force	Multiple Address Databases In A Switch Without The Need For Extra
MP0138	MP0138C1C1	US	13/205,105	8/8/2011	8588229	11/19/2013	In Force	Multiple Address Databases In A Switch Without The Need For Extra
MP0139	MP0139D1	US	11/523,252	9/19/2006	7369619	5/6/2008	In Force	Intercircuit Communications Apparatus And Method
MP0140	MP0140	US	10/170,306	6/12/2002	7388931	6/17/2008	In Force	ADC Architecture For Wireless Applications
MP0140	MP0140C1C1	US	12/945,035	11/12/2010	8031810	10/4/2011	In Force	ADC Architecture For Wireless Applications
MP0144	MP0144	US	10/135,422	4/29/2002	6961197	11/1/2005	In Force	Correcting Errors In Disk Drive Read Back Signals By Iterating With T
MP0144	MP0144C1C1	US	11/903,957	9/25/2007	8074151	12/6/2011	In Force	Correcting Errors In Disk Drive Read Back Signals By Iterating With t
MP0144	MP0144C2	US	11/217,349	9/2/2005	7372652	5/13/2008	In Force	Correcting Errors In Disk Drive Read Back Signals By Iterating With T
MP01441	MP01441	US	10/313,651	12/6/2002	7751138	7/6/2010	In Force	Correcting Errors In Disk Drive Read Back Signals By Iterating With T
MP0145	MP0145	US	10/208,312	7/29/2002	6931585	8/16/2005	In Force	Detection In The Presence Of Media Noise
MP0145	MP0145IC1	US	11/642,084	12/20/2006	7917836	3/29/2011	In Force	Detection In The Presence Of Media Noise
MP0148	MP0148	US	10/155,777	5/24/2002	6917313	7/12/2005	In Force	DC-Free Codes
MP0148	MP0148C1	US	11/181,084	7/14/2005	7106223	9/12/2006	In Force	DC-Free Codes
MP0148	MP0148C2	US	11/181,401	7/14/2005	7199731	4/3/2007	In Force	DC-Free Codes
MP0148	MP0148C3	US	11/181,545	7/14/2005	7187308	3/6/2007	In Force	DC-Free Codes
MP0148	MP0148C3C1	US	11/713,996	3/5/2007	7623048	11/24/2009	In Force	DC-Free Codes
MP0148	MP0148I	US	10/765,431	1/26/2004	7298294	11/20/2007	In Force	Filter Driven DC-Free Codes
MP0148	MP0148Iaa	US	12/352,872	1/13/2009	8009068	8/30/2011	In Force	Filter Driven DC-Free Code
MP0148	MP0148IC1	US	11/729,747	3/29/2007	7479904	1/20/2009	In Force	Filter Driven DC-Free Code
MP0151	MP0151	US	10/193,439	7/10/2002	7346135	3/18/2008	In Force	Compensation For Residual Frequency Offset, Phase Noise And Samp
MP0151	MP0151aaaa	US	14/314,684	6/25/2014	9185013	11/10/2015	In Force	Systems And Methods For Compensating A Channel Estimate For Sa
MP0151	MP0151C1	US	12/070,375	2/15/2008	7616719	11/10/2009	In Force	Compensation For Residual Frequency Offset, Phase Noise And Samp
MP0151	MP0151C1AA	US	13/548,883	7/13/2012	8767879	7/1/2014	In Force	Compensation For Residual Frequency Offset, Phase Noise And Samp
MP0151	MP0151C1C1	US	12/615,605	11/10/2009	8223893	7/17/2012	In Force	Compensation For Residual Frequency Offset, Phase Noise And Samp
MP0151	MP0151ZZa	US	14/927,982	10/30/2015	9432276	8/30/2016	In Force	Systems And Methods For Compensating A Channel Estimate For Pha
MP0152	MP0152	US	10/259,142	9/26/2002	7133473	11/7/2006	In Force	Divisionless Baseband Equalization In Symbol Modulated Communica
MP0152	MP0152aaa	US	12/645,644	12/23/2009	8130853	3/6/2012	In Force	Method And Apparatus For Performing Baseband Equalization In Sym
MP0152	MP0152aaaa	US	13/404,656	2/24/2012	8437417	5/7/2013	In Force	Method And Apparatus For Performing Baseband Equalization In Sym
MP0152	MP0152aaaaa	US	13/888,473	5/7/2013	8675786	3/18/2014	In Force	Method And Apparatus For Performing Baseband Equalization In Sym
MP0152	MP0152C1	US	11/590,708	10/31/2006	7564913	7/21/2009	In Force	Divisionless Baseband Equalization In Symbol Modulated Communica
MP0152	MP0152C1C1	US	11/645,881	12/27/2006	7643586	1/5/2010	In Force	Divisionless Baseband Equalization In Symbol Modulated Communica
MP0152	MP0152ZZa	US	14/215,636	3/17/2014	9036743	5/19/2015	In Force	System And Method For Performing Maximum Ratio Combining On A
MP0153	MP0153	US	10/158,337	5/30/2002	7362771	4/22/2008	In Force	Reduced Latency FIFO

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MP0153	MP0153C1	US	12/070,334	2/15/2008	7949002	5/24/2011	In Force	Reduced Latency FIFO
MP0154	MP0154	US	10/153,554	5/22/2002	7085330	8/1/2006	In Force	Method And Apparatus For Amplifier Linearization Using Adaptive F
MP0154	MP0154C1	US	11/412,637	4/27/2006	7362821	4/22/2008	In Force	Method And Apparatus For Amplifier Linearization Using Adaptive F
MP0154	MP0154C1C1	US	12/148,765	4/22/2008	7535974	5/19/2009	In Force	METHOD AND APPARATUS FOR AMPLIFIER LINEARIZATION USING A
MP0155	MP0155	US	10/242,135	9/10/2002	7293289	11/6/2007	In Force	Apparatus, Method And Computer Program Product For Detection
MP0155	MP0155C1	US	11/818,104	6/13/2007	7877805	1/25/2011	In Force	Apparatus, Method And Computer Program Product For Detection
MP0155	MP0155C1C1	US	13/013,294	1/25/2011	8151351	4/3/2012	In Force	Apparatus, Method And Computer Program Product For Detection
MP0156	MP0156	US	10/246,870	9/19/2002	6856200	2/15/2005	In Force	Protection Circuit And Method For RF Power Amplifiers In WLAN Tr
MP0156	MP0156C1	US	10/839,679	5/4/2004	7477107	1/13/2009	In Force	Protection Circuit And Method For RF Power Amplifiers In WLAN Tr
MP0156	MP0156C1C1	US	12/351,040	1/9/2009	7855604	12/21/2010	In Force	Protection Circuit And Method For RF Power Amplifiers In WLAN Tr
MP0157	MP0157	US	10/253,364	9/24/2002	7215933	5/8/2007	In Force	Local Transmitter Output Power Control System For Wireless Applic
MP0158	MP0158	US	10/195,047	7/12/2002	6819187	11/16/2004	In Force	LIMIT SWING CHARGE PUMP AND METHOD THEREOF
MP0158	MP0158I	US	10/219,225	8/13/2002	6747520	6/8/2004	In Force	Limit Swing Charge Pump And Method Thereof
MP0158	MP0158IC2	US	11/387,391	3/23/2006	7315197	1/1/2008	In Force	Limit Swing Charge Pump And Method Thereof
MP0159	MP0159	US	10/238,475	9/10/2002	7006824	2/28/2006	In Force	Frame/Packet-Based Calibration For Wireless Transceivers
MP0159	MP0159C1	US	11/322,019	12/29/2005	7817998	10/19/2010	In Force	Frame/Packet-Based Calibration For Wireless Transceivers
MP0159	MP0159C1C1	US	12/907,837	10/19/2010	8605633	12/10/2013	In Force	Frame/Packet-Based Calibration For Wireless Transceivers
MP0161	MP0161	US	10/150,147	5/17/2002	7110415	9/19/2006	In Force	Apparatus And Method For Dynamically Limiting Output Queue Size I
MP0161	MP0161C1	US	11/523,185	9/19/2006	7764703	7/27/2010	In Force	Apparatus And Method For Dynamically Limiting Output Queue Size I
MP0161	MP0161C1aa	US	13/220,545	8/29/2011	8718077	5/6/2014	In Force	Apparatus And Method For Dynamically Limiting Output Queue Size I
MP0161	MP0161C1C1	US	12/840,327	7/21/2010	8009689	8/30/2011	In Force	Apparatus And Method For Dynamically Limiting Output Queue Size I
MP0162	MP0162	US	10/226,549	8/23/2002	6727736	4/27/2004	In Force	VOLTAGE CONTROL OSCILLATOR NOISE REDUCTION TECHNIQUE AN
MP0163	MP0163baa	US	11/890,738	8/7/2007	7463095	12/9/2008	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163baaa	US	12/284,810	9/25/2008	7598814	10/6/2009	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163C1	US	11/049,211	2/2/2005	7088187	8/8/2006	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163C2	US	11/435,995	5/17/2006	7190230	3/13/2007	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163C2C1	US	11/650,681	1/8/2007	7253690	8/7/2007	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163D1	US	11/175,943	7/5/2005	7230491	6/12/2007	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0163	MP0163D2	US	11/175,560	7/5/2005	7230494	6/12/2007	In Force	Method And Apparatus For An LNA With High Linearity And Improve
MP0167	MP0167C1	US	11/280,027	11/16/2005	7440742	10/21/2008	In Force	Mixer Gain Calibration Method And Apparatus
MP0168	MP0168	US	10/292,181	11/12/2002	7653071	1/26/2010	In Force	Router Having A Single CPU Mac
MP0168	MP0168D1	US	11/809,823	6/1/2007	7564857	7/21/2009	In Force	Router Having A Single CPU MAC
MP0168	MP0168D2	US	11/809,844	6/1/2007	7715415	5/11/2010	In Force	Router Having A Single CPU MAC
MP0168	MP0168D3	US	11/809,822	6/1/2007	7742488	6/22/2010	In Force	Router Having A Single CPU MAC

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MP0168	MP0168I1	US	10/990,231	11/16/2004	7668203	2/23/2010	In Force	Network Switch Using A Steering Header For Management Frames
MP0168	MP0168I1C1	US	12/709,715	2/22/2010	8111715	2/7/2012	In Force	Method And Apparatus For Transferring A Frame Of Data From A Fi
MP0168	MP0168I1C1C1	US	13/366,791	2/6/2012	8804738	8/12/2014	In Force	Method And Apparatus For Transferring A Frame Of Data From A Fi
MP0171	MP0171	US	10/301,349	11/20/2002	6784738	8/31/2004	In Force	Method And Apparatus For Gain Control In A CMOS Low Noise Amp
MP0171	MP0171C1	US	10/889,643	7/12/2004	6897729	5/24/2005	In Force	Self-Calibrating Gain Control Circuit For Low Noise Amplifier
MP0172	MP0172	US	10/353,451	1/28/2003	7120834	10/10/2006	In Force	Fast Port Failover In A Network Switch
MP0172	MP0172aaa	US	12/906,911	10/18/2010	8117503	2/14/2012	In Force	Fast Port Failover In A Network Switch
MP0172	MP0172aaaa	US	13/371,918	2/13/2012	8566649	10/22/2013	In Force	Fast Port Failover In A Network Switch
MP0172	MP0172C1	US	11/525,326	9/22/2006	7308612	12/11/2007	In Force	Fast Port Failover In A Network Switch
MP0172	MP0172C1C1	US	12/001,292	12/11/2007	7818628	10/19/2010	In Force	Fast Port Failover In A Network Switch
MP0172	MP0172Zaaa	US	14/059,947	10/22/2013	9344328	5/17/2016	In Force	Fast Port Failover In A Network Switch
MP0173	MP0173	US	10/141,096	5/7/2002	7209440	4/24/2007	In Force	Method And Apparatus For Preventing Blocking In A Quality Of Serv
MP0173	MP0173C1	US	11/789,315	4/24/2007	7321554	1/22/2008	In Force	Method And Apparatus For Preventing Blocking In A Quality Of Serv
MP0174	MP0174	US	10/189,321	7/3/2002	7409057	8/5/2008	In Force	Nonlinear Echo Compensator For Class B Transmitter Line Driver
MP0174	MP0174C1C1	US	14/289,980	5/29/2014	8989251	3/24/2015	In Force	METHOD AND APPARATUS TO COMPENSATE FOR NONLINEAR ECHO
MP0176	MP0176	US	10/353,482	1/28/2003	8635374	1/21/2014	In Force	Automatic Media Converter
MP0176	MP0176C1	US	14/158,141	1/17/2014	10075330	9/11/2018	In Force	Systems And Methods For Statuses Of Communication Links
MP0177	MP0177	US	10/305,091	11/26/2002	7051267	5/23/2006	In Force	Efficient High-Speed Reed-Solomon Decoder
MP0177	MP0177C1	US	11/407,436	4/20/2006	7322004	1/22/2008	In Force	Efficient High-Speed Reed-Solomon Decoder
MP0178	MP0178	US	10/304,511	11/26/2002	7010739	3/7/2006	In Force	Error Evaluator for Inversionless Berlekamp-Massey Algorithm in Ree
MP0178	MP0178C1	US	11/243,283	10/4/2005	7249310	7/24/2007	In Force	Error Evaluator For Inversionless Berlekamp-Massey Algorithm In Ree
MP0179	MP0179	US	10/165,467	6/7/2002	6825672	11/30/2004	In Force	Cable Tester
MP0179	MP0179D1	US	10/957,285	10/1/2004	7190172	3/13/2007	In Force	Cable Tester
MP0179	MP0179fBA	US	11/895,372	8/24/2007	7403018	7/22/2008	In Force	Cable Tester
MP0179	MP0179fBaa	US	12/082,964	4/15/2008	7884615	2/8/2011	In Force	Cable Tester
MP0179	MP0179fBZa	US	13/023,410	2/8/2011	8179144	5/15/2012	In Force	Cable Tester
MP0179	MP0179fBZZ	US	13/470,893	5/14/2012	8829917	9/9/2014	In Force	Cable Tester
MP0179	MP0179gaaa	US	12/148,161	4/17/2008	7683628	3/23/2010	In Force	Cable Tester
MP0179	MP0179HDaa	US	11/831,670	7/31/2007	7576548	8/18/2009	In Force	Cable Tester
MP0179	MP0179I	US	10/331,221	12/30/2002	6977507	12/20/2005	In Force	Cable Tester With Indicator
MP0179	MP0179I2	US	10/401,221	3/27/2003	7005861	2/28/2006	In Force	Cable Tester
MP0179	MP0179I2Ca	US	11/455,467	6/19/2006	7358745	4/15/2008	In Force	Cable Tester
MP0179	MP0179I2D1	US	10/979,302	11/2/2004	6982557	1/3/2006	In Force	Cable Tester
MP0179	MP0179I2D2	US	10/981,361	11/4/2004	7276913	10/2/2007	In Force	Cable Tester

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MP0179	MP0179I2D3	US	10/990,176	11/16/2004	6992491	1/31/2006	In Force	Cable Tester
MP0179	MP0179I3	US	10/400,864	3/27/2003	7002353	2/21/2006	In Force	Cable Tester
MP0179	MP0179I3Aa	US	11/254,384	10/20/2005	7075283	7/11/2006	In Force	Cable Tester
MP0179	MP0179I3Ca	US	11/484,465	7/11/2006	7375532	5/20/2008	In Force	Cable Tester
MP0179	MP0179I3D1	US	11/066,755	2/25/2005	7068043	6/27/2006	In Force	Cable Tester
MP0179	MP0179I3D2	US	11/066,739	2/25/2005	6995551	2/7/2006	In Force	Cable Tester With Insertion Loss Estimator
MP0179	MP0179I3D3	US	11/066,807	2/25/2005	7068044	6/27/2006	In Force	Cable Tester
MP0179	MP0179I4	US	10/400,367	3/27/2003	6980007	12/27/2005	In Force	CABLE TESTER
MP0179	MP0179I4D1	US	10/997,119	11/24/2004	7173431	2/6/2007	In Force	Cable Tester
MP0179	MP0179I4DA	US	11/129,203	5/13/2005	7019533	3/28/2006	In Force	Cable Tester
MP0179	MP0179I4DB	US	11/129,202	5/13/2005	7026825	4/11/2006	In Force	Cable Tester
MP0179	MP0179I4DC	US	11/455,359	6/19/2006	7250771	7/31/2007	In Force	Cable Tester
MP0179	MP0179ID1	US	10/967,602	10/18/2004	7002354	2/21/2006	In Force	Cable Tester
MP0179	MP0179ID2	US	11/093,101	3/29/2005	7012436	3/14/2006	In Force	Cable Tester
MP0180	MP0180	US	10/329,196	12/23/2002	7593431	9/22/2009	In Force	Method And System For Monitoring A MAC Extra IPG Count And Adju
MP0182	MP0182	US	10/348,091	1/21/2003	6972977	12/6/2005	In Force	Flash Memory Module
MP0182	MP0182Aaa	US	12/831,660	7/7/2010	8072788	12/6/2011	In Force	Flash Memory Module
MP0182	MP0182C1	US	11/070,623	3/2/2005	7460382	12/2/2008	In Force	Flash Memory Module
MP0182	MP0182C2	US	11/184,753	7/19/2005	7433218	10/7/2008	In Force	Flash Memory Module
MP0182	MP0182D1	US	11/070,622	3/2/2005	7483284	1/27/2009	In Force	Flash Memory Module
MP0182	MP0182D1C1	US	12/330,939	12/9/2008	7755919	7/13/2010	In Force	Flash Memory Module
MP0183	MP0183	US	10/364,602	2/11/2003	7242693	7/10/2007	In Force	Method and Apparatus for Fiber Autonegotiation
MP0183	MP0183C1	US	11/825,820	7/9/2007	7561592	7/14/2009	In Force	Method And Apparatus For Fiber Auto-Negotiation
MP0184	MP0184	US	10/358,713	2/4/2003	7529180	5/5/2009	In Force	Switch Failover For Aggregated Data Communication Links
MP0184	MP0184C1	US	12/434,132	5/1/2009	7881185	2/1/2011	In Force	Switch Failover For Aggregated Data Communication Links
MP0185	MP0185	US	10/460,825	6/12/2003	6977492	12/20/2005	In Force	Output Regulator
MP0185	MP0185CND1	CN	2004100889182	7/10/2003	ZL2004100889	4/9/2008	In Force	Output Regulator System
MP0185	MP0185CND2	CN	200410088920X	7/10/2003	ZL2004100889	6/4/2008	In Force	Voltage Slicer
MP0185	MP0185CND3	CN	2004100889229	7/10/2003	ZL2004100889	7/30/2008	In Force	Adaptive Multi-Mode System
MP0185	MP0185CND4	CN	2004100889214	7/10/2003	ZL2004100889	9/3/2008	In Force	Adaptive Duty Cycle Limiter
MP0185	MP0185CND5	CN	2004100889178	7/10/2003	ZL2004100889	6/4/2008	In Force	Duty Cycle Estimator
MP0185	MP0185CND6	CN	2004100889163	7/10/2003	ZL2004100889	12/3/2008	In Force	Adaptive Control Loop
MP0185	MP0185CND7	CN	2004100889248	7/10/2003	ZL2004100889	12/3/2008	In Force	Single Zero Compensator
MP0185	MP0185CND8	CN	2004100889197	7/10/2003	ZL2004100889	9/3/2008	In Force	Energy Saving Discontinuous Mode System

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MP0185	MP0185CND9	CN	2004100889233	7/10/2003	ZL200410088923	12/28/2011	In Force	Power Array System And Method
MP0185	MP0185D1	US	10/712,290	11/12/2003	7023192	4/4/2006	In Force	Output Regulator System And Method
MP0185	MP0185D2	US	10/811,430	3/26/2004	7009372	3/7/2006	In Force	Adaptive Multi-Mode System And Method
MP0185	MP0185D3	US	10/818,508	4/5/2004	6894465	5/17/2005	In Force	Power Array System And Method
MP0185	MP0185D3D1	US	11/080,829	3/15/2005	7368898	5/6/2008	In Force	Power Array System And Method
MP0185	MP0185D4	US	10/827,645	4/19/2004	7053594	5/30/2006	In Force	Adaptive Duty Cycle Limiter And Method
MP0185	MP0185D5	US	10/827,633	4/19/2004	6979988	12/27/2005	In Force	Duty Cycle Estimation
MP0185	MP0185D6	US	10/827,634	4/19/2004	7358711	4/15/2008	In Force	Adaptive Control Loop
MP0185	MP0185D6C1	US	12/082,825	4/15/2008	7863880	1/4/2011	In Force	Adaptive Control Loop
MP0185	MP0185D6D1	US	11/187,761	10/11/2005	7411377	8/12/2008	In Force	Adaptive Control Loop
MP0185	MP0185D6D1C1	US	12/148,078	4/16/2008	7906949	3/15/2011	In Force	Adaptive Control Loop
MP0185	MP0185D7	US	10/827,625	4/19/2004	7042202	5/9/2006	In Force	Energy Saving Discontinuous Mode System And Method
MP0185	MP0185D8	US	10/827,644	4/19/2004	6933711	8/23/2005	In Force	Voltage Slicer And Method
MP0185	MP0185EAA	US	11/906,087	9/28/2007	7622904	11/24/2009	In Force	Power Array System And Method
MP0185	MP0185EAb	US	12/080,645	4/4/2008	7609043	10/27/2009	In Force	Power Array System And Method
MP0185	MP0185EAC1	US	11/906,084	9/28/2007	7573249	8/11/2009	In Force	Power Array System And Method
MP0185	MP0185EPD6	EP	04023234.0	7/10/2003	EP1524568	10/15/2008	In Force	ADAPTIVE DUTY CYCLE LIMITER
MP0185	MP0185EPD7	EP	EP04023247.2	7/10/2003	EP1524569	9/10/2008	In Force	DUTY CYCLE ESTIMATOR
MP0185	MP0185EPD9	EP	04023192.0	7/10/2003	1524567	9/10/2008	In Force	DUTY CYCLE ESTIMATOR
MP0185	MP0185Q04N	DE	04023257.1	7/10/2003	1524573	4/20/2011	In Force	CONTROL SYSTEM FOR AN OUTPUT REGULATOR
MP0185	MP0185Q04R	FR	04023257.1	7/10/2003	1524573	4/20/2011	In Force	CONTROL SYSTEM FOR AN OUTPUT REGULATOR
MP0185	MP0185Q04T	GB	04023257.1	7/10/2003	1524573	4/20/2011	In Force	CONTROL SYSTEM FOR AN OUTPUT REGULATOR
MP0185	MP0185QDE5	DE	40232563	7/10/2003	1524572	12/28/2011	In Force	Power Array System And Method
MP0185	MP0185QFR5	FR	04023256.3	7/10/2003	1524572	12/28/2011	In Force	Power Array System And Method
MP0185	MP0185QGB5	GB	04023256.3	7/10/2003	1524572	12/28/2011	In Force	Power Array System And Method
MP0185	MP0185TW	TW	92118863	7/10/2003	I287912	10/1/2007	In Force	OUTPUT REGULATOR
MP0185	MP0185UD6G	DE	04023234.0	7/10/2003	60324148.4	10/15/2008	In Force	Adaptive Duty Cycle Limiter
MP0185	MP0185UD6N	GB	04023234.0	7/10/2003	1524568	10/15/2008	In Force	Adaptive Duty Cycle Limiter
MP0185	MP0185UD6R	FR	04023234.0	7/10/2003	1524568	10/15/2008	In Force	Adaptive Duty Cycle Limiter
MP0185	MP0185UD9G	DE	04023192.0	7/10/2003	1524567	9/10/2008	In Force	Duty Cycle Estimator
MP0185	MP0185UD9N	GB	04023192.0	7/10/2003	1524567	9/10/2008	In Force	Duty Cycle Estimator
MP0185	MP0185UD9R	FR	04023192.0	7/10/2003	1524567	9/10/2008	In Force	DUTY CYCLE ESTIMATOR
MP0185	MP0185WEP2	EP	EP04023259.7	7/10/2003	EP1524575	4/20/2011	In Force	Output Regulator
MP0185	MP0185WJ10	JP	2008-057075	3/6/2008	5281302	5/31/2013	In Force	Output Regulator System

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MP0185	MP0185WJP2	JP	2008-057067	3/6/2008	4875012	12/2/2011	In Force	Output Regulator System
MP0185	MP0185WJP3	JP	2008-057068	7/10/2003	4922968	2/10/2012	In Force	Output Regulator System
MP0185	MP0185WJP4	JP	2008-057069	3/6/2008	4922969	2/10/2012	In Force	Output Regulator System
MP0185	MP0185WJP5	JP	2008-057070	3/6/2008	4922970	2/10/2012	In Force	Output Regulator System
MP0185	MP0185WJP6	JP	2008-057071	3/6/2008	5025528	6/29/2012	In Force	Duty Cycle Limiter
MP0185	MP0185WJP7	JP	2008-057072	3/6/2008	5022948	6/22/2012	In Force	Output Regulator System
MP0185	MP0185WJP8	JP	2008-057073	3/6/2008	5025529	6/29/2012	In Force	Output Regulator System
MP0185	MP0185WJP9	JP	2008-057074	3/6/2008	5022949	6/22/2012	In Force	Output Regulator System
MP0185	MP0185WO	WO	PCT/US2003/02	7/10/2003			Pending	Output Regulator
MP0185	MP0185WOCN	CN	38054655	7/10/2003	ZL2004038054	6/4/2008	In Force	Output Regulator
MP0185	MP0185WODE03	DE	04023258.9	7/10/2003	603341020	9/8/2010	In Force	Output Regulator
MP0185	MP0185WODE7	DE	EP04023247.2	7/10/2003	1524569	9/10/2008	In Force	DUTY CYCLE ESTIMATOR
MP0185	MP0185WOEP01	EP	40232514	7/10/2003	1524571	4/20/2011	In Force	Control System For An Output Regulator
MP0185	MP0185WOEP01DE	DE	04023251.4	7/10/2003	1524571	4/20/2011	In Force	Control System For An Output Regulator
MP0185	MP0185WOEP01FR	FR	04023251.4	7/10/2003	1524571	4/20/2011	In Force	Control System For An Output Regulator
MP0185	MP0185WOEP01G	GB	04023251.4	7/10/2003	1524571	4/20/2011	In Force	Control System For An Output Regulator
MP0185	MP0185WOEP08	EP	04023249.8	7/10/2003	1524570	9/10/2008	In Force	Adaptive Control Loop
MP0185	MP0185WOEP10	EP	37634318	7/10/2003	1532501	5/4/2011	In Force	Digital Controller For An Output Regulator
MP0185	MP0185WOEP3	EP	EP04023258.9	7/10/2003	EP1524574	9/8/2010	In Force	Output Regulator
MP0185	MP0185WOEPDE08	DE	04023249.8	7/10/2003	1524570	9/10/2008	In Force	Adaptive Control Loop
MP0185	MP0185WOEPDE10	DE	03763431.8	7/10/2003	60337012.8	5/4/2011	In Force	Digital Controller For An Output Regulator
MP0185	MP0185WOEPFR08	FR	04023249.8	7/10/2003	1524570	9/10/2008	In Force	Adaptive Control Loop
MP0185	MP0185WOEPFR10	FR	03763431.8	7/10/2003	1532501	5/4/2011	In Force	Digital Controller For An Output Regulator
MP0185	MP0185WOEPGB0	GB	04023249.8	7/10/2003	1524570	9/10/2008	In Force	Adaptive Control Loop
MP0185	MP0185WOEPGB1	GB	03763431.8	7/10/2003	1532501	5/4/2011	In Force	Digital Controller For An Output Regulator
MP0185	MP0185WOFR03	FR	04023258.9	7/10/2003	1524574	9/8/2010	In Force	Output Regulator
MP0185	MP0185WOFR7	FR	04023247.2	7/10/2003	1524569	9/10/2008	In Force	Duty Cycle Estimator
MP0185	MP0185WOGB03	GB	04023258.9	7/10/2003	1524574	9/8/2010	In Force	Output Regulator
MP0185	MP0185WOGB7	GB	04023247.2	7/10/2003	1524569	9/10/2008	In Force	Duty Cycle Estimator
MP0185	MP0185WOJP	JP	2005258595	9/6/2005			Pending	Power Array System And Method
MP0185	MP0185WOJP02	JP	2005258592	9/6/2005			Pending	Output Regulator
MP0185	MP0185WOJP03	JP	2005258593	9/6/2005			Pending	Voltage Slicer
MP0185	MP0185WOJP04	JP	2005258594	9/6/2005			Pending	Adaptive Multi-Mode System
MP0185	MP0185WOJP07	JP	2005258597	9/6/2005			Pending	Duty Cycle Estimator

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MP0185	MP0185WOJP08	JP	2005258598	9/6/2005			Pending	Adaptive Control Loop
MP0185	MP0185WOJP09	JP	2005258599	9/6/2005			Pending	Single Zero Compensator
MP0185	MP0185WOJP10	JP	2005258600	9/6/2005			Pending	Energy Saving Discontinuous Mode System
MP0185	MP0185WOJP1D1	JP	2005-258596	9/6/2005			Pending	Adaptive Duty Cycle Limiter
MP0185	MP0185WU04	EP	EP04023257.1	7/10/2003	EP1524573	4/20/2011	In Force	Control System for an Output Regulator
MP0185	MP0185WU05	EP	04023256.3	7/10/2003	1524572	12/28/2011	In Force	POWER ARRAY SYSTEM AND METHOD
MP0185	MP0185WU2N	DE	04023259.7	7/10/2003	1524575	4/20/2011	In Force	OUTPUT REGULATOR
MP0185	MP0185WU2R	FR	04023259.7	7/10/2003	1524575	4/20/2011	In Force	OUTPUT REGULATOR
MP0185	MP0185WU2T	GB	04023259.7	7/10/2003	1524575	4/20/2011	In Force	OUTPUT REGULATOR
MP0188	MP0188	US	10/313,369	12/6/2002	6839015	1/4/2005	In Force	Low Power Analog to Digital Converter
MP0188	MP0188D1	US	10/946,321	9/21/2004	6967610	11/22/2005	In Force	LOW POWER BIT AND ONE-HALF ANALOG TO DIGITAL CONVERTOR
MP0188	MP0188D1D1	US	11/242,691	10/4/2005	7071863	7/4/2006	In Force	LOW POWER ANALOG TO DIGITAL CONVERTER HAVING REDUCED B
MP0192	MP0192	US	10/251,372	9/19/2002	6970794	11/29/2005	In Force	Semiconductor Having Reduced Configuration Pins And Method The
MP0192	MP0192AAAa	US	12/075,464	3/11/2008	7788053	8/31/2010	In Force	Configurable Voltage Regulator
MP0192	MP0192AAAZ	US	12/862,815	8/25/2010	7979224	7/12/2011	In Force	Configurable Voltage Regulator
MP0192	MP0192AAD1	US	11/602,922	11/21/2006	7343256	3/11/2008	In Force	Configurable Voltage Regulator
MP0192	MP0192AAD2	US	11/789,194	4/24/2007	7437252	10/14/2008	In Force	Configurable Voltage Regulator
MP0192	MP0192AAIa	US	11/546,831	10/12/2006	7480578	1/20/2009	In Force	Configurable Voltage Regulator
MP0192	MP0192AAIb	US	11/546,832	10/12/2006	7516027	4/7/2009	In Force	Configurable Voltage Regulator
MP0192	MP0192AAIWO	WO	PCT/US2007/01	5/18/2007			Pending	Configurable Voltage Regulator
MP0192	MP0192AAIWOCN	CN	2007800257150	5/18/2007	ZL2007800257	4/25/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0192	MP0192D1	US	11/220,255	9/6/2005	7062392	6/13/2006	In Force	Configurable Voltage Regulator
MP0192	MP0192D1D1	US	11/411,377	4/26/2006	7209845	4/24/2007	In Force	Configurable Voltage Regulator
MP0192	MP0192-D1I1	US	11/517,932	9/8/2006	7512504	3/31/2009	In Force	Testing System Using Configurable Integrated Circuit
MP0192	MP0192YFay	TW	96124295	7/4/2007	I416146	11/21/2013	In Force	TESTING SYSTEM COMPRISING CONFIGURABLE INTEGRATED CIRCUIT
MP0192	MP0192YFby	TW	96124297	7/4/2007	I418964	12/11/2013	In Force	PRODUCTION TESTING SYSTEM FOR TESTING AN INTEGRATED CIRCUI
MP0192	MP0192YFTW	TW	96124298	7/4/2007	I412769	10/21/2013	In Force	Testing System Using Configurable Integrated Circuit
MP0192	MP0192YIQN	DE	07795014.5	5/18/2007	2038720	1/11/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0192	MP0192YIQR	FR	07795014.5	5/18/2007	2038720	1/11/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0192	MP0192YIQT	GB	07795014.5	5/18/2007	2038720	1/11/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0192	MP0192YIWJ	JP	2009-518122	5/18/2007	5047282	7/27/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0192	MP0192YIWU	EP	07795014.5	5/18/2007	2038720	1/11/2012	In Force	CONFIGURABLE VOLTAGE REGULATOR
MP0194	MP0194	US	10/307,845	12/2/2002	6978337	12/20/2005	In Force	Serial ATA Controller Having Failover Function
MP0195	MP0195	US	10/339,897	1/10/2003	7246192	7/17/2007	In Force	Serial/Parallel ATA Controller And Converter

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MP0195	MP0195C1C1	US	14/215,919	3/17/2014	9514080	12/6/2016	In Force	Method And Apparatus For Providing An Interface Between A Host D
MP0197	MP0197	US	10/389,825	3/17/2003	6972916	12/6/2005	In Force	Preamplifier Arranged In Proximity Of Disk Drive Head
MP0197	MP0197C1	US	11/242,371	10/3/2005	7835097	11/16/2010	In Force	Preamplifier Arranged In Proximity Of Disk Drive Head
MP0199	MP0199	US	10/272,247	10/15/2002	7042301	5/9/2006	In Force	Crystal Oscillator Emulator
MP0199	MP0199CN	CN	31563511	9/4/2003	ZL2003031563	12/3/2008	In Force	Crystal Oscillator Emulator
MP0199	MP0199EP	EP	EP03017477.5	8/1/2003	EP1411630	7/6/2016	In Force	Crystal oscillator emulator
MP0199	MP0199EP01	EP	40207797	9/1/2004			Pending	Integrated Circuit Including Processor And Crystal Oscillator Emulat
MP0199	MP0199EPDE	DE	03017477.5	8/1/2003	603491022	7/6/2016	In Force	Crystal Oscillator Emulator
MP0199	MP0199EPFR	FR	03017477.5	8/1/2003	1411630	7/6/2016	In Force	Crystal Oscillator Emulator
MP0199	MP0199EPGB	GB	03017477.5	8/1/2003	1411630	7/6/2016	In Force	Crystal Oscillator Emulator
MP0199	MP0199fSG4	SG	2006059182	8/31/2006	131026	9/30/2009	In Force	Integrated Circuit Including Silicon Wafer With Annealed Glass Past
MP0199	MP0199HeTW	TW	96138627	10/16/2007	I433465	4/1/2014	In Force	Crystal Oscillator Emulator
MP0199	MP0199HGTW	TW	96138628	10/16/2007	I424680	1/21/2014	In Force	CRYSTAL OSCILLATOR EMULATOR
MP0199	MP0199HLTW	TW	96138629	10/16/2007	I429187	3/1/2014	In Force	CRYSTAL OSCILLATOR EMULATOR
MP0199	MP0199I1	US	10/892,709	7/16/2004	7148763	12/12/2006	In Force	Integrated Circuit Including Processor And Crystal Oscillator Emulator
MP0199	MP0199I1JP	JP	2004-264760	9/10/2004			Pending	INTEGRATED CIRCUIT INCLUDING PROCESSOR AND CRYSTAL OSCILLA
MP0199	MP0199I1TW	TW	93127282	9/9/2004	I340393	4/11/2011	In Force	INTEGRATED CIRCUIT INCLUDING PROCESSOR AND CRYSTAL OSCILLA
MP0199	MP0199I2C1	US	11/486,945	7/14/2006	7301408	11/27/2007	In Force	Integrated Circuit With Low Dielectric Loss Packaging Material
MP0199	MP0199I2C2	US	11/486,898	7/14/2006	7812683	10/12/2010	In Force	Integrated Circuit Package With Glass Layer And Oscillator
MP0199	MP0199I2C5	US	11/487,077	7/14/2006	7253495	8/7/2007	In Force	Integrated Circuit Package With Air Gap
MP0199	MP0199I2CN	CN	2006101269472	9/6/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2CN03	CN	200610126949.:	9/6/2006	ZL2006101269	6/16/2010	In Force	INTEGRATED CIRCUIT PACKAGE WITH GLASS LAYER AND OSCILLATOR
MP0199	MP0199I2CN04	CN	2006101269487	9/6/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2EP	EP	60181047	8/30/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2Ga	US	14/089,261	11/25/2013	9143083	9/22/2015	In Force	Crystal Oscillator Emulator With Externally Selectable Operating Conf
MP0199	MP0199I2Gb	US	14/852,747	9/14/2015	9350360	5/24/2016	In Force	Systems And Methods For Configuring A Semiconductor Device
MP0199	MP0199I2HK	HK	71089648	9/6/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2HK03	HK	71089666	9/6/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2HK04	HK	71089657	9/6/2006			Pending	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2SG	SG	2006059158	8/31/2006	131024	3/31/2009	In Force	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2SG03	SG	2006059166	8/31/2006	131025	6/30/2009	In Force	Integrated Circuit Including Silicon Wafer With Annealed Glass Paste
MP0199	MP0199I2TW	TW	95132917	9/6/2006	I400777	7/1/2013	In Force	INTEGRATED CIRCUIT INCLUDING SILICON WAFER WITH ANNEALED
MP0199	MP0199I2TW03	TW	95132921	9/6/2006			Pending	INTEGRATED CIRCUIT INCLUDING SILICON WAFER WITH ANNEALED
MP0199	MP0199I2TW04	TW	95132919	9/6/2006			Pending	INTEGRATED CIRCUIT INCLUDING SILICON WAFER WITH ANNEALED

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MP0199	MP0199I4	US	11/649,433	1/4/2007	7768360	8/3/2010	In Force	Crystal Oscillator Emulator
MP0199	MP0199I4C1	US	11/732,418	4/3/2007	7786817	8/31/2010	In Force	Crystal Oscillator Emulator
MP0199	MP0199I4C1WO	WO	PCT/US2007/02	10/17/2007			Pending	Crystal Oscillator Emulator
MP0199	MP0199I4C2	US	11/732,409	4/3/2007	7768361	8/3/2010	In Force	Crystal Oscillator Emulator
MP0199	MP0199I4C3	US	11/732,435	4/3/2007	7760039	7/20/2010	In Force	Crystal Oscillator Emulator
MP0199	MP0199I4C4	US	11/732,465	4/3/2007	7760036	7/20/2010	In Force	Crystal Oscillator Emulator
MP0199	MP0199I4C5	US	11/732,304	4/3/2007	7791424	9/7/2010	In Force	Crystal Oscillator Emulator
MP0199	MP0199I4C5WO	WO	PCT/US2007/02	10/16/2007			Pending	Crystal Oscillator Emulator
MP0199	MP0199I4La	US	12/876,739	9/7/2010	8063711	11/22/2011	In Force	Crystal Oscillator Emulator
MP0199	MP0199I4WO	WO	PCT/US2007/02	10/17/2007			Pending	Crystal Oscillator Emulator
MP0199	MP0199ICN	CN	2004100784015	9/10/2004			Pending	Integrated Circuit Including Processor and Crystal Oscillator Emulato
MP0199	MP0199JP	JP	2003-347358	10/6/2003			Pending	CRYSTAL OSCILIATOR EMULATOR
MP0199	MP0199TW	TW	92119741	7/18/2003	I279072	4/11/2007	In Force	CRYSTAL OSCILLATOR EMULATOR
MP0200	MP0200	US	10/392,242	3/18/2003	7417817	8/26/2008	In Force	Write Driver Circuit For Magnetic Data Storage Systems
MP0200	MP0200C1	US	12/229,393	8/22/2008	7880989	2/1/2011	In Force	Write Driver Circuit For Magnetic Data Storage Systems
MP0200	MP0200D1	US	11/091,225	3/28/2005	7280300	10/9/2007	In Force	Write Driver Circuit For Magnetic Data Storage Systems
MP0200	MP0200D2	US	11/091,608	3/28/2005	7277245	10/2/2007	In Force	Write Driver Circuit For Magnetic Data Storage Systems
MP0202	MP0202	US	10/259,088	9/26/2002	7567634	7/28/2009	In Force	Reduced Complexity Viterbi Decoding Method And Apparatus
MP0202	MP0202C1	US	12/150,836	5/1/2008	7630462	12/8/2009	In Force	Reduced Complexity Viterbi Decoding Method And Apparatus
MP0202	MP0202C1C1	US	12/632,877	12/8/2009	7949079	5/24/2011	In Force	Reduced Complexity Viterbi Decoding Method And Apparatus
MP0203	MP0203	US	10/259,024	9/26/2002	7065146	6/20/2006	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203aaa	US	12/686,111	1/12/2010	8126074	2/28/2012	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203aaaa	US	13/400,291	2/20/2012	8494068	7/23/2013	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203aaZa	US	13/947,562	7/22/2013	8731085	5/20/2014	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203C1	US	11/449,502	6/8/2006	7388924	6/17/2008	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203C1C1	US	12/214,266	6/16/2008	7646821	1/12/2010	In Force	Method And Apparatus For Equalization And Decoding In A Wireless
MP0203	MP0203ZZaa	US	14/278,514	5/15/2014	8971459	3/3/2015	In Force	Receiver And Method For Incorporating Channel State Information In
MP0203	MP0203ZZZa	US	14/633,659	2/27/2015	9166854	10/20/2015	In Force	Method And Apparatus For Decoding A Symbol Incorporating Channe
MP0204	MP0204	US	10/316,806	12/10/2002	7433298	10/7/2008	In Force	Compensation For Residual Frequency Offset, Phase Noise And I/Q I
MP0204	MP0204C1	US	12/287,199	10/7/2008	7643405	1/5/2010	In Force	Compensation For Residual Frequency Offset, Phase Noise And I/Q I
MP0204	MP0204C1aa	US	13/010,295	1/20/2011	8488442	7/16/2013	In Force	Compensation For Residual Frequency Offset, Phase Noise And I/Q I
MP0204	MP0204C1C1	US	12/645,678	12/23/2009	7881237	2/1/2011	In Force	Compensation For Residual Frequency Offset, Phase Noise And I/Q I
MP0204	MP0204C1ZA	US	13/941,787	7/15/2013	8792325	7/29/2014	In Force	Apparatuses And Method For Mapping Bits Of Orthogonal Frequency
MP0206	MP0206	US	10/387,299	3/12/2003	7415652	8/19/2008	In Force	Out Of Order Checksum Calculation For Fragmented Packets

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MP0206	MP0206C1	US	12/221,168	7/31/2008	8161364	4/17/2012	In Force	Out Of Order Checksum Calculation For Fragmented Packets
MP0208	MP0208	US	10/393,640	3/21/2003	7107514	9/12/2006	In Force	METHOD AND APPARATUS FOR DETECTING VITERBI DECODER ERRC
MP0208	MP0208C1	US	11/516,272	9/6/2006	7383488	6/3/2008	In Force	Method And Apparatus For Detecting Viterbi Decoder Errors Due To
MP0208	MP0208C1C1	US	12/156,040	5/29/2008	8046670	10/25/2011	In Force	Method And Apparatus For Detecting Viterbi Decoder Errors Due To
MP0208	MP0208C2	US	11/516,364	9/6/2006	7383489	6/3/2008	In Force	Method And Apparatus For Detecting Viterbi Decoder Errors Due To
MP0208	MP0208C2C1	US	12/156,036	5/29/2008	8082485	12/20/2011	In Force	Method And Apparatus For Detecting Viterbi Decoder Errors Due To
MP0209	MP0209	US	10/375,986	2/27/2003	7216276	5/8/2007	In Force	Apparatus And Method For Testing And Debugging An Integrated C
MP0209	MP0209C1	US	11/800,614	5/7/2007	7562276	7/14/2009	In Force	Apparatus And Method For Testing And Debugging An Integrated C
MP0209	MP0209C1C1	US	12/500,245	7/9/2009	8074135	12/6/2011	In Force	Apparatus And Method For Testing And Debugging An Integrated C
MP0209	MP0209fAa	US	12/778,225	5/12/2010	7930604	4/19/2011	In Force	Apparatus And Method For Testing And Debugging An Integrated C
MP0209	MP0209Faaa	US	13/089,660	4/19/2011	8161336	4/17/2012	In Force	Apparatus And Method For Testing And Debugging An Integrated C
MP0209	MP0209Faaaa	US	13/446,627	4/13/2012	8356223	1/15/2013	In Force	Apparatus And Method For Testing And Debugging An Integrated C
MP0209	MP0209FVZa	US	14/262,204	4/25/2014	8977921	3/10/2015	In Force	System And Method For Providing A Test Result From An Integratec
MP0209	MP0209FVZZ	US	14/643,083	3/10/2015	9285421	3/15/2016	In Force	Serializer/Deserializer And Method For Transferring Data Between An
MP0209	MP0209FVZZa	US	15/051,714	2/24/2016	9739834	8/22/2017	In Force	System And Method For Transferring Serialized Test Result Data Fro
MP0209	MP0209FZZa	US	13/741,496	1/15/2013	8572448	10/29/2013	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209FZZZ	US	14/064,945	10/28/2013	8713391	4/29/2014	In Force	SYSTEM AND METHOD FOR TESTING AN INTEGRATED CIRCUIT EMBE
MP0209	MP0209I1	US	11/065,584	2/24/2005	7444571	10/28/2008	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209I1C1	US	11/130,995	5/17/2005	7590911	9/15/2009	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209I1C2	US	11/131,073	5/17/2005	7496812	2/24/2009	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209I2	US	11/178,807	7/11/2005	7496818	2/24/2009	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0209	MP0209I2D1	US	12/154,896	5/28/2008	7721167	5/18/2010	In Force	Apparatus And Method For Testing And Debugging An Integrated Circ
MP0210	MP0210	US	10/443,972	5/22/2003	7433401	10/7/2008	In Force	Mixed-Mode Signal Processor Architecture And Device
MP0210	MP0210C1	US	12/287,077	10/6/2008	7564900	7/21/2009	In Force	Mixed-Mode Signal Processor Architecture And Device
MP0213	MP0213	US	10/322,130	12/17/2002	7016651	3/21/2006	In Force	Apparatus And Method For Measuring Signal Quality Of A Wireless C
MP0213	MP0213C1	US	11/346,503	2/2/2006	7599663	10/6/2009	In Force	Apparatus And Method For Measuring Signal Quality Of A Wireless C
MP0213	MP0213C1C1	US	12/574,262	10/6/2009	7885607	2/8/2011	In Force	Apparatus And Method For Measuring Signal Quality Of A Wireless C
MP0214	MP0214	US	10/449,218	5/30/2003	7113356	9/26/2006	In Force	Method For Checking The Quality Of Servo Gray Codes
MP0214	MP0214C1	US	11/492,428	7/25/2006	7315427	1/1/2008	In Force	Method For Checking The Quality Of Servo Gray Codes
MP0216	MP0216	US	10/426,745	4/30/2003	7454643	11/18/2008	In Force	Pre-Emptive Power Supply Control System And Method
MP0216	MP0216C1	US	12/150,570	4/29/2008	7711972	5/4/2010	In Force	Pre-Emptive Power Supply Control System And Method
MP0216	MP0216D1	US	11/403,517	4/13/2006	7472294	12/30/2008	In Force	Pre-Emptive Power Supply Control System And Method
MP0216	MP0216D2	US	11/403,717	4/13/2006	7472295	12/30/2008	In Force	Pre-Emptive Power Supply Control System And Method
MP0216	MP0216EP	EP	EP04007362.9	3/26/2004	EP1473607	3/9/2011	In Force	Predictive Power Supply Control

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MP0216	MP0216EPDE	DE	40073629	3/26/2004	602004031686	3/9/2011	In Force	Predictive Power Supply Control
MP0216	MP0216EPFR	FR	04007362.9	3/26/2004	1473607	3/9/2011	In Force	Predictive Power Supply Control
MP0216	MP0216EPGB	GB	04007362.9	3/26/2004	1473607	3/9/2011	In Force	Predictive Power Supply Control
MP0216	MP0216JP	JP	2004-099561	3/30/2004	4753543	6/3/2011	In Force	PRE-EMPTIVE POWER SUPPLY CONTROL SYSTEM AND METHOD
MP0216	MP0216TW	TW	93108086	3/25/2004	I336435	1/21/2011	In Force	PRE-EMPTIVE POWER SUPPLY CONTROL SYSTEM AND METHOD
MP0219	MP0219	US	10/413,927	4/15/2003	6844711	1/18/2005	In Force	Low Power And High Accuracy Band Gap Voltage Circuit
MP0219	MP0219baaa	US	13/245,489	9/26/2011	8531171	9/10/2013	In Force	Low Power and High Accuracy Band Gap Voltage Circuit
MP0219	MP0219C1	US	10/926,185	8/25/2004	7023194	4/4/2006	In Force	Low Power And High Accuracy Band Gap Voltage Reference Circuit
MP0219	MP0219C2	US	11/334,030	1/18/2006	7579822	8/25/2009	In Force	Low Power And High Accuracy Band Gap Voltage Reference Circuit
MP0219	MP0219C2C1	US	12/546,298	8/24/2009	7795857	9/14/2010	In Force	Low Power And High Accuracy Band Gap Voltage Reference Circuit
MP0222	MP0222	US	10/280,420	10/25/2002	6809594	10/26/2004	In Force	Ultra Broadband Low Noise Amplifier With Matched Reactive Input
MP0223	MP0223	US	10/271,664	10/15/2002	6856163	2/15/2005	In Force	Power Supply Decoupling For Parallel Terminated Transmission Line
MP0223	MP0223CN	CN	31569900	9/17/2003	ZL2003031569	5/12/2010	In Force	Power Supply Decoupling For Parallel Terminated Transmission Line
MP0223	MP0223DE	DE	03017479.1	8/1/2003	603011101	7/27/2005	In Force	Power Supply Decoupling For Parallel Terminated Transmission Line
MP0223	MP0223EP	EP	EP03017479.1	8/1/2003	EP1403752	7/27/2005	In Force	POWER SUPPLY DECOUPLING FOR PARALLEL TERMINATED TRANSMI
MP0223	MP0223FR	FR	03017479.1	8/1/2003	1403752	7/27/2005	In Force	Power Supply Decoupling For Parallel Terminated Transmission Line
MP0223	MP0223GB	GB	03017479.1	8/1/2003	1403752	7/27/2005	In Force	Power Supply Decoupling For Parallel Terminated Transmission Line
MP0223	MP0223JP	JP	2003-333968	9/25/2003	4397665	10/30/2009	In Force	POWER SUPPLY DECOUPLING FOR PARALLEL TERMINATED TRANSMI
MP0223	MP0223TW	TW	92119720	7/18/2003	I281783	5/21/2007	In Force	POWER SUPPLY DECOUPLING FOR PARALLEL TERMINATED TRANSMI
MP0224	MP0224	US	10/358,709	2/5/2003	7185225	2/27/2007	In Force	Self-Reparable Semiconductor And Method Thereof
MP0224	MP0224CN	CN	31348041	9/24/2003	ZL2003031348	4/11/2007	In Force	Self-Reparable Semiconductor And Method Thereof
MP0224	MP0224D1	US	11/594,390	11/8/2006	7373547	5/13/2008	In Force	Self-Reparable Semiconductor And Method Thereof
MP0224	MP0224D2	US	11/594,537	11/8/2006	7657784	2/2/2010	In Force	Self-Reparable Semiconductor And Method Thereof
MP0224	MP0224D3	US	11/594,312	11/8/2006	7313723	12/25/2007	In Force	Self-Reparable Semiconductor And Method Thereof
MP0224	MP0224EP	EP	EP03026882.5	11/24/2003	EP1434134	9/23/2015	In Force	Self-Reparable Semiconductor and Method Thereof
MP0224	MP0224EPDE	DE	03026882.5	11/24/2003	60348077.2	9/23/2015	In Force	Self-Reparable Semiconductor And Method Thereof
MP0224	MP0224EPFR	FR	03026882.5	11/24/2003	1434134	9/23/2015	In Force	Self-Reparable Semiconductor And Method Thereof
MP0224	MP0224EPGB	GB	03026882.5	11/24/2003	1434134	9/23/2015	In Force	Self-Reparable Semiconductor And Method Thereof
MP0224	MP0224I	US	10/892,707	7/16/2004	7340644	3/4/2008	In Force	Self-Reparable Semiconductor And Method Thereof
MP0224	MP0224I2C1	US	13/735,588	1/7/2013	8812905	8/19/2014	In Force	Self-Repairable Semiconductor And Method Thereof
MP0224	MP0224I2JP	JP	2012-019133	1/31/2012	5571709	7/4/2014	In Force	SELF-REPARABLE SEMICONDUCTOR AND METHOD THEREOF
MP0224	MP0224IC1	US	12/074,557	3/4/2008	7730349	6/1/2010	In Force	Self-Reparable Semiconductor And Method Thereof
MP0224	MP0224ICN	CN	2004100737372	9/9/2004	ZL2004100737	12/30/2009	In Force	Self-Reparable Semiconductor And System Thereof
MP0224	MP0224IDE	DE	04020775.5	9/1/2004	602004027592	6/9/2010	In Force	Self-Reparable Semiconductor

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MP0224	MP0224IEP	EP	04020775.5	9/1/2004	1544740	6/9/2010	In Force	Self-Reparable Semiconductor
MP0224	MP0224IFR	FR	04020775.5	9/1/2004	1544740	6/9/2010	In Force	Self-Reparable Semiconductor
MP0224	MP0224IGB	GB	04020775.5	9/1/2004	1544740	6/9/2010	In Force	Self-Reparable Semiconductor
MP0224	MP0224IJP	JP	2004-264756	9/10/2004	4963782	4/6/2012	In Force	SELF-REPARABLE SEMICONDUCTOR AND METHOD THEREOF
MP0224	MP0224ITW	TW	93127283	9/9/2004	I359546	3/1/2012	In Force	SELF-REPARABLE SEMICONDUCTOR AND METHOD THEREOF
MP0224	MP0224TW	TW	92120125	7/23/2003	I281547	5/21/2007	In Force	Self-Reparable Semiconductor And Method Thereof
MP0226	MP0226	US	10/267,177	10/9/2002	7263153	8/28/2007	In Force	Clock Offset Compensator
MP0226	MP0226C1	US	11/895,974	8/28/2007	8681914	3/25/2014	In Force	Clock Offset Compensator
MP0227	MP0227	US	10/619,278	7/14/2003	7295644	11/13/2007	In Force	APPARATUS FOR CLOCK DATA RECOVERY
MP0228	MP0228	US	10/459,798	6/12/2003	8447900	5/21/2013	In Force	Adaptive Multiple FIFO Scheme
MP0228	MP0228D1	US	13/896,869	5/17/2013	8788729	7/22/2014	In Force	Method And Apparatus For Resolving Write And Read Conflicts Bet
MP0229	MP0229	US	10/657,672	9/4/2003	7265522	9/4/2007	In Force	Dynamic Multiphase Operation
MP0229	MP0229C1	US	11/895,673	8/27/2007	7479766	1/20/2009	In Force	Dynamic Multiphase Operation
MP0229	MP0229C1C1	US	12/355,877	1/19/2009	7696732	4/13/2010	In Force	Dynamic Multiphase Operation
MP0229	MP0229CN	CN	200410048724X	6/10/2004			Pending	Dynamic Multiphase Operation
MP0229	MP0229EP	EP	40117798	5/18/2004			Pending	Dynamic Multiphase Operation
MP0229	MP0229JP	JP	2004-196815	7/2/2004	4542839	7/2/2010	In Force	DYNAMIC MULTIPHASE OPERATION
MP0229	MP0229TW	TW	93117302	6/16/2004	I340306	4/11/2011	In Force	DYNAMIC MULTIPHASE OPERATION
MP0230	MP0230	US	10/277,449	10/22/2002	7319705	1/15/2008	In Force	Programmable Pre-Emphasis Circuit For Serial ATA
MP0230	MP0230C1	US	11/904,886	9/28/2007	7733920	6/8/2010	In Force	Programmable Pre-Emphasis Circuit For Serial ATA
MP0230	MP0230C1AA	US	13/675,577	11/13/2012	8605759	12/10/2013	In Force	Device With Pre-Emphasis Based Transmission
MP0230	MP0230C1C1	US	12/792,247	6/2/2010	8311064	11/13/2012	In Force	Programmable Pre-Emphasis Circuit For Serial ATA
MP0230	MP0230C1Za	US	14/101,502	12/10/2013	8937975	1/20/2015	In Force	Apparatus And Method For Providing Pre-Emphasis To A Signal
MP0231	MP0231	US	10/412,076	4/9/2003	6818955	11/16/2004	In Force	ELECTROSTATIC DISCHARGE PROTECTION
MP0235	MP0235	US	10/423,552	4/25/2003	7269778	9/11/2007	In Force	Data Coding For Enforcing Constraints On Ones And Zeros In A Comm
MP0235	MP0235C1	US	11/799,362	5/1/2007	7568144	7/28/2009	In Force	Data Coding For Enforcing Constraints On Ones And Zeros In A Comm
MP0235	MP0235C1C1	US	12/509,754	7/27/2009	7865802	1/4/2011	In Force	Data Coding For Enforcing Constraints On Ones And Zeros In A Comm
MP0236	MP0236	US	10/434,524	5/8/2003	7288845	10/30/2007	In Force	FABRICATION OF WIRE BOND PADS OVER UNDERLYING ACTIVE DEVI
MP0237	MP0237	US	10/621,058	7/15/2003	7161342	1/9/2007	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237C1	US	11/131,106	5/16/2005	7148673	12/12/2006	In Force	METHOD AND APPARATUS INCLUDING LOW LOSS DC/DC CONVERTE
MP0237	MP0237C1C1	US	11/505,104	8/15/2006	7271573	9/18/2007	In Force	LOW LOSS DC/DC CONVERTER INCLUDING A MULTI-LEVEL CONTROL
MP0237	MP0237CN	CN	200310101782.3	10/23/2003	ZL 200310101	11/28/2007	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237CND1	CN	200710162718.(	10/23/2003	ZL 200710162	6/20/2012	In Force	LOW LOSS DC/DC CONVERTER AND METHOD OF DC/DC POWER CON
MP0237	MP0237CND2	CN	2007-10162719	10/23/2003	ZL 200710162	6/2/2010	In Force	LOW LOSS DC/DC CONVERTER AND METHOD OF DC/DC POWER CON

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MP0237	MP0237EP	EP	03024437.0	10/23/2003	EP1414143	4/26/2017	In Force	Low Loss DC/DC Converter
MP0237	MP0237EPDE	DE	03024437.0	10/23/2003	60350154.0	4/26/2017	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237EPFR	FR	03024437.0	10/23/2003	1414143	4/26/2017	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237EPGB	GB	03024437.0	10/23/2003	1414143	4/26/2017	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237HKD1	HK	8110495.1	10/23/2003			Pending	LOW LOSS DC/DC CONVERTER
MP0237	MP0237HKD2	HK	8110496	10/23/2003			Pending	LOW LOSS DC/DC CONVERTER
MP0237	MP0237JP	JP	2003-362674	10/23/2003	3887368	12/1/2006	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237JPD1	JP	2006-221415	8/15/2006	4332166	6/26/2009	In Force	LOW LOSS DC/DC CONVERTER
MP0237	MP0237TW	TW	92129617	10/24/2003	I 233250	5/21/2005	In Force	LOW LOSS DC/DC CONVERTER
MP0238	MP0238	US	10/336,364	1/2/2003	7420800	9/2/2008	In Force	Faceplate Including Wireless LAN Communications
MP0239	MP0239	US	10/648,632	8/25/2003	7173489	2/6/2007	In Force	PROGRAMMABLE GAIN VOLTAGE BUFFER
MP0242	MP0242	US	10/392,436	3/17/2003	6903448	6/7/2005	In Force	HIGH PERFORMANCE LEADFRAME IN ELECTRONIC PACKAGE
MP0242	MP0242D1	US	11/016,443	12/16/2004	7528013	5/5/2009	In Force	METHOD FOR FABRICATING HIGH PERFORMANCE LEADFRAME IN E
MP0243	MP0243	US	10/390,431	3/14/2003	7062582	6/13/2006	In Force	Method And Apparatus For Bus Arbitration Dynamic Priority Based O
MP0243	MP0243C1	US	11/390,627	3/28/2006	7698486	4/13/2010	In Force	Method And Apparatus For Bus Arbitration Dynamic Priority Based O
MP0243	MP0243C1aa	US	13/274,674	10/17/2011	8307139	11/6/2012	In Force	Method And Apparatus For Dynamically Granting Access Of A Shared
MP0243	MP0243C1C1	US	12/758,849	4/13/2010	8041870	10/18/2011	In Force	Method And Apparatus For Dynamically Granting Access Of A Shared
MP0246	MP0246	US	10/621,755	7/16/2003	7336750	2/26/2008	In Force	OPTIMAL ONE-SHOT PHASE AND FREQUENCY ESTIMATION FOR TIMI
MP0248	MP0248	US	10/458,049	6/9/2003	7167942	1/23/2007	In Force	Dynamic Random Access Memory Controller
MP0248	MP0248C1	US	11/653,731	1/15/2007	9430379	8/30/2016	In Force	Dynamic Random Access Memory Controller
MP0250	MP0250	US	10/639,796	8/12/2003	7218255	5/15/2007	In Force	Methods And Apparatus For Improving Minimum Hamming Weights
MP0250	MP0250C1	US	11/786,246	4/11/2007	7312727	12/25/2007	In Force	Methods And Apparatus For Improving Minimum Hamming Weights
MP0252	MP0252	US	10/319,391	12/12/2002	7414306	8/19/2008	In Force	PREAMPLIFIER INTEGRATED CIRCUIT ON FLEX CIRCUIT FOR MAGNETI
MP0252	MP0252C1	US	12/221,707	8/6/2008	8803309	8/12/2014	In Force	Preamplifier Integrated Circuit On Flex Circuit For Magnetic Media St
MP0253	MP0253C1	US	10/894,864	7/20/2004	7158058	1/2/2007	In Force	Method And Apparatus For Generating A Seed Set In A Data Depende
MP0255	MP0255	US	10/397,402	3/26/2003	6911739	6/28/2005	In Force	Methods And Apparatus For Improving High Frequency Input/Output
MP0255	MP0255D1	US	11/033,202	1/11/2005	6987326	1/17/2006	In Force	Methods And Apparatus For Improving High Frequency Input/Output
MP0255	MP0255D2	US	11/034,093	1/12/2005	6977444	12/20/2005	In Force	Methods And Apparatus For Improving High Frequency Input/Output
MP0255	MP0255D3	US	11/034,979	1/13/2005	7009308	3/7/2006	In Force	Methods And Apparatus For Improving High Frequency Input/Output
MP0256	MP0256	US	10/656,001	9/5/2003	7505788	3/17/2009	In Force	SPATIAL MULTIPLEXING WITH ANTENNA AND CONSTELLATION SELEC
MP0257	MP0257	US	10/388,940	3/14/2003	7209522	4/24/2007	In Force	Blast MIMO Signal Processing Method And Apparatus
MP0261	MP0261	US	10/619,858	7/15/2003	6977490	12/20/2005	In Force	Compensation For Low Drop Out Voltage Regulator
MP0266	MP0266	US	10/402,438	3/27/2003	7451413	11/11/2008	In Force	METHODS OF MINIMIZING LEAKAGE CURRENT BY ANALYZING POST L
MP0270	MP0270	US	10/446,341	5/27/2003	7080172	7/18/2006	In Force	MANAGEMENT OF MEMORY, HARDWARE AND ASSOCIATED DEVICE

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MP0273	MP0273	US	10/701,271	11/4/2003	7234097	6/19/2007	In Force	Methods Of Supporting Host CRC In Data Storage Systems Without R
MP0273	MP0273D1	US	11/810,221	6/5/2007	8020069	9/13/2011	In Force	Methods Of Supporting Host CRC In Data Storage Systems Without "
MP0274	MP0274	US	10/649,187	8/26/2003	7231505	6/12/2007	In Force	ALIGNING IP PAYLOADS ON MEMORY BOUNDARIES FOR IMPROVED
MP0274	MP0274C1	US	11/761,876	6/12/2007	7386699	6/10/2008	In Force	ALIGNING IP PAYLOADS ON MEMORY BOUNDARIES FOR IMPROVED
MP0274	MP0274C1C1	US	12/131,837	6/2/2008	7870361	1/11/2011	In Force	ALIGNING IP PAYLOADS ON MEMORY BOUNDARIES FOR IMPROVED
MP0275	MP0275	US	10/600,419	6/20/2003	8161361	4/17/2012	In Force	AVERAGING SIGNALS TO IMPROVE SIGNAL INTERPRETATION
MP0275	MP0275C1	US	13/445,712	4/12/2012	8359527	1/22/2013	In Force	AVERAGING SIGNALS TO IMPROVE SIGNAL INTERPRETATION
MP0275	MP0275C1C1	US	13/745,655	1/18/2013	8589774	11/19/2013	In Force	AVERAGING SIGNALS TO IMPROVE SIGNAL INTERPRETATION
MP0277	MP0277	US	10/672,086	9/26/2003	7237178	6/26/2007	In Force	MARKING UNRELIABLE SYMBOLS IN A HARD DISK DRIVE READ BACK
MP0277	MP0277C1	US	11/821,937	6/26/2007	7490285	2/10/2009	In Force	Marking Unreliable Symbols In A Hard Disk Drive Read Back Signal
MP0277	MP0277C1C1	US	12/364,577	2/3/2009	7827474	11/2/2010	In Force	Marking Unreliable Symbols In A Hard Disk Drive Read Back Signal
MP0286	MP0286	US	10/620,024	7/14/2003	8149810	4/3/2012	In Force	DATA RATE ADAPTATION IN MULTIPLE-IN-MULTIPLE-OUT SYSTEMS
MP0286	MP0286C1	US	13/425,350	3/20/2012	8532081	9/10/2013	In Force	Data Rate Adaptation In Multiple-In-Multiple-Out Systems
MP0286	MP0286C1C1	US	14/022,046	9/9/2013	8861499	10/14/2014	In Force	Data Rate Adaptation In Multiple-In-Multiple-Out Systems
MP0290	MP0290	US	10/646,601	8/21/2003	7343425	3/11/2008	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0290	MP0290Aaaa	US	14/107,369	12/16/2013	9479554	10/25/2016	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0290	MP0290D1	US	11/891,930	8/14/2007	7418514	8/26/2008	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0290	MP0290D1aa	US	13/245,513	9/26/2011	8612629	12/17/2013	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0290	MP0290D1C1	US	12/229,376	8/22/2008	8028095	9/27/2011	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0290	MP0290D2	US	11/891,929	8/14/2007	7840709	11/23/2010	In Force	Multi-Speed Serial Interface For Media Access Control And Physical L
MP0291	MP0291	US	10/601,235	6/20/2003	7251699	7/31/2007	In Force	Low Overhead Coding With Arbitrary Control Symbol Placement
MP0291	MP0291C1	US	11/888,188	7/31/2007	7613838	11/3/2009	In Force	Low Overhead Coding With Arbitrary Control Code Placement
MP0293	MP0293	US	10/715,551	11/17/2003	7469049	12/23/2008	In Force	Data Dependent Scrambler With Reduced Overhead
MP0293	MP0293C1	US	12/342,263	12/23/2008	7697685	4/13/2010	In Force	Data Dependent Scrambler With Reduced Overhead
MP0294	MP0294	US	10/455,668	6/5/2003	6900686	5/31/2005	In Force	Analog Switching Circuit
MP0294	MP0294C1	US	11/073,806	3/7/2005	7012458	3/14/2006	In Force	Analog Switching Circuit
MP0294	MP0294C2I1	US	11/857,238	9/18/2007	7889752	2/15/2011	In Force	Dual Ported Network Physical Layer
MP0296	MP0296	US	10/679,030	10/3/2003	7308530	12/11/2007	In Force	Architecture For A Data Storage Device
MP0297	MP0297	US	10/658,666	9/9/2003	7706295	4/27/2010	In Force	Methods And Apparatus For Breaking And Resynchronizing A Data Lin
MP0297	MP0297C1	US	12/764,171	4/21/2010	8027270	9/27/2011	In Force	Methods And Apparatus For Breaking And Resynchronizing A Data Lin
MP0299	MP0299	US	10/634,218	8/4/2003	7486718	2/3/2009	In Force	ARCHITECTURES, CIRCUITS, SYSTEMS AND METHODS FOR REDUCING
MP0299	MP0299D1	US	12/330,218	12/8/2008	7835425	11/16/2010	In Force	ARCHITECTURES, CIRCUITS, SYSTEMS AND METHODS FOR REDUCING
MP0300	MP0300	US	10/702,744	11/5/2003	7571287	8/4/2009	In Force	Multiport Memory Architecture, Devices and Systems Including the S
MP0300	MP0300CN	CN	200410008497.!	3/12/2004	ZL2004100456	1/28/2009	In Force	System structure with multiport memory, device, system and use me

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MP0300	MP0300D1	US	12/494,076	6/29/2009	8335878	12/18/2012	In Force	Multiport memory architecture, devices and systems including the sa
MP0300	MP0300D1aa	US	14/230,555	3/31/2014	9105319	8/11/2015	In Force	Multiport Memory Architecture
MP0300	MP0300D1C1	US	13/716,481	12/17/2012	8688877	4/1/2014	In Force	Multiport memory architecture
MP0300	MP0300DE	DE	04006012.1	3/12/2004	1457993	9/8/2010	In Force	MULTIPORT MEMORY ARCHITECTURE, DEVICES AND SYSTEMS INCL
MP0300	MP0300EP	EP	EP04006012.1	3/12/2004	EP1457993	9/8/2010	In Force	Multiport Memory Architecture, Devices and Systems Including the
MP0300	MP0300FR	FR	04006012.1	3/12/2004	1457993	9/8/2010	In Force	MULTIPORT MEMORY ARCHITECTURE, DEVICES AND SYSTEMS INCL
MP0300	MP0300GB	GB	04006012.1	3/12/2004	1457993	9/8/2010	In Force	MULTIPORT MEMORY ARCHITECTURE, DEVICES AND SYSTEMS INCL
MP0300	MP0300JP	JP	2004-071574	3/12/2004			Pending	MULTIPORT MEMORY ARCHITECTURE DEVICES AND SYSTEMS INCL
MP0300	MP0300JPD1	JP	2008-270813	10/21/2008	5107204	10/12/2012	In Force	MULTIPORT MEMORY ARCHITECTURE AND INTEGRATED CIRCUIT
MP0300	MP0300TW	TW	93106492	3/11/2004	I263228	10/1/2006	In Force	Multiport memory architecture, devices and systems including the s
MP0301	MP0301	US	10/629,164	7/28/2003	7088534	8/8/2006	In Force	DISK SYNCHRONOUS WRITE
MP0302	MP0302	US	10/761,879	1/21/2004	7826452	11/2/2010	In Force	Efficient Host-Controller Address Learning In Ethernet Switches
MP0302	MP0302C1	US	12/917,405	11/1/2010	8472445	6/25/2013	In Force	Efficient Host-Controller Address Learning In Ethernet Switches
MP0302	MP0302C1C1	US	13/920,577	6/18/2013	9294397	3/22/2016	In Force	Apparatus And Method For Forwarding Packets Based On Approved
MP0303	MP0303	US	10/773,474	2/5/2004	7590114	9/15/2009	In Force	Efficient IP Multicast Bridging In Ethernet Switches
MP0303	MP0303C1	US	12/559,969	9/15/2009	7983262	7/19/2011	In Force	Efficient IP Multicast Bridging In Ethernet Switches
MP0303	MP0303C1aa	US	14/101,750	12/10/2013	9065662	6/23/2015	In Force	Method And Apparatus For Using A Bridge Table As A Routing Table T
MP0303	MP0303C1C1	US	13/185,231	7/18/2011	8605727	12/10/2013	In Force	Efficient IP Multicast Bridging In Ethernet Switches
MP0304	MP0304	US	10/790,689	3/3/2004	7903777	3/8/2011	In Force	System And Method For Reducing Electromagnetic Interference And
MP0304	MP0304C1	US	13/042,344	3/7/2011	8170167	5/1/2012	In Force	System And Method For Reducing Electromagnetic Interference And
MP0304	MP0304C1aa	US	14/149,308	1/7/2014	9031181	5/12/2015	In Force	Method And Apparatus For Controlling Clocking Of Transceivers In A
MP0305	MP0305	US	10/464,278	6/17/2003	6850124	2/1/2005	In Force	CIRCUIT, ARCHITECTURE AND METHOD FOR TRACKING LOOP BAND
MP0306	MP0306I	US	11/070,481	3/2/2005	7454634	11/18/2008	In Force	Power Savings Apparatus And Method For Wireless Network Devices
MP0307	MP0307	US	10/623,031	7/17/2003	7535955	5/19/2009	In Force	METHODS, ALGORITHMS, SOFTWARE, ARCHITECTURES, SYSTEMS AN
MP0308	MP0308	US	10/788,998	2/27/2004	7505537	3/17/2009	In Force	System And Method For Controlling Gain And Timing Phase In A Pres
MP0308	MP0308aaaa	US	14/091,514	11/27/2013	9319024	4/19/2016	In Force	Apparatus And Method For Accounting For Gain And Timing Phase Er
MP0308	MP0308aZZa	US	15/491,424	4/19/2017	10027515	7/17/2018	In Force	Apparatus And Method For Introducing Gain And Phase Offset Via A
MP0308	MP0308C1	US	12/372,832	2/18/2009	8081720	12/20/2011	In Force	System And Method For Controlling Gain And Timing Phase In A Pres
MP0308	MP0308C1C1	US	13/329,987	12/19/2011	8279984	10/2/2012	In Force	System And Method For Controlling Gain And Timing Phase In A Pres
MP0308	MP0308C1ZZ	US	15/098,639	4/14/2016	9641359	5/2/2017	In Force	Apparatus And Method For Accounting For Gain And Phase Error Intr
MP0309	MP0309	US	10/671,030	9/24/2003	7199961	4/3/2007	In Force	Detecting Fly Height Of A Head Over A Storage Medium
MP0309	MP0309C1	US	11/656,734	1/22/2007	7359139	4/15/2008	In Force	Detecting Fly Height Of A Head Over A Storage Medium
MP0309	MP0309C1C1	US	12/082,423	4/11/2008	7518820	4/14/2009	In Force	Detecting Fly Height Of A Head Over A Storage Medium
MP0309	MP0309D1	US	11/103,748	4/12/2005	7136250	11/14/2006	In Force	Detecting Fly Height Of A Head Over A Storage Medium

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP0310	MP0310D1	US	11/463,278	8/8/2006	7262928	8/28/2007	In Force	DC-OFFSET COMPENSATION LOOPS FOR MAGNETIC RECORDING SYS
MP0310	MP0310D1C1	US	11/836,050	8/8/2007	7525746	4/28/2009	In Force	DC-OFFSET COMPENSATION LOOPS FOR MAGNETIC RECORDING SY
MP0310	MP0310D2	US	11/463,280	8/8/2006	7286312	10/23/2007	In Force	DC-OFFSET COMPENSATION LOOPS FOR MAGNETIC RECORDING SY
MP0311	MP0311	US	10/644,166	8/20/2003	7002761	2/21/2006	In Force	Demodulation Compensation For Spiral Servo Tracks In Hard Disk D
MP0311	MP0311aaa	US	12/330,789	12/9/2008	7715134	5/11/2010	In Force	Demodulation Compensation For Spiral Servo Tracks In Hard Disk D
MP0311	MP0311C1	US	11/333,725	1/17/2006	7307806	12/11/2007	In Force	Demodulation Compensation For Spiral Servo Tracks In Hard Disk D
MP0311	MP0311C1C1	US	12/001,285	12/11/2007	7463437	12/9/2008	In Force	Demodulation Compensation For Spiral Servo Tracks In Hard Disk D
MP0317	MP0317	US	10/752,339	1/6/2004	7228485	6/5/2007	In Force	ERROR CORRECTION USING ERROR DETECTION CODES
MP0317	MP0317C1	US	11/828,169	7/25/2007	8069391	11/29/2011	In Force	Error Correction Using Error Detection Codes
MP0317	MP0317D1	US	11/785,538	4/18/2007	7380195	5/27/2008	In Force	ERROR CORRECTION USING ERROR DETECTION CODES
MP0317	MP0317D1C1	US	12/127,669	5/27/2008	8190963	5/29/2012	In Force	Error Correction Using Error Detection Codes
MP0319	MP0319	US	10/765,474	1/26/2004	7265448	9/4/2007	In Force	Interconnect Structure For Power Transistors
MP0319	MP0319AFb	US	12/156,196	5/30/2008	7989852	8/2/2011	In Force	Integrated Circuits And Interconnect Structure For Integrated Circui
MP0319	MP0319AFCN	CN	200880008400	3/17/2008			Pending	INTEGRATED CIRCUITS AND INTERCONNECT STRUCTURE FOR INTEGR
MP0319	MP0319AFTW	TW	97109386	3/17/2008	I479634	4/1/2015	In Force	INTEGRATED CIRCUITS AND INTERCONNECT STRUCTURE FOR INTEGR
MP0319	MP0319AFWO	WO	PCT/US2008/00	3/17/2008			Pending	Integrated Circuits And Interconnect Structure For Integrated Circuits
MP0319	MP0319AFWOCN	CN	2008800084014	9/15/2009			Pending	Integrated Circuits And Interconnect Structure For Integrated Circuits
MP0319	MP0319C1HK	HK	61112711	10/13/2006	1090752	8/7/2009	In Force	Interconnect Structure With Wings
MP0319	MP0319C2HK	HK	61112720	1/12/2005			Pending	Interconnect Structure With Aluminum Core
MP0319	MP0319C2TW	TW	94115682	1/12/2005	I354368	12/11/2011	In Force	Interconnect Structure With Aluminum Core
MP0319	MP0319CN	CN	2005100020307	1/12/2005	ZL2005100020	11/19/2008	In Force	Integrated Circuit With Planar Connections
MP0319	MP0319CND1	CN	2005100026500	1/12/2005	ZL2005100026	12/3/2008	In Force	Interconnect Structure With Wings
MP0319	MP0319D1	US	11/386,276	3/22/2006	7459381	12/2/2008	In Force	Integrated Circuits And Interconnect Structure For Integrated Circuits
MP0319	MP0319D1I1	US	12/049,533	3/17/2008	7960833	6/14/2011	In Force	Integrated Circuits And Interconnect Structure For Integrated Circuits
MP0319	MP0319EP	EP	05000284.9	1/26/2005	1571708	6/19/2019	In Force	Integrated Circuits and Interconnect Structure for Integrated Circuits
MP0319	MP0319EPDE	DE	05000284.9	1/26/2005	602005055921	6/19/2019	In Force	Integrated Circuits and Interconnect Structure for Integrated Circuits
MP0319	MP0319EPFR	FR	05000284.9	1/26/2005	1571708	6/19/2019	In Force	Integrated Circuits and Interconnect Structure for Integrated Circuits
MP0319	MP0319EPGB	GB	05000284.9	1/26/2005	1571708	6/19/2019	In Force	Integrated Circuits and Interconnect Structure for Integrated Circuits
MP0319	MP0319I2CN	CN	2005100026483	1/12/2005			Pending	Interconnect Structure With Aluminum Core
MP0319	MP0319I2EP	EP	06011396.6	1/7/2005			Pending	Integrated circuits and interconnect structure for integrated circuits
MP0319	MP0319IC1TW	TW	94115698	1/12/2005	I354369	12/11/2011	In Force	Interconnect Structure With Wings
MP0319	MP0319IEP	EP	EP06011395.8	1/7/2005			Pending	Integrated circuits and interconnect structure for integrated circuits
MP0319	MP0319JP	JP	2005-006181	1/13/2005	4596925	10/1/2010	In Force	INTEGRATED CIRCUITS AND INTERCONNECT STRUCTURE FOR INTEGR
MP0319	MP0319JP2	JP	2005-276911	9/22/2005	5502255	3/20/2014	In Force	INTERCONNECT STRUCTURE WITH WINGS

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MP0319	MP0319JP3	JP	2005-276912	9/22/2005			Pending	INTERCONNECT STRUCTURE WITH ALUMINUM CORE
MP0319	MP0319TW	TW	94100907	1/12/2005	I354367	12/11/2011	In Force	Integrated Circuits And Interconnect Structure For Integrated Circui
MP0320	MP0320aFby	TW	96116400	5/8/2007	I420665	12/21/2013	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320aFcy	TW	96116397	5/8/2007	I407566	9/1/2013	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320AFUN	DE	07809055.2	5/8/2007	2030237	2/9/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320AFUR	FR	07809055.2	5/8/2007	2030237	2/9/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320AFUT	GB	07809055.2	5/8/2007	2030237	2/9/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320aFWJ	JP	2009-509836	5/8/2007	5137947	11/22/2012	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320AFWS	SG	200808294-3	5/8/2007	147678	7/15/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320aFWU	EP	EP07809055.2	5/8/2007	EP2030237	2/9/2011	In Force	Efficient Transistor Structure
MP0320	MP0320aFy	TW	96116398	5/8/2007	I429078	3/1/2014	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320al1a	US	11/586,467	10/25/2006	7528444	5/5/2009	In Force	Efficient Transistor Structure
MP0320	MP0320al1aTW	TW	96116401	5/8/2007			Pending	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320al1c	US	11/586,471	10/25/2006	7652338	1/26/2010	In Force	Efficient Transistor Structure
MP0320	MP0320C1l1	US	11/524,113	9/20/2006	7851872	12/14/2010	In Force	Efficient Transistor Structure
MP0320	MP0320C1l1WO	WO	PCT/US2007/01	5/8/2007			Pending	Efficient Transistor Structure
MP0320	MP0320C1l1WOCN	CN	200780025919.4	5/8/2007	ZL2007800259	1/26/2011	In Force	Efficient Transistor Structure
MP0320	MP0320C1l1WOKR	KR	1020087029444	5/8/2007	101373792	3/6/2014	In Force	Efficient Transistor Structure
MP0320	MP0320C1l1WOSG	SG	2008082943	11/7/2008	147678	7/15/2011	In Force	Efficient Transistor Structure
MP0320	MP0320CN	CN	2004100865328	10/21/2004	ZL2004100865	11/25/2009	In Force	Efficient Transistor Structure
MP0320	MP0320JP	JP	2004-238034	8/18/2004	4667788	1/21/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0320	MP0320TW	TW	93119017	6/29/2004	I349978	10/1/2011	In Force	EFFICIENT TRANSISTOR STRUCTURE
MP0329	MP0329	US	10/701,626	11/6/2003	7796952	9/14/2010	In Force	Transceiver System Including Dual Low-Noise Amplifiers
MP0329	MP0329C1	US	12/881,226	9/14/2010	8027644	9/27/2011	In Force	TRANSCEIVER SYSTEM INCLUDING DUAL LOW-NOISE AMPLIFIERS.
MP0330	MP0330D1	US	11/487,040	7/14/2006	7877075	1/25/2011	In Force	Signal Mixer Having A Single-Ended Input And A Differential Output
MP0330	MP0330D1C1	US	13/012,624	1/24/2011	8577322	11/5/2013	In Force	Signal Mixer Having A Single-Ended Input And A Differential Output
MP0331	MP0331	US	10/753,101	1/6/2004	7184799	2/27/2007	In Force	METHOD AND APPARATUS FOR REDUCING WAKE UP TIME OF A PO
MP0331	MP0331C1	US	11/546,605	10/11/2006	8359074	1/22/2013	In Force	METHOD AND APPARATUS FOR REDUCING WAKE UP TIME OF A PO
MP0331	MP0331C1C1	US	13/745,727	1/18/2013	8781539	7/15/2014	In Force	METHOD AND APPARATUS FOR REDUCING WAKE UP TIME OF A PO
MP0332	MP0332	US	10/673,263	9/30/2003	7433658	10/7/2008	In Force	System And Method For Controlling True Output Power Of A Transmi
MP0332	MP0332C1	US	12/287,194	10/7/2008	8099066	1/17/2012	In Force	System And Method For Controlling True Output Power Of A Transmi
MP0336	MP0336	US	10/714,804	11/17/2003	7475331	1/6/2009	In Force	DATA DEPENDENT SCRAMBLER WITH IMPROVED GLOBAL CONSTRAI
MP0337	MP0337	US	10/693,566	10/23/2003	7606316	10/20/2009	In Force	MIMO-OFDM PREAMBLE FOR CHANNEL ESTIMATION
MP0342	MP0342	US	10/670,022	9/23/2003	7561590	7/14/2009	In Force	Network Switch Having Virtual Input Queues For Flow Control

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MP0342	MP0342C1	US	12/502,046	7/13/2009	8005079	8/23/2011	In Force	Network Switch Having Virtual Input Queues For Flow Control
MP0342	MP0342C1aa	US	14/164,597	1/27/2014	8929363	1/6/2015	In Force	Apparatus And Method For Allocating Buffers Of A Memory Includir
MP0342	MP0342C1C1	US	13/215,106	8/22/2011	8638784	1/28/2014	In Force	Network Switch Having Virtual Input Queues For Flow Control
MP0343	MP0343	US	10/669,783	9/23/2003	7688736	3/30/2010	In Force	NETWORK SWITCH WITH QUALITY OF SERVICE FLOW CONTROL
MP0344	MP0344	US	10/752,785	1/5/2004	8319524	11/27/2012	In Force	Deglitch Circuit Removing Glitches From Input Clock Signal
MP0347	MP0347	US	10/825,781	4/16/2004	7239199	7/3/2007	In Force	AMPLIFIER CALIBRATION
MP0348	MP0348C1	US	11/189,464	7/26/2005	7015756	3/21/2006	In Force	Push-Pull Buffer/Amplifier
MP0349	MP0349	US	10/747,520	12/29/2003	7365582	4/29/2008	In Force	Bootstrapped Charge Pump Driver In A Phase-Lock Loop
MP0350	MP0350C1	US	11/717,576	3/13/2007	7501907	3/10/2009	In Force	Self-Biased Active VCO Level Shifter
MP0351	MP0351	US	10/776,476	2/10/2004	7444124	10/28/2008	In Force	ADJUSTABLE SEGMENTED POWER AMPLIFIER
MP0351	MP0351C1	US	12/212,598	9/17/2008	7863976	1/4/2011	In Force	ADJUSTABLE SEGMENTED POWER AMPLIFIER
MP0351	MP0351C1C1	US	12/984,477	1/4/2011	8044716	10/25/2011	In Force	ADJUSTABLE SEGMENTED POWER AMPLIFIER
MP0352	MP0352	US	10/694,306	10/27/2003	6950300	9/27/2005	In Force	Ultra Low Inductance Multi Layer Ceramic Capacitor
MP0352	MP0352C1	US	11/184,208	7/19/2005	7230816	6/12/2007	In Force	Ultra Low Inductance Multi Layer Ceramic Capacitor
MP0352	MP0352C1C1	US	11/811,881	6/12/2007	7701695	4/20/2010	In Force	Ultra Low Inductance Multi Layer Ceramic Capacitor
MP0352	MP0352CN	CN	2004100310216	4/7/2004	ZL2004100310	5/12/2010	In Force	Ultra Low Inductance Multi Layer Ceramic Capacitor
MP0352	MP0352EP	EP	EP04010108.1	4/28/2004	EP1480236	6/8/2016	In Force	Ultra Low Inductance Multi Layer Ceramic Capacitor
MP0352	MP0352EPDE	DE	04010108.1	4/28/2004	602004049415	6/8/2016	In Force	Ultra Low Inductance Multi Layer Ceramic Capacitor
MP0352	MP0352EPFR	FR	04010108.1	4/28/2004	1480236	6/8/2016	In Force	Ultra Low Inductance Multi Layer Ceramic Capacitor
MP0352	MP0352EPGB	GB	04010108.1	4/28/2004	1480236	6/8/2016	In Force	Ultra Low Inductance Multi Layer Ceramic Capacitor
MP0352	MP0352JP	JP	2004-134183	4/28/2004	4045258	11/22/2007	In Force	ULTRA LOW INDUCTANCE MULTI LAYER CERAMICCAPACITOR
MP0352	MP0352JP01	JP	2007-206337	8/8/2007	4498397	4/23/2010	In Force	ULTRA LOW INDUCTANCE MULTI LAYER CERAMIC CAPACITOR
MP0352	MP0352TW	TW	93110964	4/20/2004	I338903	3/11/2011	In Force	ULTRA LOW INDUCTANCE MULTI LAYER CERAMIC CAPACITOR
MP0354	MP0354	US	10/743,941	12/23/2003	7545867	6/9/2009	In Force	Adaptive Channel Bandwidth Selection for MIMO Wireless Systems
MP0354	MP0354C1	US	12/480,179	6/8/2009	7688898	3/30/2010	In Force	Adaptive Channel Bandwidth Selection For MIMO Wireless Systems
MP0354	MP0354C1aa	US	13/567,599	8/6/2012	8634483	1/21/2014	In Force	Operating Environment Based Wireless Communication Devices And
MP0354	MP0354C1C1	US	12/748,911	3/29/2010	8238453	8/7/2012	In Force	Adaptive Channel Bandwidth Selection For MIMO Wireless Systems
MP0354	MP0354C1Za	US	14/159,541	1/21/2014	9042466	5/26/2015	In Force	Apparatus And Method For Adjusting Bandwidth Based On An Operat
MP0355	MP0355	US	10/621,297	7/17/2003	7321593	1/22/2008	In Force	Fibre Channel Serdes With Elastic Transmitter FIFO
MP0356	MP0356	US	10/795,907	3/8/2004	6943712	9/13/2005	In Force	Voltage Built-in Real-Time Digital Non-Linearity Measurement Device
MP0356	MP0356C1	US	11/190,781	7/27/2005	6999013	2/14/2006	In Force	Built-In Real-Time Digital Non-Linearity Measurement Device And Me
MP0357	MP0357	US	10/627,269	7/25/2003	7437532	10/14/2008	In Force	Memory Mapped Register File
MP0357	MP0357C1C1	US	13/017,867	1/31/2011	8078828	12/13/2011	In Force	Memory Mapped Register File
MP0359	MP0359	US	10/738,368	12/15/2003	7181180	2/20/2007	In Force	SIGMA DELTA MODULATED PHASE LOCK LOOP WITH PHASE INTERP

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MP0359	MP0359C1	US	11/626,829	1/24/2007	7899422	3/1/2011	In Force	SIGMA DELTA MODULATED PHASE LOCK LOOP WITH PHASE INTERP
MP0360	MP0360	US	10/747,522	12/29/2003	7190936	3/13/2007	In Force	Voltage Regulator For High Performance RF Systems
MP0360	MP0360C1	US	11/715,027	3/7/2007	7809339	10/5/2010	In Force	Voltage Regulator For High Performance RF Systems
MP0360	MP0360C1AA	US	13/709,627	12/10/2012	8639201	1/28/2014	In Force	Voltage Regulator For High Performance RF Systems
MP0360	MP0360C1C1	US	12/893,604	9/29/2010	8331884	12/11/2012	In Force	Voltage Regulator For High Performance RF Systems
MP0361	MP0361	US	10/753,007	1/6/2004	6985033	1/10/2006	In Force	CIRCUITS, SOFTWARE AND METHODS FOR ADJUSTING POWER AMP
MP0362	MP0362	US	10/767,045	1/28/2004	7268990	9/11/2007	In Force	POWER AMPLIFIER PROTECTION
MP0362	MP0362C1	US	11/845,660	8/27/2007	7606016	10/20/2009	In Force	POWER AMPLIFIER PROTECTION
MP0362	MP0362C1aa	US	12/965,223	12/10/2010	8179648	5/15/2012	In Force	POWER AMPLIFIER PROTECTION
MP0362	MP0362C1C1	US	12/560,397	9/15/2009	7864498	1/4/2011	In Force	POWER AMPLIFIER PROTECTION
MP0364	MP0364C1	US	11/699,648	1/30/2007	7459975	12/2/2008	In Force	Baseband Filter Start-Up Circuit
MP0364	MP0364D1	US	11/699,890	1/30/2007	7528657	5/5/2009	In Force	Baseband Filter Start-Up Circuit
MP0364	MP0364D1C1	US	12/430,305	4/27/2009	7808317	10/5/2010	In Force	Baseband Filter Start-Up Circuit
MP0365	MP0365C1D1	US	13/154,295	6/6/2011	8731122	5/20/2014	In Force	SPURIOUS COMPONENT REDUCTION
MP0366	MP0366	US	10/861,481	6/7/2004	7167045	1/23/2007	In Force	System And Method For Modifying Output Power Of An Information
MP0366	MP0366C1	US	11/656,576	1/23/2007	7808322	10/5/2010	In Force	System And Method For Modifying Output Power Of An Information
MP0367	MP0367	US	10/672,774	9/26/2003	7096345	8/22/2006	In Force	DATA PROCESSING SYSTEM WITH BYPASS REORDER BUFFER HAVING
MP0367	MP0367C1	US	11/497,805	8/2/2006	7730285	6/1/2010	In Force	DATA PROCESSING SYSTEM WITH PARTIAL BYPASS REORDER BUFFER
MP0368	MP0368	US	10/752,601	1/6/2004	7378810	5/27/2008	In Force	METHOD AND APPARATUS FOR SPINDLE MOTOR CONTROL
MP0368	MP0368C2	US	12/098,008	4/4/2008	7573214	8/11/2009	In Force	METHOD AND APPARATUS FOR SPINDLE MOTOR CONTROL
MP0368	MP0368C2C1	US	12/494,868	6/30/2009	8022646	9/20/2011	In Force	METHOD AND APPARATUS FOR SPINDLE MOTOR CONTROL
MP0369	MP0369	US	10/701,661	11/5/2003	6897792	5/24/2005	In Force	REDUCING NUMBER OF CONSECUTIVE ONES IN DATA-DEPENDENT S
MP0376	MP0376	US	10/734,779	12/12/2003	6940107	9/6/2005	In Force	FUSE STRUCTURES, METHODS OF MAKING AND USING THE SAME, A
MP0376	MP0376D1	US	11/136,925	5/24/2005	7344924	3/18/2008	In Force	FUSE STRUCTURES, METHODS OF MAKING AND USING THE SAME, A
MP0376	MP0376D1C1	US	12/012,723	2/4/2008	7704805	4/27/2010	In Force	FUSE STRUCTURES, METHODS OF MAKING AND USING THE SAME, A
MP0376	MP0376D1D1	US	12/012,724	2/4/2008	7820493	10/26/2010	In Force	METHODS OF MAKING AND USING FUSE STRUCTURES, AND INTEGRA
MP0377	MP0377	US	10/829,866	4/21/2004	7706363	4/27/2010	In Force	METHOD AND APPARATUS FOR MANAGING PACKETS IN A PACKET S
MP0377	MP0377C1	US	12/753,808	4/2/2010	8144706	3/27/2012	In Force	METHOD AND APPARATUS FOR MANAGING PACKETS IN A PACKET S
MP0377	MP0377C1C1	US	13/415,557	3/8/2012	8923297	12/30/2014	In Force	METHOD AND APPARATUS FOR MANAGING PACKETS IN A PACKET S
MP0378	MP0378	US	10/752,817	1/6/2004	7330320	2/12/2008	In Force	METHOD AND APPARATUS TO LIMIT DC-LEVEL IN CODED DATA
MP0378	MP0378D1	US	12/022,131	1/29/2008	8098447	1/17/2012	In Force	METHOD AND APPARATUS TO LIMIT DC-LEVEL IN CODED DATA
MP0378	MP0378D1aa	US	13/745,694	1/18/2013	8773786	7/8/2014	In Force	SYSTEM FOR CORRECTING DC CHARACTERISTICS OF A MAGNETIC RE
MP0378	MP0378D1C1	US	13/350,684	1/13/2012	8358479	1/22/2013	In Force	METHOD AND APPARATUS TO LIMIT DC-LEVEL IN CODED DATA
MP0378	MP0378D1Za	US	14/321,641	7/1/2014	9165599	10/20/2015	In Force	METHOD FOR CORRECTING DC CHARACTERISTICS IN A MAGNETIC RE

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MP0382	MP0382	US	10/818,327	4/5/2004	6972921	12/6/2005	In Force	Circuit And Method For Protecting Emergency Head-Retract
MP0382	MP0382aaa	US	12/287,899	10/13/2008	7623316	11/24/2009	In Force	Circuit And Method For Protecting Emergency Head-Retract Capacit
MP0382	MP0382C2	US	11/485,597	7/12/2006	7436618	10/14/2008	In Force	Circuit And Method For Protecting Emergency Head-Retract Capacit
MP0386	MP0386	US	10/796,599	3/9/2004	7647507	1/12/2010	In Force	Secure Digital Content Distribution System And Secure Hard Drive
MP0386	MP0386C1	US	12/685,344	1/11/2010	8230236	7/24/2012	In Force	Secure Digital Content Distribution System And Secure Hard Drive
MP0386	MP0386C1aa	US	14/159,505	1/21/2014	9009497	4/14/2015	In Force	Secure Methods For Generating Content And Operating A Drive Bas
MP0386	MP0386C1C1	US	13/555,432	7/23/2012	8635466	1/21/2014	In Force	Secure Digital Content Distribution System And Secure Hard Drive
MP0388	MP0388	US	10/621,128	7/16/2003	7023313	4/4/2006	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388C1	US	11/274,360	11/15/2005	8035471	10/11/2011	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388CN	CN	2004100065182	3/4/2004	ZL2004100065	10/28/2009	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388EP	EP	EP04010841.7	5/6/2004	EP1498914	9/7/2016	In Force	Power inductor with reduced DC current saturation
MP0388	MP0388EP	EP	04760500.1	3/24/2004	1625666	8/1/2018	In Force	Dual Antenna System Having One Phase Lock Loop
MP0388	MP0388EP3	EP	EP04020568.4	8/30/2004	EP1548763	8/16/2017	In Force	Power Inductor with Reduced DC Current Saturation
MP0388	MP0388EPDE	DE	04010841.7	5/6/2004	602004049893	9/7/2016	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388EPFR	FR	04010841.7	5/6/2004	1498914	9/7/2016	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388EPGB	GB	04010841.7	5/6/2004	1498914	9/7/2016	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388fED1	US	11/728,112	3/23/2007	7868725	1/11/2011	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388fED2	US	11/728,064	3/23/2007	7987580	8/2/2011	In Force	A METHOD OF FABRICATING CONDUCTOR CROSSOVER STRUCTURE F
MP0388	MP0388fIDE	DE	04020571.8	8/30/2004	602004049820	8/24/2016	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388fIFR	FR	04020571.8	8/30/2004	1548764	8/24/2016	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388fIGB	GB	04020571.8	8/30/2004	1548764	8/24/2016	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388FJP2	JP	2005-183998	9/1/2004	4732811	4/28/2011	In Force	POWER INDUCTOR WITH REDUCED DC CURRENT SATURATION
MP0388	MP0388I	US	10/744,416	12/22/2003	7489219	2/10/2009	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388I2	US	10/875,903	6/24/2004	7307502	12/11/2007	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388I2CN	CN	2004100741664	9/1/2004	ZL2004100741	8/24/2011	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388I2D2	US	11/367,516	3/3/2006	7218197	5/15/2007	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388I2D3	US	11/367,176	3/3/2006	8028401	10/4/2011	In Force	A METHOD OF FABRICATING A CONDUCTING CROSSOVER STRUCTUR
MP0388	MP0388I2EP	EP	04020571.8	8/30/2004	EP1548764	8/24/2016	In Force	Power Inductor with Reduced DC Current Saturation
MP0388	MP0388I2JP	JP	2004254991	9/1/2004			Pending	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388I2JP02	JP	2004-254991	11/1/2004			Pending	POWER INDUCTOR WITH REDUCED DC CURRENT SATURATION
MP0388	MP0388I2TW	TW	93127467	9/10/2004	I401710	7/11/2013	In Force	Power Inductor With Reduced DC Current Saturation, DC/DC Convert
MP0388	MP0388I3CN	CN	2004100738002	9/1/2004			Pending	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388I3TW	TW	93127468	9/10/2004	I333220	11/11/2010	In Force	POWER INDUCTOR WITH REDUCED DC CURRENT SATURATION
MP0388	MP0388ICN	CN	2004100381809	5/11/2004			Pending	Power Inductor With Reduced DC Current Saturation

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MP0388	MP0388ID1	US	11/327,100	1/6/2006	8098123	1/17/2012	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388ID2	US	11/327,065	1/6/2006	7849586	12/14/2010	In Force	A METHOD OF MAKING A POWER INDUCTOR WITH REDUCED DC CU
MP0388	MP0388IEP	EP	04011558.6	5/14/2004	EP1498915	8/5/2015	In Force	Power inductor with reduced DC current saturation
MP0388	MP0388IJP	JP	2004178924	6/16/2004			Pending	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388ITW	TW	93116550	6/9/2004	I401711	7/11/2013	In Force	Power Inductor With Reduced DC Current Saturation And System C
MP0388	MP0388IUDE	DE	04011558.6	5/14/2004	602004047604	8/5/2015	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388IUFR	FR	04011558.6	5/14/2004	1498915	8/5/2015	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388IUGB	GB	04011558.6	5/14/2004	1498915	8/5/2015	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388JP	JP	2004-146964	5/17/2004	4473040	3/12/2010	In Force	POWER INDUCTOR WITH REDUCED DC CURRENT SATURATION
MP0388	MP0388TW	TW	93108084	3/25/2004	I333219	11/11/2010	In Force	POWER INDUCTOR WITH REDUCED DC CURRENT SATURATION
MP0388	MP0388U3DE	DE	04020568.4	8/30/2004	602004051655	8/16/2017	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388U3FR	FR	04020568.4	8/30/2004	1548763	8/16/2017	In Force	Power Inductor With Reduced DC Current Saturation
MP0388	MP0388U3GB	GB	04020568.4	8/30/2004	1548763	8/16/2017	In Force	Power Inductor With Reduced DC Current Saturation
MP0389	MP0389	US	10/734,440	12/11/2003	7864678	1/4/2011	In Force	RATE ADAPTATION IN WIRELESS SYSTEMS
MP0389	MP0389C1C1	US	14/244,750	4/3/2014	9271192	2/23/2016	In Force	RATE ADAPTATION IN WIRELESS SYSTEMS
MP0392	MP0392	US	10/701,491	11/5/2003	7161752	1/9/2007	In Force	ASYMMETRIC COMPENSATION CIRCUIT
MP0393	MP0393	US	10/666,892	9/17/2003	7568083	7/28/2009	In Force	Memory Mapped Register File And Method For Accessing The Same
MP0393	MP0393D1	US	11/339,049	1/25/2006	7558942	7/7/2009	In Force	Memory Mapped Register File And Method For Accessing The Same
MP0394	MP0394	US	10/744,982	12/22/2003	7737666	6/15/2010	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394CN	CN	200410042854.2	5/26/2004	ZL 2004100428	5/26/2010	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394CND1	CN	200910221130.7	5/26/2004	ZL 2009102211	3/13/2013	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394DE	DE	04009304.9	4/20/2004	1505716	11/3/2010	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394EP	EP	EP04009304.9	4/20/2004	EP1505716	11/3/2010	In Force	Split Gate Drive Scheme to Improve Reliable Voltage Operation Rang
MP0394	MP0394FR	FR	04009304.9	4/20/2004	1505716	11/3/2010	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394GB	GB	04009304.9	4/20/2004	1505716	11/3/2010	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394JP	JP	2004-114712	4/8/2004	3774220	2/24/2006	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0394	MP0394TW	TW	93111803	4/28/2004	I282203	6/1/2007	In Force	SPLIT GATE DRIVE SCHEME TO IMPROVE RELIABLE VOLTAGE OPERAT
MP0395	MP0395	US	10/722,079	11/25/2003	7477467	1/13/2009	In Force	Preamp Circuit Including A Loopback Mode For Data Storage
MP0395	MP0395AAaa	US	13/767,176	2/14/2013	8514508	8/20/2013	In Force	Preamp Circuit Including A Loopback Mode for Data Storage Devices
MP0395	MP0395AAZZ	US	14/181,864	2/17/2014	8947808	2/3/2015	In Force	Preamp Circuit Including A Loopback Mode For Data Storage Devices
MP0395	MP0395D1	US	12/341,021	12/22/2008	7852585	12/14/2010	In Force	Preamp Circuit Including A Loopback Mode For Data Storage Devices
MP0395	MP0395D1Aa	US	13/323,764	12/12/2011	8379341	2/19/2013	In Force	Preamp Circuit Including A Loopback Mode For Data Storage Devices
MP0395	MP0395D1D1	US	12/966,099	12/13/2010	8077415	12/13/2011	In Force	Preamp Circuit Including A Loopback Mode For Data Storage Devices
MP0396	MP0396	US	10/767,067	1/28/2004	7623441	11/24/2009	In Force	SCALABLE SPACE-FREQUENCY CODING FOR MIMO SYSTEMS

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MP0396	MP0396C1	US	12/616,753	11/11/2009	8159930	4/17/2012	In Force	SCALABLE SPACE-FREQUENCY CODING FOR MIMO SYSTEMS
MP0399	MP0399C1	US	11/523,423	9/19/2006	7262665	8/28/2007	In Force	Active Bias Circuit For Low-Noise Amplifiers
MP0399	MP0399C1C1	US	11/895,973	8/28/2007	7515000	4/7/2009	In Force	Active Bias Circuit For Low-Noise Amplifiers
MP0400	MP0400	US	10/693,787	10/24/2003	7760525	7/20/2010	In Force	Voltage Regulator
MP0400	MP0400CN	CN	2004100794322	8/20/2004	ZL2004100794	6/24/2009	In Force	Voltage Regulator
MP0400	MP0400CN2	CN	2004100794337	8/20/2004	ZL2004100794	9/22/2010	In Force	Voltage Regulator
MP0400	MP0400EP	EP	EP20040019978	8/23/2004			Pending	Digit Low Dropout Regulator
MP0400	MP0400EP1	EP	EP20040019981	8/23/2004			Pending	Voltage Regulator
MP0400	MP0400EP2	EP	EP10174008.2	8/23/2004	2254226	4/4/2018	In Force	Voltage Regulator
MP0400	MP0400EP3	EP	07004458.1	8/23/2004			Pending	Voltage Regulator
MP0400	MP0400EP4	EP	70049606	3/9/2007	1808952	4/16/2014	In Force	Voltage Regulator
MP0400	MP0400EP5	EP	10177633.4	8/23/2004			Pending	Digit Low Dropout Regulator
MP0400	MP0400FUDE	DE	04019963.0	8/23/2004	602004030193	11/24/2010	In Force	Voltage Regulator
MP0400	MP0400FUFR	FR	04019963.0	8/23/2004	1508958	11/24/2010	In Force	Voltage Regulator
MP0400	MP0400FUGB	GB	04019963.0	8/23/2004	1508958	11/24/2010	In Force	Voltage Regulator
MP0400	MP0400I	US	10/754,187	1/8/2004	7872454	1/18/2011	In Force	Digital Low Dropout Regulator
MP0400	MP0400I1C1	US	12/838,679	7/19/2010	8299763	10/30/2012	In Force	Digital Low Dropout Regulator
MP0400	MP0400ICN	CN	2004100794981	8/20/2004	ZL2004100794	12/23/2009	In Force	Digital Low Dropout Regulator
MP0400	MP0400IEP	EP	04019963.0	8/23/2004	EP1508958	11/24/2010	In Force	Voltage Regulator
MP0400	MP0400IJP	JP	2004-243036	8/23/2004	4435647	6/11/2010	In Force	VOLTAGE REGULATOR
MP0400	MP0400IJP1	JP	2004-242998	8/23/2004	4527470	8/18/2010	In Force	DIGITAL LOW DROPOUT REGULATOR
MP0400	MP0400ITW	TW	93125139	8/20/2004	I373697	10/1/2012	In Force	Digital Low Dropout Regulator
MP0400	MP0400JP	JP	2005-338676	11/24/2005	4435731	1/8/2010	In Force	VOLTAGE REGULATOR
MP0400	MP0400TW	TW	93125134	8/20/2004	I363263	5/1/2012	In Force	Coupled Inductor Regulator For Amplifier System, Vehicle System, Dri
MP0400	MP0400TW2	TW	93125133	8/20/2004	I361342	4/1/2012	In Force	Voltage Regulator
MP0400	MP0400U2DE	DE	EP10174008.2	8/23/2004	602004052564	4/4/2018	In Force	Voltage Regulator
MP0400	MP0400U2FR	FR	EP10174008.2	8/23/2004	2254226	4/4/2018	In Force	Voltage Regulator
MP0400	MP0400U2GB	GB	EP10174008.2	8/23/2004	2254226	4/4/2018	In Force	Voltage Regulator
MP0400	MP0400U4DE	DE	07004960.6	8/23/2004	1808952	4/16/2014	In Force	VOLTAGE REGULATOR
MP0400	MP0400U4FR	FR	07004960.6	8/23/2004	1808952	4/16/2014	In Force	VOLTAGE REGULATOR
MP0400	MP0400U4GB	GB	07004960.6	8/23/2004	1808952	4/16/2014	In Force	VOLTAGE REGULATOR
MP0401	MP0401C1	US	11/170,926	6/30/2005	7030685	4/18/2006	In Force	Frequency Boosting Circuit For High Swing Cascode Biasing Circuits
MP0402	MP0402	US	10/789,306	2/27/2004	7049894	5/23/2006	In Force	Ahuja Compensation Circuit With Enhanced Bandwidth
MP0403	MP0403	US	10/804,660	3/19/2004	7266001	4/9/2007	In Force	METHOD AND APPARATUS FOR CONTROLLING POWER FACTOR COR

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MP0403	MP0403C1I1	US	12/100,345	4/9/2008	7990740	8/2/2011	In Force	Method And Apparatus For Controlling Power Factor Correction
MP0403	MP0403I1	US	11/849,158	8/31/2007	7733678	8/6/2010	In Force	POWER FACTOR CORRECTION BOOST CONVERTER WITH CONTINUC
MP0405	MP0405	US	10/665,252	9/19/2003	7457271	11/25/2008	In Force	WIRELESS LOCAL AREA NETWORK AD-HOC MODE FOR REDUCING P
MP0405	MP0405C1	US	12/269,413	11/12/2008	8265048	9/11/2012	In Force	Wireless Local Area Network Ad-Hoc Mode For Reducing Power Cor
MP0405	MP0405C1C1	US	13/608,496	9/10/2012	8902872	12/2/2014	In Force	Wireless Local Area Network Ad-Hoc Mode For Reducing Power Cor
MP0407	MP0407	US	10/795,023	3/5/2004	7088181	8/8/2006	In Force	METHOD AND APPARATUS FOR COMMON MODE CONTROL
MP0407	MP0407C1	US	11/444,240	5/30/2006	7345544	3/18/2008	In Force	METHOD AND APPARATUS FOR COMMON MODE CONTROL
MP0408	MP0408	US	10/792,619	3/3/2004	7276969	10/2/2007	In Force	Multi-Amplifier Circuit
MP0408	MP0408C1	US	11/904,765	9/28/2007	7501890	3/10/2009	In Force	Multi-Amplifier Circuit
MP0408	MP0408D1	US	11/904,365	9/27/2007	7471149	12/30/2008	In Force	Multi-Amplifier Circuit
MP0408	MP0408FAa	US	12/286,772	10/2/2008	7688141	3/30/2010	In Force	Multi-Amplifier Circuit
MP0408	MP0408I1	US	11/504,348	8/15/2006	7312659	12/25/2007	In Force	Multi-Amplifier Circuit
MP0408	MP0408I1D1	US	12/004,126	12/20/2007	7468633	12/23/2008	In Force	Multi-Amplifier Circuit
MP0409	MP0409C1	US	11/511,138	8/28/2006	7411755	8/12/2008	In Force	Write Driver System For Data Storage Systems
MP0410	MP0410	US	10/703,135	11/6/2003	7190224	3/13/2007	In Force	Class D Amplifier
MP0410	MP0410aaa	US	11/985,653	11/16/2007	7468631	12/23/2008	In Force	Class D Amplifier
MP0410	MP0410aaaa	US	12/342,768	12/23/2008	7646240	1/12/2010	In Force	Class D Amplifier
MP0410	MP0410C1	US	11/170,440	6/28/2005	7242248	7/10/2007	In Force	Class D Amplifier
MP0410	MP0410C1C1	US	11/702,728	2/6/2007	7298209	11/20/2007	In Force	Class D Amplifier
MP0410	MP0410CN	CN	2004100704773	8/3/2004	ZL2004100704	7/1/2009	In Force	Class D Amplifier
MP0410	MP0410EP	EP	EP04011557.8	5/14/2004	EP1530286	1/10/2007	In Force	CLASS D AMPLIFIER
MP0410	MP0410EPDE	DE	04011557.8	5/14/2004	602004004230	1/10/2007	In Force	Class D Amplifier
MP0410	MP0410EPFR	FR	04011557.8	5/14/2004	1530286	1/10/2007	In Force	Class D Amplifier
MP0410	MP0410EPGB	GB	04011557.8	5/14/2004	1530286	1/10/2007	In Force	Class D Amplifier
MP0410	MP0410JP	JP	2004-231068	8/6/2004	4015648	9/21/2007	In Force	CLASS D AMPLIFIER
MP0410	MP0410TW	TW	93118269	6/24/2004	I337805	2/21/2011	In Force	CLASS D AMPLIFIER
MP0410	MP0410Zaaa	US	12/685,884	1/12/2010	7884670	2/8/2011	In Force	Class D Amplifier
MP0412	MP0412	US	10/818,395	4/2/2004	8199686	6/12/2012	In Force	Wireless Local Area Network Infrastructure Mode For Reducing Powe
MP0412	MP0412D1	US	13/493,210	6/11/2012	8842591	9/23/2014	In Force	Wireless Local Area Network Infrastructure Mode For Reducing Powe
MP0414	MP0414	US	10/794,733	3/5/2004	7203017	4/10/2007	In Force	TIMING RECOVERY FOR DATA STORAGE CHANNELS WITH BUFFERED
MP0415	MP0415	US	10/862,414	6/8/2004	7460326	12/2/2008	In Force	Propagation Self Servo Write System And Method For Storage Device
MP0415	MP0415C1C1	US	12/945,204	11/12/2010	8094404	1/10/2012	In Force	Propagation Self Servo Write System And Method For Storage Device
MP0416	MP0416	US	10/799,474	3/11/2004	7307807	12/11/2007	In Force	DISK SERVO PATTERN WRITING
MP0416	MP0416C1	US	11/953,811	12/10/2007	7580216	8/25/2009	In Force	DISK SERVO PATTERN WRITING

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MP0416	MP0416D1	US	11/953,794	12/10/2007	7760459	7/20/2010	In Force	FRAMING SELECTION AND WAVEFORM POLARITY DETERMINATION F
MP0416	MP0416D1C1	US	12/795,129	6/7/2010	7990643	8/2/2011	In Force	READ CHANNEL CONFIGURATION BASED ON POLARITY DETERMINE
MP0417	MP0417	US	10/762,153	1/20/2004	7471670	12/30/2008	In Force	METHOD AND APPARATUS FOR REDUCING ECHO AND CROSSTALK I
MP0417	MP0417C1	US	12/345,440	12/29/2008	8270394	9/18/2012	In Force	METHOD AND APPARATUS FOR REDUCING AN INTERFERENCE SIGN
MP0417	MP0417C1aa	US	14/258,781	4/22/2014	9461703	10/4/2016	In Force	INTERFERENCE SIGNAL COMPENSATION
MP0417	MP0417C1C1	US	13/607,547	9/7/2012	8787861	7/22/2014	In Force	METHOD AND APPARATUS FOR REDUCING ECHO AND CROSSTALK I
MP0418	MP0418	US	10/845,028	5/13/2004	7640446	12/29/2009	In Force	System-On-Chip Power Reduction Through Dynamic Clock Frequenc
MP0418	MP0418C1	US	12/645,840	12/23/2009	8281170	10/2/2012	In Force	System-On-Chip Power Reduction Through Dynamic Clock Frequenc
MP0418	MP0418C1aa	US	14/065,879	10/29/2013	9423821	8/23/2016	In Force	Method And Apparatus For Setting A Frequency Of A Clock Signal Fc
MP0418	MP0418C1C1	US	13/632,551	10/1/2012	8572419	10/29/2013	In Force	System-On-Chip Power Reduction Through Dynamic Clock Frequenc
MP0422	MP0422	US	10/802,597	3/16/2004	7116144	10/3/2006	In Force	HIGH BANDWIDTH PHASE LOCKED LOOP (PLL)
MP0422	MP0422D1	US	11/542,961	10/3/2006	7427883	9/23/2008	In Force	HIGH BANDWIDTH PHASE LOCKED LOOP (PLL)
MP0422	MP0422D1C1	US	12/235,507	9/22/2008	7898306	3/1/2011	In Force	HIGH BANDWIDTH PHASE LOCKED LOOP (PLL) WITH FEEDBACK LOC
MP0423	MP0423	US	10/797,254	3/10/2004	7372924	5/13/2008	In Force	METHOD AND SYSTEM TO IMPROVE DECISION-DRIVEN CONTROL LO
MP0424	MP0424	US	10/819,333	4/6/2004	7595948	9/29/2009	In Force	Thermal Asperity Detection For Perpendicular Magnetic Recording
MP0425	MP0425	US	10/788,844	2/27/2004	7286328	10/23/2007	In Force	Electrostatic Discharge Protection Circuit For Preamps Connected To
MP0425	MP0425C1	US	11/975,975	10/23/2007	7480121	1/20/2009	In Force	Electrostatic Discharge Protection Circuit For Preamps Connected To
MP0425	MP0425D1	US	11/975,979	10/23/2007	8424190	4/23/2013	In Force	Method Of Providing Electrostatic Discharge Protection For A Read El
MP0425	MP0425Faa	US	12/967,753	12/14/2010	8149531	4/3/2012	In Force	Electrostatic Discharge Protection Circuit For Magneto-Resistive Read
MP0425	MP0425Faaa	US	13/437,322	4/2/2012	8462457	6/11/2013	In Force	Electrostatic Discharge Protection Circuit For Magneto-Resistive Read
MP0425	MP0425Faaaa	US	13/911,571	6/6/2013	8760791	6/24/2014	In Force	Magnetic Storage Systems With Transistors And Limiting Circuits For
MP0425	MP0425I	US	10/877,033	6/25/2004	7167331	1/23/2007	In Force	Electrostatic Discharge Protection Circuit For Magneto-Resistive Read
MP0425	MP0425IC1	US	11/643,431	12/21/2006	7852591	12/14/2010	In Force	Electrostatic Discharge Protection Circuit For Magneto-Resistive Read
MP0426	MP0426	US	10/814,534	3/31/2004	7023271	4/4/2006	In Force	Variable-Gain Constant-Bandwidth Transimpedance Amplifier
MP0426	MP0426C1C1	US	11/500,241	8/7/2006	7239202	7/3/2007	In Force	Variable-Gain Constant-Bandwidth Transimpedance Amplifier
MP0427	MP0427	US	10/794,563	3/5/2004	7610022	10/27/2009	In Force	FILTER CIRCUIT WITH NEGATIVE TRANSCONDUCTANCE
MP0428	MP0428	US	10/793,359	3/4/2004	7652515	1/26/2010	In Force	CLOCK SIGNAL CONVERSION SYSTEM
MP0429	MP0429	US	10/832,900	4/26/2004	7667916	2/23/2010	In Force	SIGNAL CONVERSION SYSTEM AND METHOD
MP0429	MP0429C1	US	12/701,412	2/5/2010	7929241	4/19/2011	In Force	SIGNAL CONVERSION SYSTEM AND METHOD
MP0431	MP0431	US	10/792,194	3/3/2004	7433425	10/7/2008	In Force	ADAPTIVE SIGNAL TUNING FOR DIGITAL RADIO RECEIVERS OPERATIN
MP0431	MP0431C1	US	12/150,621	4/30/2008	7555065	6/30/2009	In Force	Adaptive Signal Tuning For Digital Radio Receivers Operating In FM B
MP0432	MP0432	US	10/799,548	3/11/2004	7532563	5/12/2009	In Force	MECHANISM TO IMPROVE QUALITY OF CHANNEL ESTIMATES IN OFD
MP0434	MP0434	US	10/896,161	7/20/2004	7675979	3/9/2010	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, ARCHITECTURES, A
MP0440	MP0440	US	10/886,860	7/7/2004	7957394	6/7/2011	In Force	Automatic Network Switch Configuration To Support Quality Of Servi

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MP0441	MP0441	US	10/883,540	6/30/2004	7466653	12/16/2008	In Force	Quality Of Service For A Stackable Network Switch
MP0442	MP0442C1	US	10/926,946	8/27/2004	7675869	3/9/2010	In Force	APPARATUS AND METHOD FOR MASTER ELECTION AND TOPOLOGY
MP0443	MP0443	US	10/977,245	10/29/2004	7646717	1/12/2010	In Force	METHOD AND APPARATUS FOR PACKET SCHEDULING
MP0443	MP0443C1	US	12/686,307	1/12/2010	7894347	2/22/2011	In Force	METHOD AND APPARATUS FOR PACKET SCHEDULING
MP0445	MP0445	US	10/901,689	7/29/2004	7266680	9/4/2007	In Force	METHOD AND APPARATUS FOR LOADING CONFIGURATION DATA
MP0447	MP0447	US	10/039,585	1/4/2002	7216285	5/8/2007	In Force	System And Method For Generating Cyclic Redundancy Check
MP0447	MP0447C1	US	11/385,962	3/21/2006	7418648	8/26/2008	In Force	System And Method For Generating A Cyclic Redundancy Check
MP0447	MP0447C1C1	US	12/229,585	8/25/2008	8161359	4/17/2012	In Force	System And Method For Generating A Cyclic Redundancy Check
MP0450	MP0450	US	10/779,544	2/13/2004	7421602	9/2/2008	In Force	Computer With Low-Power Secondary Processor And Secondary Dis
MP0450	MP0450C1	US	12/229,034	8/19/2008	8151129	4/3/2012	In Force	Computer With Low-Power Secondary Processor And Secondary Dis
MP0450	MP0450C1C1	US	13/437,490	4/2/2012	8341447	12/25/2012	In Force	Computer With Low-Power Secondary Processor And Secondary Dis
MP0450	MP0450CN	CN	2005100075297	2/5/2005	ZL2005100075	12/23/2009	In Force	Computer With Low-Power Secondary Processor And Secondary Dis
MP0450	MP0450EP	EP	50020593	2/1/2005			Pending	Computer With Low-Power Secondary Processor And Secondary Dis
MP0450	MP0450JP	JP	2005-033647	2/9/2005	4963791	4/6/2012	In Force	Computer With Low-Power Secondary Processor and Secondary Displ
MP0450	MP0450TW	TW	94101598	1/19/2005	I368140	7/11/2012	In Force	Computer With Low-Power Secondary Processor And Secondary Disp
MP0451	MP0451	US	10/872,098	6/17/2004	7301482	11/27/2007	In Force	CIRCUITS, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS AND
MP0451	MP0451C1	US	11/978,788	10/29/2007	7425905	9/16/2008	In Force	CIRCUITS, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS AND
MP0451	MP0451C1C1	US	12/206,673	8/9/2008	7786902	8/31/2010	In Force	CIRCUITS, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS AND
MP0453	MP0453C1	US	12/791,727	6/1/2010	8392548	3/5/2013	In Force	METHOD AND APPARATUS FOR GENERATING DIAGNOSTIC INFORMA
MP0454	MP0454	US	10/819,649	4/6/2004	7406302	7/29/2008	In Force	DIGITAL FM STEREO RECEIVER ARCHITECTURE
MP0454	MP0454C1	US	12/170,224	7/9/2008	8406717	3/26/2013	In Force	DIGITAL FM STEREO RECEIVER ARCHITECTURE TO RECOVER CARRIER
MP0454	MP0454C1C1	US	13/800,215	3/13/2013	9337944	5/10/2016	In Force	DIGITAL FM STEREO RECEIVER ARCHITECTURE
MP0455	MP0455	US	10/843,285	5/11/2004	7277031	10/2/2007	In Force	100BASE-FX SERIALIZER/DESERIALIZER USING 1000BASE-X SERIALIZ
MP0455	MP0455D1	US	11/900,484	9/12/2007	7477172	1/13/2009	In Force	100BASE-FX SERIALIZER/DESERIALIZER USING 1000BASE-X SERIALIZE
MP0455	MP0455D1aa	US	12/874,343	9/2/2010	7994950	8/9/2011	In Force	100Base-FX Serializer/Deserializer Using 1000Base-X Serializer/Deseri
MP0455	MP0455D1C1	US	12/351,061	1/9/2009	7791512	9/7/2010	In Force	100Base-FX Serializer/Deserializer Using 1000Base-X Serializer/Deseri
MP0458	MP0458	US	10/819,454	4/6/2004	7366488	4/29/2008	In Force	DIGITAL CARRIER-RECOVERY SCHEME FOR FM STEREO DETECTION
MP0458	MP0458C1	US	12/106,989	4/21/2008	7826812	11/2/2010	In Force	DIGITAL CARRIER-RECOVERY SCHEME FOR FM STEREO DETECTION
MP0458	MP0458C1C1	US	12/916,335	10/29/2010	8064858	11/22/2011	In Force	DIGITAL CARRIER-RECOVERY SCHEME FOR FM STEREO DETECTION
MP0460	MP0460	US	10/861,802	6/4/2004	7649855	1/19/2010	In Force	Alternative 1000Base-T Scrambler
MP0461	MP0461	US	10/852,540	5/24/2004	7246018	7/17/2007	In Force	Interpolator Testing Circuit
MP0461	MP0461C1	US	11/800,816	5/8/2007	7532995	5/12/2009	In Force	Interpolator Testing Circuit
MP0465	MP0465C1	US	11/710,384	2/23/2007	7679347	3/16/2010	In Force	Closed-Loop Digital Control System For A DC/DC Converter
MP0465	MP0465C1C1	US	12/723,867	3/15/2010	8183846	5/22/2012	In Force	Method And Apparatus For Controlling A DC/DC Converter

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MP0465	MP0465EP	EP	EP05012809.9	6/14/2005	1607548	2/9/2011	In Force	Closed-Loop Digital Control System for a DC/DC Converter
MP0465	MP0465EPDE	DE	05012809.9	6/14/2005	602005026233	2/9/2011	In Force	Closed-Loop Digital Control System For A DC/DC Converter
MP0465	MP0465EPFR	FR	05012809.9	6/14/2005	1617548	2/9/2011	In Force	Closed-Loop Digital Control System For A DC/DC Converter
MP0465	MP0465EPGB	GB	05012809.9	6/14/2005	1617548	2/9/2011	In Force	Closed-Loop Digital Control System For A DC/DC Converter
MP0465	MP0465JP	JP	2005-179978	6/20/2005	4652901	12/24/2010	In Force	Closed-Loop Digital Control System For A DC/DC Converter
MP0465	MP0465TW	TW	94120283	6/17/2005	I381606	1/1/2013	In Force	CLOSE-LOOP DIGITAL CONTROL SYSTEM FOR A DC/DC CONVERTER
MP0467	MP0467	US	10/810,452	3/26/2004	8324872	12/4/2012	In Force	Voltage Regulator With Coupled Inductors Having High Coefficient C
MP0467	MP0467CN	CN	2005100513380	3/4/2005			Pending	Voltage Regulator
MP0467	MP0467EP	EP	50032002	2/15/2005			Pending	Voltage Regulator
MP0467	MP0467JP	JP	2005059456	3/3/2005			Pending	Voltage Regulator
MP0467	MP0467TW	TW	94104865	2/18/2005			Pending	Voltage Regulator
MP0468	MP0468	US	10/883,342	1/7/2004	7420333	2/9/2008	In Force	MIXED MODE CONTROL FOR DIMMABLE FLUORESCENT LAMP
MP0468	MP0468C1	US	12/202,017	8/29/2008	7923939	12/4/2011	In Force	MIXED MODE CONTROL FOR DIMMABLE FLUORESCENT LAMP
MP0469	MP0469I	US	11/076,726	3/9/2005	7525293	4/28/2009	In Force	POWER SUPPLY SWITCHING CIRCUIT FOR A HALOGEN LAMP
MP0469	MP0469I1C1	US	12/422,857	4/13/2009	8044643	10/25/2011	In Force	POWER SUPPLY SWITCHING CIRCUIT FOR A HALOGEN LAMP
MP0470	MP0470	US	11/029,194	1/3/2005	7199661	4/3/2007	In Force	VARIABLE GAIN AMPLIFICATION USING TAYLOR EXPANSION
MP0474	MP0474	US	10/899,630	7/26/2004	7489750	2/10/2009	In Force	DC-CONTROL FOR POST PROCESSOR
MP0474	MP0474D1	US	12/360,065	1/26/2009	8218258	7/10/2012	In Force	DC-CONTROL FOR POST PROCESSOR
MP0477	MP0477	US	10/896,313	7/20/2004	7995455	8/9/2011	In Force	Scalable MIMO-OFDM PHY For High Throughput WLANS
MP0477	MP0477D1	US	13/206,105	8/9/2011	8345534	1/1/2013	In Force	Scalable MIMO-OFDM PHY For High Throughput WLANs
MP0477	MP0477D1aa	US	14/931,089	11/3/2015	9935665	4/3/2018	In Force	Systems And Methods For Transmitting Data Frames Using Multiple A
MP0477	MP0477D1C1	US	13/727,882	12/27/2012	9191051	11/17/2015	In Force	Scalable MIMO-OFDM PHY For High Throughput WLANs
MP0478	MP0478	US	10/776,381	2/11/2004	7002409	2/21/2006	In Force	Compensation Circuit For Amplifiers Having Multiple Stages
MP0478	MP0478C1	US	11/293,556	12/2/2005	7403067	7/22/2008	In Force	Compensation Circuit For Amplifiers Having Multiple Stages
MP0478	MP0478C1C1	US	12/218,991	7/18/2008	7633338	12/15/2009	In Force	Compensation Circuit For Amplifiers Having Multiple Stages
MP0480	MP0480D1	US	12/355,687	1/16/2009	8429507	4/23/2013	In Force	Device And Method For Detecting And Correcting Errors In A Data Str
MP0481	MP0481	US	10/846,717	5/13/2004	7514911	4/7/2009	In Force	Voltage Regulator Feedback Protection Method And Apparatus
MP0481	MP0481AUDE	DE	05011612.8	4/18/2005	602005019946	3/17/2010	In Force	Voltage Regulator With Signal Generator Having High Output Impeda
MP0481	MP0481AUFR	FR	05011612.8	4/18/2005	1602997	3/17/2010	In Force	Voltage Regulator With Signal Generator Having High Output Impeda
MP0481	MP0481AUGB	GB	05011612.8	4/18/2005	1602997	3/17/2010	In Force	Voltage Regulator With Signal Generator Having High Output Impeda
MP0481	MP0481C1HK	HK	71012962	5/13/2005			Pending	Voltage Regulator With Multiple Feedback
MP0481	MP0481CN	CN	2005100679545	4/29/2005	ZL2005100679	6/2/2010	In Force	Voltage Regulator With Separately-Derived Pull-Up Voltage
MP0481	MP0481D1	US	12/398,265	3/5/2009	7960958	6/14/2011	In Force	Voltage Regulator Feedback Protection Method And Apparatus
MP0481	MP0481EP	EP	EP05008414.4	4/18/2005	1596267	10/15/2008	In Force	VOLTAGE REGUALTOR FEEDBACK PROTECTION METHOD AND APPAR

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MP0481	MP0481EPD1	EP	50116128	5/30/2005	1602997	3/17/2010	In Force	Voltage Regulator With Signal Generator Having High Output Impeda
MP0481	MP0481EPDE	DE	05008414.4	4/18/2005	602005010325	10/15/2008	In Force	Voltage Regulator Feedback Protection Method And Apparatus
MP0481	MP0481EPFR	FR	05008414.4	4/18/2005	1596267	10/15/2008	In Force	Voltage Regulator Feedback Protection Method And Apparatus
MP0481	MP0481EPGB	GB	05008414.4	4/18/2005	1596267	10/15/2008	In Force	Voltage Regulator Feedback Protection Method And Apparatus
MP0481	MP0481I2TW	TW	94132733	9/21/2005	I370341	8/11/2012	In Force	Integrated Circuit With Signal Generator Having High Output Imped
MP0481	MP0481IEP	EP	50116136	5/30/2005			Pending	Voltage Regulator With Multiple Feedback
MP0481	MP0481ITW	TW	94132734	4/20/2005			Pending	Voltage Regulator With Multiple Feedback
MP0481	MP0481JP	JP	2005-126973	4/25/2005	4712429	4/1/2011	In Force	Voltage Regulator Feedback Protection Method and Apparatus
MP0481	MP0481JP2	JP	2005-180009	6/20/2005	4712453	4/1/2011	In Force	Voltage Regulator with Signal Generator Having High Output Imped
MP0481	MP0481TW	TW	94112632	4/20/2005	I388963	3/11/2013	In Force	A Feedback Protection Integrated Circuit Of A Voltage Regulator
MP0481	MP0481TW2	TW	94132733	4/20/2005	I370341	8/11/2012	In Force	INTEGRATED CIRCUIT WITH SIGNAL GENERATOR HAVING HIGH OUT
MP0483	MP0483	US	10/993,106	11/18/2004	7583459	9/1/2009	In Force	METHOD AND APPARATUS FOR WRITE PRECOMPENSATION IN A M.
MP0483	MP0483C1C1	US	13/012,422	1/24/2011	8358478	1/22/2013	In Force	METHOD AND APPARATUS FOR WRITE PRECOMPENSATION IN A M.
MP0484	MP0484	US	10/865,732	6/10/2004	7730335	6/1/2010	In Force	Low Power Computer With Main And Auxiliary Processors
MP0484	MP0484ACN1	CN	200510073223.:	5/20/2005	ZL2005100732	5/5/2010	In Force	SEPARATE OS STORED IN SEPARATE NV MEMORY AND EXECUTED DU
MP0484	MP0484C1	US	12/787,478	5/26/2010	8572416	10/29/2013	In Force	Low Power Computer With Main And Auxiliary Processors
MP0484	MP0484C1C1	US	14/064,633	10/28/2013	8874948	10/28/2014	In Force	Apparatuses For Operating, During Respective Power Modes, Transist
MP0484	MP0484C1CN01	CN	2005100732231	5/20/2005	ZL2005100732	5/5/2010	In Force	Separate OS Stored In Separate NV Memory And Executed During HP
MP0484	MP0484CN	CN	2005100718484	5/20/2005	ZL2005100718	5/5/2010	In Force	High Power And Low Power Processors
MP0484	MP0484CN02	CN	2005100732246	5/20/2005	ZL2005100732	5/5/2010	In Force	Cache Hierarchy
MP0484	MP0484D1	US	12/151,526	5/7/2008	7827423	11/2/2010	In Force	Low Power Computer With Main And Auxiliary Processors
MP0484	MP0484D2	US	12/152,030	5/12/2008	7788514	8/31/2010	In Force	Low Power Computer With Main And Auxiliary Processors
MP0484	MP0484EP3	EP	EP05010993.3	5/20/2005	1605361	4/18/2018	In Force	Cache hierarchy
MP0484	MP0484HK2	HK	71007344	5/20/2005			Pending	Separate OS Stored In Separate NV Memory And Executed During HP
MP0484	MP0484HK3	HK	71007353	5/20/2005			Pending	Cache Hierarchy
MP0484	MP0484JP	JP	2005-144894	5/18/2005	4621540	11/5/2010	In Force	Low Power Computer With Main and Auxiliary Processors
MP0484	MP0484JP2	JP	2005-209263	5/18/2005	4740673	5/13/2011	In Force	Separate OS stored in separate NV memory and executed during HP
MP0484	MP0484JP3	JP	2005-209264	5/18/2005	4749786	5/27/2011	In Force	Cache Hierarchy
MP0484	MP0484TW3	TW	94133391	5/17/2005	I379235	12/11/2012	In Force	Cache Hierarchy
MP0484	MP0484U3DE	DE	EP05010993.3	5/20/2005	602005053831	4/18/2018	In Force	Cache hierarchy
MP0484	MP0484U3FR	FR	EP05010993.3	5/20/2005	1605361	4/18/2018	In Force	Cache hierarchy
MP0484	MP0484U3GB	GB	EP05010993.3	5/20/2005	1605361	4/18/2018	In Force	Cache hierarchy
MP0485	MP0485D1	US	11/498,958	8/2/2006	7312642	12/25/2007	In Force	CONTINUOUS, WIDE-RANGE FREQUENCY SYNTHESIS AND PHASE TRA
MP0488	MP0488	US	10/921,526	8/19/2004	7336572	2/26/2008	In Force	Detecting Sync Patterns For Optical Media

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MP0488	MP0488C1	US	12/070,365	2/15/2008	7545711	6/9/2009	In Force	Detecting Sync Patterns For Optical Media
MP0492	MP0492	US	10/876,216	6/24/2004	7558014	7/7/2009	In Force	Programmable High Pass Amplifier For Perpendicular Recording Sys
MP0492	MP0492C1	US	11/656,774	1/22/2007	7876520	1/25/2011	In Force	Programmable High Pass Amplifier For Perpendicular Recording Sys
MP0492	MP0492CN	CN	2005100775538	6/17/2005	ZL2005100775	6/24/2015	In Force	Closed-Loop Digital Control System For A DC/DC Converter
MP0492	MP0492I2CN	CN	2005100693699	5/13/2005	ZL2005100693	4/14/2010	In Force	Voltage Regulator With Signal Generator Having High Output Impec
MP0492	MP0492ICN	CN	2005100752254	5/13/2005			Pending	Voltage Regulator With Multiple Feedback
MP0492	MP0492TW	TW	94120283	6/17/2005	I381606	1/1/2013	In Force	Closed-Loop Digital Control System For A DC/DC Converter
MP0493	MP0493	US	10/929,211	8/30/2004	7414804	8/19/2008	In Force	TMR/GMR Amplifier With Input Current Compensation
MP0493	MP0493D1	US	11/652,202	1/11/2007	7529053	5/5/2009	In Force	TMR/GMR Amplifier With Input Current Compensation
MP0493	MP0493D2	US	11/652,184	1/11/2007	7359136	4/15/2008	In Force	TMR/GMR Amplifier With Input Current Compensation
MP0493	MP0493D3	US	11/652,183	1/11/2007	7289286	10/30/2007	In Force	TMR/GMR Amplifier With Input Current Compensation
MP0493	MP0493D4	US	11/652,701	1/11/2007	7532427	5/12/2009	In Force	TMR/GMR Amplifier With Input Current Compensation
MP0494	MP0494	US	10/832,783	4/27/2004	8310927	11/13/2012	In Force	PRIORITY SCHEME FOR CONTROL TRAFFIC IN NETWORK SWITCHES
MP0495	MP0495	US	10/836,661	4/30/2004	7593320	9/22/2009	In Force	FAILOVER SCHEME FOR STACKABLE NETWORK SWITCHES
MP0497	MP0497	US	10/929,214	8/30/2004	7183832	2/27/2007	In Force	Level Shifter With Boost And Attenuation Programming
MP0497	MP0497C1	US	11/710,362	2/23/2007	7417484	8/26/2008	In Force	Level Shifter With Boost And Attenuation Programming
MP0503	MP0503	US	11/006,189	12/6/2004	8228110	7/24/2012	In Force	LOW POWER, LOW VOLTAGE PHASE INTERPOLATOR
MP0503	MP0503C1	US	13/550,514	7/16/2012	8536927	9/17/2013	In Force	LOW POWER, LOW VOLTAGE PHASE INTERPOLATOR
MP0505	MP0505	US	10/866,326	6/9/2004	8465853	6/18/2013	In Force	Glassy Metal Disk
MP0505	MP0505CN	CN	2005100088155	2/23/2005	ZL2005100088	8/24/2011	In Force	Glassy Metal Disk
MP0505	MP0505EP	EP	05004127.6	2/25/2005			Pending	Glassy Metal Disk
MP0505	MP0505I1EP	EP	EP07794750.5	5/10/2007	2016476	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive with Flash Interfac
MP0505	MP0505I1EPDE	DE	EP07794750.5	5/10/2007	602007058795	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive with Flash Interfac
MP0505	MP0505I1EPFR	FR	EP07794750.5	5/10/2007	2016476	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive with Flash Interfac
MP0505	MP0505I1EPGB	GB	EP07794750.5	5/10/2007	2016476	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive with Flash Interfac
MP0505	MP0505JP	JP	2005-052133	2/25/2005	4384069	10/2/2009	In Force	GLASSY METAL DISK
MP0505	MP0505TW	TW	94103780	2/4/2005	I397066	5/21/2013	In Force	Glassy Metal Disk
MP0506	MP0506	US	10/829,131	4/21/2004	7768959	8/3/2010	In Force	All-in-One Wireless Network Device
MP0506	MP0506C1	US	12/845,315	7/28/2010	8018892	9/13/2011	In Force	All-in-One Wireless Network Device
MP0506	MP0506C1aa	US	14/076,732	11/11/2013	9195485	11/24/2015	In Force	Method And Apparatus For Concurrently Maintaining Queues For Mu
MP0506	MP0506C1C1	US	13/230,595	9/12/2011	8582507	11/12/2013	In Force	All-in-One Wireless Network Device
MP0509	MP0509	US	10/888,307	7/9/2004	7636338	12/22/2009	In Force	ACCESS POINT CONFIGURATION
MP0509	MP0509C1	US	12/613,211	11/5/2009	7990938	8/2/2011	In Force	ACCESS POINT CONFIGURATION
MP0513	MP0513	US	10/891,672	7/15/2004	7522899	4/21/2009	In Force	IMAGE REJECTION SCHEME FOR RECEIVERS

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MP0513	MP0513C1	US	12/273,051	11/18/2008	8175565	5/8/2012	In Force	IMAGE REJECTION SCHEME FOR RECEIVERS
MP0514	MP0514	US	10/942,761	9/16/2004	7688968	3/30/2010	In Force	Adaptive Analog Echo/Next Cancellation
MP0514	MP0514Aaa	US	13/476,418	5/21/2012	8712034	4/29/2014	In Force	Adaptive Analog Echo/Next Cancellation
MP0514	MP0514C1	US	12/748,987	3/29/2010	8077859	12/13/2011	In Force	Adaptive Analog Echo/Next Cancellation
MP0514	MP0514D1	US	12/215,956	7/1/2008	8184802	5/22/2012	In Force	Adaptive Analog Echo/Next Cancellation
MP0514	MP0514D1aa	US	14/257,561	4/21/2014	9407322	8/2/2016	In Force	Adaptive Analog Echo/Next Cancellation
MP0514	MP0514D2	US	12/215,967	7/1/2008	8203975	6/19/2012	In Force	Adaptive Analog Echo/NEXT Cancellation
MP0515	MP0515	US	10/875,362	6/24/2004	8244228	8/14/2012	In Force	Method And Apparatus For Providing A Mobile Wireless Local Area
MP0515	MP0515C1	US	13/584,959	8/14/2012	8606250	12/10/2013	In Force	Method And Apparatus For Providing A Mobile Wireless Local Area
MP0516	MP0516	US	11/006,381	12/7/2004	7328395	2/5/2008	In Force	ITERATIVE REED-SOLOMON ERROR-CORRECTION DECODING
MP0516	MP0516C1	US	11/956,861	12/14/2007	7689893	3/30/2010	In Force	ITERATIVE REED-SOLOMON ERROR-CORRECTION DECODING
MP0517	MP0517	US	10/850,239	5/19/2004	7616587	11/10/2009	In Force	METHODS AND APPARATUS FOR PERFORMING REVERSE AUTO-NEG
MP0517	MP0517D1	US	12/573,498	10/5/2009	8045483	10/25/2011	In Force	METHODS AND APPARATUS FOR PERFORMING REVERSE AUTO-NEG
MP0517	MP0517D1aa	US	14/171,132	2/3/2014	9531586	12/27/2016	In Force	METHODS AND APPARATUS FOR PERFORMING REVERSE AUTO-NEG
MP0517	MP0517D1C1	US	13/280,098	10/24/2011	8665901	3/4/2014	In Force	METHODS AND APPARATUS FOR PERFORMING REVERSE AUTO-NEG
MP0518	MP0518	US	10/976,110	10/27/2004	7298570	11/20/2007	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0518	MP0518aaaa	US	13/346,665	1/9/2012	8331054	12/11/2012	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0518	MP0518C1	US	11/868,932	10/8/2007	7548389	6/16/2009	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0518	MP0518C1aa	US	13/004,805	1/11/2011	8094400	1/10/2012	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0519	MP0519	US	10/923,066	8/20/2004	8107542	1/31/2012	In Force	SOFT DECODING OF CODED BIT-STREAMS
MP0519	MP0519C1	US	13/355,349	1/20/2012	8774289	7/8/2014	In Force	SOFT DECODING OF CODED BIT-STREAMS
MP0519	MP0519C1C1	US	14/321,442	7/1/2014	9065473	6/23/2015	In Force	SOFT DECODING OF CODED BIT-STREAMS
MP0520	MP0520	US	10/949,533	9/24/2004	7499233	3/3/2009	In Force	MULTI-PHASE CONTROL INFORMATION DETECTION AND SIGNAL POL
MP0522	MP0522	US	10/995,659	11/22/2004	8294503	10/23/2012	In Force	METHOD AND APPARATUS FOR REDUCING JITTER IN A TRANSMITTE
MP0522	MP0522C1	US	13/651,205	10/12/2012	8698537	4/15/2014	In Force	METHOD AND APPARATUS FOR REDUCING JITTER IN A TRANSMITTE
MP0524	MP0524	US	10/870,579	6/16/2004	6980414	12/27/2005	In Force	CAPACITOR STRUCTURE IN A SEMICONDUCTOR DEVICE
MP0524	MP0524aAaA	US	13/192,388	7/27/2011	8537524	9/17/2013	In Force	CAPACITOR STRUCTURE IN A SEMICONDUCTOR DEVICE
MP0524	MP0524C1	US	11/179,133	7/11/2005	7116544	10/3/2006	In Force	CAPACITOR STRUCTURE IN A SEMICONDUCTOR DEVICE
MP0524	MP0524C1D1	US	11/474,246	6/23/2006	7578858	8/25/2009	In Force	MAKING CAPACITOR STRUCTURE IN A SEMICONDUCTOR DEVICE
MP0525	MP0525	US	10/865,368	6/10/2004	7634615	12/15/2009	In Force	Adaptive Storage System
MP0525	MP0525C1	US	11/489,664	7/19/2006	7512734	3/31/2009	In Force	Adaptive Storage System
MP0525	MP0525CN	CN	2005100709131	5/17/2005	ZL2005100709	9/16/2009	In Force	A Disk Drive System
MP0525	MP0525CN03	CN	200510077196.!	5/17/2005	ZL2005100772	9/16/2009	In Force	RAID SYSTEM WITH HIGH POWER AND LOW POWER DISK DRIVES
MP0525	MP0525CND1	CN	2005100771950	5/17/2005	ZL2005100771	9/10/2008	In Force	Hard Disk Drive Power Reduction Module

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MP0525	MP0525D1DE	DE	05010676.4	5/17/2005	6.02005E+12	3/26/2008	In Force	Hard Disk Drive Power Reduction Module
MP0525	MP0525D1EP	EP	05010671.5	5/17/2005	EP1605453	9/27/2017	In Force	Adaptive Storage System
MP0525	MP0525D1EPDE	DE	05010671.5	5/17/2005	602005052748	9/27/2017	In Force	Adaptive Storage System
MP0525	MP0525D1EPFR	FR	05010671.5	5/17/2005	EP1605453	9/27/2017	In Force	Adaptive Storage System
MP0525	MP0525D1EPGB	GB	05010671.5	5/17/2005	EP1605453	9/27/2017	In Force	Adaptive Storage System
MP0525	MP0525D1FR	FR	05010676.4	5/17/2005	1605456	3/26/2008	In Force	Hard Disk Drive Power Reduction Module
MP0525	MP0525D1GB	GB	05010676.4	5/17/2005	1605456	3/26/2008	In Force	Hard Disk Drive Power Reduction Module
MP0525	MP0525D1HK	HK	71007317	5/17/2005	1094259	6/19/2009	In Force	Hard Disk Drive Power Reduction Module
MP0525	MP0525D2CN	CN	2005100771946	6/14/2005			Pending	Multi-Disk Drive With High Power And Low Power Disk Drivers'
MP0525	MP0525D2HK	HK	71007326	5/17/2005			Pending	Multi-Disk Drive With High Power And Low Power Disk Drivers'
MP0525	MP0525D3CN	CN	2005100771965	5/17/2005	ZL2005100771	9/16/2009	In Force	RAID System With High Power And Low Power Disk Drives
MP0525	MP0525D3DE	DE	05010673.1	5/17/2005	602005013322	3/18/2009	In Force	RAID With High Power And Low Power Disk Drives
MP0525	MP0525D3EP	EP	EP05010673.1	5/17/2005	EP2684321	3/18/2009	In Force	RAID with High Power and Low Power Disk Drives
MP0525	MP0525D3FR	FR	05010673.1	5/17/2005	1605455	3/18/2009	In Force	RAID With High Power And Low Power Disk Drives
MP0525	MP0525D3GB	GB	05010673.1	5/17/2005	1605455	3/18/2009	In Force	RAID With High Power And Low Power Disk Drives
MP0525	MP0525D3HK	HK	71007335	5/17/2005			Pending	RAID With High Power And Low Power Disk Drives
MP0525	MP0525EPD1	EP	05010676.4	5/17/2005	EP1605456	3/26/2008	In Force	HARD DISK DRIVE POWER REDUCTION MODULE
MP0525	MP0525FaWJ	JP	2009-509863	5/10/2007			Pending	ADAPTIVE STORAGE SYSTEM INCLUDING HARD DISK DRIVE WITH FLA
MP0525	MP0525fFWO	WO	PCT/US2008/00	2/20/2008			Pending	EXTERNALLY REMOVABLE NON-VOLATILE SEMICONDUCTOR MEMOR
MP0525	MP0525fFWy	TW	97105956	2/20/2008	I472914	2/11/2015	In Force	EXTERNALLY REMOVABLE N/A-VOLATILE SEMICONDUCTOR MEMOR
MP0525	MP0525FWKR	KR	1020087029319	5/10/2007	101379940	4/1/2014	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I1	US	11/503,016	8/11/2006	7702848	4/20/2010	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I1C1	US	11/796,248	4/27/2007	7617359	11/10/2009	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I1C1WO	WO	PCT/US2007/01	5/10/2007			Pending	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I1C2	US	11/796,645	4/27/2007	7636809	12/22/2009	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I1CN	CN	200780016904.	5/10/2007	ZL2007800169	2/24/2016	In Force	ADAPTIVE STORAGE SYSTEM INCLUDING HARD DISK DRIVE WITH FLA
MP0525	MP0525I1TW	TW	96116619	5/10/2007	I426444	2/11/2014	In Force	ADAPTIVE STORAGE SYSTEM INCLUDING HARD DISK DRIVE WITH FLA
MP0525	MP0525I1WO	WO	PCT/US2007/01	5/10/2007			Pending	Adaptive Storage System Including Hard Disk Drive With Flash Interfa
MP0525	MP0525I2C1CN	CN	2007100873332	3/9/2007			Pending	System With High Power And Low Power Processors And Thread Tra
MP0525	MP0525I2C1EP	EP	70033535	2/16/2007			Pending	System With High Power And Low Power Processors And Thread Tra
MP0525	MP0525I2C1HK	HK	81068642	3/9/2007			Pending	System With High Power And Low Power Processors And Thread Tra
MP0525	MP0525I2C1JP	JP	2007059011	3/8/2007			Pending	System With High Power And Low Power Processors And Thread Tra
MP0525	MP0525I2C1SG	SG	2007011893	2/22/2007			Pending	System With High Power And Low Power Processors And Thread Tra
MP0525	MP0525I2C1TW	TW	96108295	3/9/2007			Pending	System With High Power And Low Power Processors And Thread Tra

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MP0525	MP0525I2SG01	SG	2009070632	10/21/2009			Pending	System With High Power and Low Power Processors and Thread Tran
MP0525	MP0525IWEP	EP	07794752.1	5/10/2007	2049968	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Inter
MP0525	MP0525IWEPDE	DE	07794752.1	5/10/2007	602007058800	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Inter
MP0525	MP0525IWEPFR	FR	07794752.1	5/10/2007	2049968	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Inter
MP0525	MP0525IWEPGB	GB	07794752.1	5/10/2007	2049968	7/10/2019	In Force	Adaptive Storage System Including Hard Disk Drive With Flash Inter
MP0525	MP0525IWJP	JP	2009-509862	5/10/2007			Pending	ADAPTIVE STORAGE SYSTEM INCLUDING HARD DISK DRIVE WITH FL
MP0525	MP0525JP	JP	2005-142145	5/16/2005	JP5059298	8/10/2012	In Force	DISK DRIVE SYSTEM AND DATA STORAGE SYSTEM
MP0525	MP0525JP2	JP	2005-189048	5/16/2005	JP4969803	4/13/2012	In Force	Adaptive Storage System
MP0525	MP0525JP3	JP	2005-189049	5/16/2005	JP4969804	4/13/2012	In Force	Adaptive Storage System
MP0525	MP0525JP4	JP	2005-189050	5/16/2005	JP4969805	4/13/2012	In Force	Adaptive Storage System
MP0525	MP0525TW2	TW	94143636	5/6/2005	I363293	5/1/2012	In Force	Multi-Disk Drive With High Power And Low Power Disk Drivers'
MP0525	MP0525TW3	TW	94143634	5/6/2005	I417743	12/1/2013	In Force	Raid With High Power And Low Power Disk Drives
MP0526	MP0526	US	10/896,726	7/22/2004	7392464	6/24/2008	In Force	Universal Parity Encoder
MP0526	MP0526D1	US	12/156,649	6/3/2008	8230314	7/24/2012	In Force	Universal Parity Decoder
MP0526	MP0526D1C1	US	13/555,860	7/23/2012	8533577	9/10/2013	In Force	Universal Parity Encoder
MP0530	MP0530	US	10/940,855	9/13/2004	6956510	10/18/2005	In Force	METHODS, SOFTWARE, CIRCUITS AND SYSTEMS FOR CODING INFOR
MP0530	MP0530C1	US	11/157,233	6/20/2005	6995694	2/7/2006	In Force	METHODS, SOFTWARE, CIRCUITS AND SYSTEMS FOR CODING INFOR
MP0531	MP0531	US	10/958,077	10/4/2004	7606230	10/20/2009	In Force	Link Aggregation For Routed Ports
MP0531	MP0531C1	US	12/581,512	10/19/2009	8085778	12/27/2011	In Force	Link Aggregation For Routed Ports
MP0532	MP0532	US	10/944,506	9/17/2004	7403351	7/22/2008	In Force	SEQUENCE-PERMUTATION CONTROL INFORMATION DETECTION AN
MP0533	MP0533	US	10/960,551	10/6/2004	8238287	8/7/2012	In Force	METHOD AND APPARATUS FOR PROVIDING QUALITY OF SERVICE (Q
MP0533	MP0533C1	US	13/565,734	8/2/2012	8780755	7/15/2014	In Force	CONFIGURING TRANSMISSION PARAMETERS BASED ON A CONGESTI
MP0534	MP0534	US	10/912,829	8/5/2004	7408976	8/5/2008	In Force	MIMO-OFDM RECEIVER PROCESSING WITH FREQUNECY AND CHANN
MP0534	MP0534C1	US	12/179,830	7/25/2008	7796681	9/14/2010	In Force	MIMO-OFDM RECEIVER PROCESSING
MP0534	MP0534C1aa	US	13/669,167	11/5/2012	8634451	1/21/2014	In Force	MIMO-OFDM RECEIVER PROCESSING
MP0534	MP0534C1C1	US	12/876,034	9/3/2010	8306094	11/6/2012	In Force	MIMO-OFDM RECEIVER PROCESSING
MP0534	MP0534C1Za	US	14/146,665	1/2/2014	9094116	7/28/2015	In Force	MIMO-OFDM RECEIVER PROCESSING
MP0535	MP0535C1	US	11/788,571	4/20/2007	7439803	10/21/2008	In Force	Fully Differential Current-Feedback CMOS/Bipolar Operational Amplif
MP0536	MP0536	US	10/997,236	11/24/2004	7227393	6/5/2007	In Force	METHOD AND APPARATUS FOR ADAPTIVE DELAY CANCELLATION IN
MP0537	MP0537C1	US	11/616,256	12/26/2006	7551897	6/23/2009	In Force	METHOD AND APPARATUS FOR PERFORMING TRANSMIT PRE-EMPH
MP0539	MP0539	US	10/930,004	8/30/2004	7707450	4/27/2010	In Force	TIME SHARED MEMORY ACCESS
MP0540	MP0540	US	10/926,486	8/19/2004	7958292	6/7/2011	In Force	Disk Drive System On Chip With Integrated Buffer Memory And Supp
MP0540	MP0540C1	US	13/154,356	6/6/2011	8332555	12/11/2012	In Force	Disk Drive System On Chip With Integrated Buffer Memory And Supp
MP0540	MP0540C1C1	US	13/710,814	12/11/2012	8521928	8/27/2013	In Force	CIRCUIT WITH MEMORY AND SUPPORT FOR HOST ACCESSES OF STO

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MP0540	MP0540CN	CN	2005100730626	5/27/2005			Pending	Storage Controller With High Speed External Interface To Remote Buf
MP0540	MP0540D1CN	CN	2005100796731	5/27/2005			Pending	System With Host And Storage Controller
MP0540	MP0540D1EP	EP	50117522	5/31/2005			Pending	System With Host And Storage Controller
MP0540	MP0540D1HK	HK	71007362	5/27/2005			Pending	System With Host And Storage Controller
MP0540	MP0540D1JP	JP	2005221928	7/29/2005			Pending	System With Host And Storage Controller
MP0540	MP0540D1TW	TW	94142124	11/30/2005			Pending	System With Host And Storage Controller
MP0540	MP0540D2CN	CN	2005100796746	5/27/2005	ZL2005100796	3/9/2009	In Force	Bridge Circuit
MP0540	MP0540D2EP	EP	50117514	5/31/2005			Pending	Bridge Circuit
MP0540	MP0540D2HK	HK	71007371	5/27/2005			Pending	Bridge Circuit
MP0540	MP0540D2TW	TW	94142119	11/30/2005			Pending	Bridge Circuit
MP0540	MP0540D3CN	CN	2005100796750	5/27/2005			Pending	Storage Device Circuit With SATA Interface And Remote Buffering
MP0540	MP0540D3EP	EP	50117654	5/31/2005			Pending	Storage Device Circuit With SATA Interface And Remote Buffering
MP0540	MP0540D3HK	HK	71007380	5/27/2005			Pending	Storage Device Circuit With SATA Interface And Remote Buffering
MP0540	MP0540D3TW	TW	94142133	11/30/2005			Pending	Storage Device Circuit With SATA Interface And Remote Buffering
MP0540	MP0540EP	EP	50117506	5/31/2005			Pending	Storage Controller With High Speed External Interface To Remote Buf
MP0540	MP0540JP	JP	2005-159376	5/31/2005			Pending	Disk Drive System on Chip With Integrated Buffer Memory and Suppo
MP0540	MP0540JP02	JP	2005-221929	7/29/2005			Pending	Bridge Circuit
MP0540	MP0540JP03	JP	2005-221930	7/29/2005			Pending	Storage device circuit with SATA interface and remote buffering
MP0540	MP0540TW	TW	94117116	5/25/2005			Pending	Storage Controller With High Speed External Interface To Remote Buf
MP0541	MP0541	US	10/949,624	9/24/2004	7292013	11/6/2007	In Force	CIRCUITS, SYSTEMS, METHODS, AND SOFTWARE FOR POWER FACTO
MP0541	MP0541afTW	TW	98109005	3/20/2009	I470915	1/21/2015	In Force	BOOST CONVERTER AND POWER FACTOR CONTROLLER
MP0541	MP0541aFWO	WO	PCT/US2008/06	5/22/2008			Pending	Power Factor Control Systems And Methods
MP0541	MP0541C1	US	11/977,869	10/26/2007	7511460	3/31/2009	In Force	CIRCUITS, SYSTEMS, METHODS, AND SOFTWARE FOR POWER FACTO
MP0541	MP0541C1Fa	US	12/902,440	10/12/2010	8111050	2/7/2012	In Force	Power Factor Control Systems And Methods
MP0541	MP0541C1Faa	US	13/366,951	2/6/2012	8680822	3/25/2014	In Force	Power Factor Control Systems And Methods
MP0541	MP0541C1FZa	US	14/224,398	3/25/2014	9455644	9/27/2016	In Force	Method And Apparatus For Changing A Frequency Of A Switch Prior T
MP0541	MP0541C1I1	US	12/115,175	5/5/2008	7812576	10/12/2010	In Force	Power Factor Control Systems And Methods
MP0541	MP0541C1RE	US	12/813,470	6/10/2010	RE42947	11/22/2011	In Force	Circuits, Systems, Methods, And Software For Power Factor Correctio
MP0541	MP0541RE	US	12/813,467	6/10/2010	RE42946	11/22/2011	In Force	Circuits, Systems, Methods, And Software For Power Factor Correctio
MP0542	MP0542	US	10/960,378	10/6/2004	7558289	7/7/2009	In Force	METHOD AND APPARATUS FOR PROVIDING QUALITY OF SERVICE (Q
MP0542	MP0542D1	US	12/474,153	5/28/2009	8559943	10/15/2013	In Force	ACKNOWLEDGING RECEIPT OF REAL-TIME DATA
MP0542	MP0542D1C1	US	14/053,257	10/14/2013	9179365	11/3/2015	In Force	METHOD AND APPARATUS FOR PROVIDING QUALITY OF SERVICE (Q
MP0544	MP0544	US	10/934,526	9/7/2004	7477621	1/13/2009	In Force	System And Method For Estimating Bandwidth Requirements Of And
MP0544	MP0544C1	US	12/351,097	1/9/2009	7957347	6/7/2011	In Force	System And Method For Estimating Bandwidth Requirements Of And

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MP0544	MP0544C1aa	US	14/330,229	7/14/2014	9178673	11/3/2015	In Force	Dynamic Bandwidth Allocation
MP0544	MP0544C1C1	US	13/155,128	6/7/2011	8780831	7/15/2014	In Force	System And Method For Estimating Bandwidth Requirements Of An '
MP0546	MP0546C1	US	11/788,560	4/19/2007	7315189	1/1/2008	In Force	RETIMING CIRCUITS FOR PHASE-LOCKED LOOPS
MP0547	MP0547	US	10/936,348	9/7/2004	7570612	8/4/2009	In Force	Multi-Band Communications For A Single Wireless Base Station
MP0547	MP0547D1	US	12/482,573	6/11/2009	7881254	2/1/2011	In Force	Multi-Band Communications For A Single Wireless Base Station
MP0547	MP0547D1aa	US	13/711,230	12/11/2012	8971306	3/3/2015	In Force	Multi-Band Communications For A Single Wireless Base Station
MP0547	MP0547D1C1	US	13/018,926	2/1/2011	8331345	12/11/2012	In Force	Multi-Band Communications For A Single Wireless Base Station
MP0548	MP0548	US	10/962,376	10/8/2004	7653408	1/26/2010	In Force	Self-Adaptive Transmit Power Control For Wireless Network
MP0549	MP0549	US	11/030,619	1/5/2005	7209312	4/24/2007	In Force	SELF-SERVO-WRITE USING RAMP-TRACKS
MP0549	MP0549C1	US	11/739,620	4/24/2007	7522370	4/21/2009	In Force	SELF-SERVO-WRITE USING RAMP-TRACKS
MP0549	MP0549C1C1	US	12/409,428	3/23/2009	7852598	12/14/2010	In Force	SELF-SERVO-WRITE USING SLOPED TRACKS WRITTEN USING A CALIE
MP0549	MP0549I1	US	11/726,747	3/21/2007	7751144	7/6/2010	In Force	SELF-SERVO-WRITE USING RAMP-TRACKS
MP0549	MP0549I1Aa	US	13/474,657	5/17/2012	8514510	8/20/2013	In Force	SELF-SERVO-WRITE USING RAMP-TRACKS
MP0549	MP0549I1D1	US	12/813,406	6/10/2010	8184393	5/22/2012	In Force	SELF-SERVO-WRITE USING RAMP-TRACKS
MP0550	MP0550	US	10/988,318	11/12/2004	7697449	4/13/2010	In Force	ADAPTIVELY DETERMINING A DATA RATE OF PACKETIZED INFORMAT
MP0550	MP0550C1	US	12/750,531	3/30/2010	8687510	4/1/2014	In Force	ADAPTIVELY DETERMINING A DATA RATE OF PACKETIZED INFORMAT
MP0550	MP0550C1C1	US	14/223,838	3/24/2014	9369914	6/14/2016	In Force	ADAPTIVELY DETERMINING A DATA RATE OF PACKETIZED INFORMAT
MP0551	MP0551	US	10/902,299	7/29/2004	7417952	8/26/2008	In Force	Adaptive Wireless Network Multiple Access Techniques Using Traffic
MP0551	MP0551C1	US	12/229,377	8/22/2008	8121074	2/21/2012	In Force	Adaptive Wireless Network Multiple Access Techniques Using Traffic
MP0551	MP0551C1C1	US	13/400,369	2/20/2012	8774233	7/8/2014	In Force	Adaptive Wireless Network Multiple Access Techniques Using Traffic
MP0551	MP0551D1	US	12/082,437	4/11/2008	8144675	3/27/2012	In Force	Adaptive Wireless Network Multiple Access Techniques Using Traffic
MP0553	MP0553C1	US	11/602,676	11/20/2006	7408492	8/5/2008	In Force	A/D CONVERTER FOR WIDEBAND DIGITAL COMMUNICATION
MP0553	MP0553C1C1	US	12/166,028	7/1/2008	7733246	6/8/2010	In Force	A/D CONVERTER FOR WIDEBAND DIGITAL COMMUNICATION
MP0554	MP0554	US	11/028,687	1/4/2005	7395454	7/1/2008	In Force	Integrated Circuit With Integrated Debugging Mechanism For Standar
MP0556	MP0556C1	US	12/612,556	11/4/2009	7957491	6/7/2011	In Force	DIGITAL TECHNIQUE OF COMPENSATING MISMATCHES BETWEEN IN
MP0557	MP0557	US	10/961,777	10/8/2004	8131209	3/6/2012	In Force	REPEATER CONFIGURATION AND MANAGEMENT
MP0557	MP0557D1	US	13/411,014	3/2/2012	8818261	8/26/2014	In Force	CONFIGURATION OF A NETWORK DEVICE
MP0558	MP0558	US	11/026,543	12/30/2004	7421013	9/2/2008	In Force	MAXIMUM LIKELIHOOD ESTIMATION OF TIME AND FREQUENCY OFF
MP0558	MP0558C1	US	12/194,445	8/19/2008	7639733	12/29/2009	In Force	MAXIMUM LIKELIHOOD ESTIMATION OF TIME AND FREQUENCY OFF
MP0559	MP0559	US	11/046,070	1/28/2005	7607073	10/20/2009	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, RECEIVERS AND SYS
MP0560	MP0560	US	11/051,789	2/4/2005	7478314	1/13/2009	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, RECEIVERS AND SYS
MP0560	MP0560D1	US	12/330,260	12/8/2008	8201065	6/12/2012	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, RECEIVERS AND SYS
MP0560	MP0560D1aa	US	13/871,520	4/26/2013	9209837	12/8/2015	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, RECEIVERS AND SYS
MP0560	MP0560D1C1	US	13/473,165	5/16/2012	8443272	5/14/2013	In Force	METHODS, ALGORITHMS, SOFTWARE, CIRCUITS, RECEIVERS AND SYS

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MP0561	MP0561	US	10/961,004	10/7/2004	7643583	1/5/2010	In Force	HIGH-PRECISION SIGNAL DETECTION FOR HIGH-SPEED RECEIVER
MP0561	MP0561C1	US	12/621,703	11/19/2009	7949078	5/24/2011	In Force	HIGH-PRECISION SIGNAL DETECTION FOR HIGH-SPEED RECEIVER
MP0561	MP0561C1C1	US	13/107,194	5/13/2011	8144817	3/27/2012	In Force	HIGH-PRECISION SIGNAL DETECTION FOR HIGH-SPEED RECEIVER
MP0562	MP0562	US	10/978,128	10/29/2004	7542528	6/2/2009	In Force	Far End Crosstalk Mitigation In Multiple-Input Multiple-Output (MI
MP0563	MP0563	US	11/002,786	12/1/2004	7596182	9/29/2009	In Force	OPTIMUM SYMBOL TIMING ESTIMATION WITH VARIOUS PERFORM
MP0565	MP0565	US	11/031,984	1/7/2005	8036123	10/11/2011	In Force	Integrated Circuit For Network Stress Testing
MP0565	MP0565C1	US	13/267,248	10/6/2011	8854961	10/7/2014	In Force	Integrated Circuit For Network Stress Testing
MP0566	MP0566	US	11/189,458	7/26/2005	7250784	7/31/2007	In Force	Integrated Systems Testing
MP0566	MP0566C1	US	11/243,661	10/5/2005	7253652	8/7/2007	In Force	Integrated Systems Testing
MP0566	MP0566C1D1	US	11/601,170	11/17/2006	7808262	10/5/2010	In Force	Integrated Systems Testing
MP0566	MP0566C2	US	11/243,697	10/5/2005	7250751	7/31/2007	In Force	Integrated Systems Testing
MP0566	MP0566C3	US	11/243,660	10/5/2005	7439729	10/21/2008	In Force	Integrated Systems Testing
MP0566	MP0566C4	US	11/888,214	7/31/2007	7873766	1/18/2011	In Force	Integrated Systems Testing
MP0566	MP0566C4aa	US	13/762,682	2/8/2013	8624613	1/7/2014	In Force	Integrated Systems Testing
MP0566	MP0566C4C1	US	13/006,958	1/14/2011	8373422	2/12/2013	In Force	Integrated Systems Testing
MP0566	MP0566C4Za	US	14/147,206	1/3/2014	9013198	4/21/2015	In Force	Systems And Methods For Testing Components Of A Hard Disk Drive
MP0567	MP0567C1	US	12/728,583	3/22/2010	7949833	5/24/2011	In Force	Transparent Level 2 Cache Controller
MP0568	MP0568	US	10/974,388	10/27/2004	7697688	4/13/2010	In Force	Pipelined Packet Encapsulation And Decapsulation For Temporal Key
MP0568	MP0568C1	US	12/758,865	4/13/2010	8208632	6/26/2012	In Force	Pipelined Packet Encapsulation And Decapsulation For Temporal Key
MP0568	MP0568C1C1	US	13/533,567	6/26/2012	8577037	11/5/2013	In Force	Pipelined Packet Encapsulation And Decapsulation For Temporal Key
MP0569	MP0569	US	11/166,548	6/23/2005	7571372	8/4/2009	In Force	METHODS AND ALGORITHMS FOR JOINT CHANNEL-CODE DECODING
MP0569	MP0569D1	US	12/498,320	7/6/2009	8291290	10/16/2012	In Force	METHODS AND ALGORITHMS FOR JOINT CHANNEL-CODE DECODING
MP0569	MP0569D1C1	US	13/608,390	9/10/2012	8516332	8/20/2013	In Force	METHODS AND ALGORITHMS FOR JOINT CHANNEL-CODE DECODING
MP0573	MP0573	US	11/115,578	4/26/2005	7295061	11/13/2007	In Force	FULLY INTEGRATED PROGRAMMABLE GAIN CHOPPER AMPLIFIER WI
MP0574	MP0574	US	11/030,002	1/4/2005	7545057	6/9/2009	In Force	RELAY CIRCUITRY AND SWITCHING CIRCUITRY FOR POWER-OVER-NE
MP0574	MP0574C1	US	12/465,523	5/13/2009	7915764	3/29/2011	In Force	RELAY CIRCUITRY AND SWITCHING CIRCUITRY FOR POWER-OVER-NE
MP0575	MP0575	US	11/078,717	3/10/2005	7664204	2/16/2010	In Force	ADAPTIVE TIMING USING CLOCK RECOVERY
MP0575	MP0575C1	US	12/690,002	1/19/2010	8077807	12/13/2011	In Force	ADAPTIVE TIMING USING CLOCK RECOVERY
MP0575	MP0575C1aa	US	13/604,383	9/5/2012	8477895	7/2/2013	In Force	ADAPTIVE TIMING USING CLOCK RECOVERY
MP0575	MP0575C1C1	US	13/323,480	12/12/2011	8265201	9/11/2012	In Force	ADAPTIVE TIMING USING CLOCK RECOVERY
MP0576	MP0576	US	11/037,733	1/18/2005	7518447	4/14/2009	In Force	Transimpedance Amplifier
MP0577	MP0577C1	US	11/077,394	3/10/2005	7657160	2/2/2010	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577C2HK	HK	61111419	8/30/2005			Pending	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577C3	US	11/077,396	3/10/2005	7702221	4/20/2010	In Force	Unified Control And Memory For A Combined DVD/HDD System

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MP0577	MP0577C3HK	HK	71032931	8/31/2005			Pending	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577CN	CN	2005100937513	8/29/2005	ZL2005100937	4/18/2012	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577CN2	CN	2005100938234	8/30/2005	ZL2005100938	5/16/2012	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577CN3	CN	200510093822X	8/30/2005	ZL2005100938	2/8/2012	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577CN4	CN	2005100979253	8/31/2005	ZL2005100979	6/22/2011	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577EP	EP	05018923.2	8/31/2005			Pending	Unified control and memory for a combined DVD/HDD system
MP0577	MP0577EP03	EP	EP05018615.4	8/26/2005			Pending	UNIFIED CONTROL AND MEMORY FOR A COMBINED DVD/HDD SYST
MP0577	MP0577EP1	EP	05018815.0	8/30/2005			Pending	Unified control and memory for a combined DVD/HDD system
MP0577	MP0577EP2	EP	05018616.2	8/26/2005			Pending	UNIFIED CONTROL AND MEMORY FOR A COMBINED DVD/HDD SYST
MP0577	MP0577HK	HK	61111393	8/29/2005			Pending	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577JP	JP	2005-252816	8/31/2005			Pending	UNIFIED CONTROL AND MEMORY FOR A COMBINED DVD/HDD SYST
MP0577	MP0577JP2	JP	2005-252814	8/31/2005			Pending	UNIFIED CONTROL AND MEMORY FOR A COMBINED DVD/HDD SYST
MP0577	MP0577JP3	JP	2005-252815	8/31/2005			Pending	UNIFIED CONTROL AND MEMORY FOR A COMBINED DVD/HDD SYST
MP0577	MP0577TW	TW	94127628	8/12/2005	I394147	4/21/2013	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577TW2	TW	94127609	8/12/2005	I383380	1/21/2013	In Force	DVD Copying System And Virtual DVD Carousel Including The Same
MP0577	MP0577TW3	TW	94127614	8/12/2005	I394146	4/21/2013	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0577	MP0577TW4	TW	94127623	8/12/2005	I394145	4/21/2013	In Force	Unified Control And Memory For A Combined DVD/HDD System
MP0581	MP0581	US	10/974,458	10/27/2004	7742594	6/22/2010	In Force	Pipelined Packet Encryption And Decryption Using Counter Mode Wit
MP0581	MP0581D1	US	12/217,928	7/10/2008	8229110	7/24/2012	In Force	Pipelined Packet Encryption And Decryption Using Counter Mode Wit
MP0581	MP0581D1D1	US	13/555,825	7/23/2012	8631233	1/14/2014	In Force	Pipelined Packet Encryption And Decryption Using Counter Mode Wit
MP0581	MP0581D2	US	14/154,464	1/14/2014	9055039	6/9/2015	In Force	System And Method For Pipelined Encryption In Wireless Network De
MP0582	MP0582	US	11/142,176	5/31/2005	7208974	4/24/2007	In Force	RAIL-TO-RAIL SOURCE FOLLOWERS
MP0582	MP0582C1	US	11/737,085	4/18/2007	7463066	12/9/2008	In Force	RAIL-TO-RAIL SOURCE FOLLOWERS
MP0584	MP0584	US	11/094,980	3/31/2005	7626938	12/1/2009	In Force	Local Area Network Switch Using Control Plane Packet Mirroring To S
MP0585	MP0585	US	11/090,418	3/25/2005	7644147	1/5/2010	In Force	Remote Network Device Management
MP0585	MP0585C1	US	12/633,137	12/8/2009	8301745	10/30/2012	In Force	Remote Network Device Management
MP0586	MP0586	US	11/101,047	4/6/2005	7502358	3/10/2009	In Force	Diffusion Bucket Scheduler For Wireless Network Devices
MP0587	MP0587	US	11/103,755	4/12/2005	7531975	5/12/2009	In Force	Adjustable Frequency PWM Driver
MP0590	MP0590	US	11/095,039	3/30/2005	7446514	11/4/2008	In Force	LINEAR REGULATOR FOR USE WITH ELECTRONIC CIRCUITS
MP0590	MP0590C1	US	12/264,118	11/3/2008	7782041	8/24/2010	In Force	LINEAR REGULATOR FOR USE WITH ELECTRONIC CIRCUITS
MP0590	MP0590C1C1	US	12/858,735	8/18/2010	8217638	7/10/2012	In Force	LINEAR REGULATOR FOR USE WITH ELECTRONIC CIRCUITS
MP0591	MP0591	US	11/034,664	1/13/2005	7764792	7/27/2010	In Force	System And Method For Encoding Data Transmitted On A Bus
MP0591	MP0591RE	US	13/558,734	7/26/2012	RE44777	2/25/2014	In Force	SYSTEM AND METHOD FOR ENCODING DATA TRANSMITTED ON A B
MP0591	MP0591RED1	US	14/031,881	9/19/2013	RE45334	1/13/2015	In Force	SYSTEM AND METHOD FOR ENCODING DATA TRANSMITTED ON A B

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MP0592	MP0592	US	11/122,444	5/5/2005	7298173	11/20/2007	In Force	Slew Rate Control Circuit For Small Computer System Interface (SCSI)
MP0592	MP0592C1	US	11/985,706	11/16/2007	7579873	8/25/2009	In Force	Slew Rate Control Circuit For Small Computer System Interface (SCSI
MP0592	MP0592C1C1	US	12/546,237	8/24/2009	7719314	5/18/2010	In Force	Slew Rate Control Circuit For Small Computer System Interface (SCS
MP0594	MP0594	US	11/092,095	3/28/2005	7511910	3/31/2009	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0594	MP0594AaA	US	13/357,406	1/24/2012	8477447	7/2/2013	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0594	MP0594D1	US	12/405,161	3/16/2009	7817368	10/19/2010	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0594	MP0594D1C1	US	12/901,087	10/8/2010	8120870	2/21/2012	In Force	ASYMMETRY CORRECTION IN READ SIGNAL
MP0595	MP0595	US	11/195,403	8/1/2005	7590923	9/15/2009	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR IDENTIFI
MP0595	MP0595C1	US	13/572,783	8/13/2012	8527850	9/3/2013	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR IDENTIFI
MP0595	MP0595D1	US	12/512,710	7/30/2009	8245118	8/14/2012	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR IDENTIFI
MP0596	MP0596	US	11/195,087	8/1/2005	7444582	10/28/2008	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR-CORREC
MP0596	MP0596C1	US	12/324,285	11/26/2008	8219894	7/10/2012	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR-CORREC
MP0596	MP0596C1C1	US	13/554,830	7/20/2012	8788916	7/22/2014	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR-CORREC
MP0596	MP0596D1	US	12/249,474	10/10/2008	8132082	3/6/2012	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR-CORRECT
MP0596	MP0596D1C1	US	13/364,802	2/2/2012	8296635	10/23/2012	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON ERROR-CORRECT
MP0597	MP0597	US	11/254,588	10/20/2005	7680053	3/16/2010	In Force	Inter-Device Flow Control
MP0597	MP0597C1	US	11/291,477	12/1/2005	7668104	2/23/2010	In Force	Inter-Device Flow Control
MP0597	MP0597C2	US	11/291,476	12/1/2005	7668103	2/23/2010	In Force	Inter-Device Flow Control
MP0597	MP0597C3	US	12/723,078	3/12/2010	7957285	6/7/2011	In Force	Inter-Device Flow Control
MP0597	MP0597C3aa	US	13/785,229	3/5/2013	8705355	4/22/2014	In Force	Network Switch And Method For Asserting Flow Control Of Frames Tr
MP0597	MP0597C3C1	US	13/154,240	6/6/2011	8391144	3/5/2013	In Force	Inter-Device Flow Control
MP0598	MP0598	US	11/124,997	5/9/2005	7135996	11/14/2006	In Force	Multispeed Communications Device
MP0598	MP0598aaa	US	12/356,155	1/20/2009	7839309	11/23/2010	In Force	Multispeed Communications Device
MP0598	MP0598C1C1	US	11/801,316	5/9/2007	7479906	1/20/2009	In Force	Multispeed Communications Device
MP0599	MP0599	US	11/114,402	4/26/2005	7392412	6/24/2008	In Force	Low Power Mode For A Network Interface
MP0599	MP0599C1	US	12/157,239	6/9/2008	7937600	5/3/2011	In Force	Low Power Mode For A Network Interface
MP0599	MP0599C1C1	US	13/099,947	5/3/2011	8286017	10/9/2012	In Force	Low Power Mode For A Network Interface
MP0600	MP0600	US	11/170,960	6/29/2005	8488458	7/16/2013	In Force	Secure Unauthenticated Virtual Local Area Network
MP0600	MP0600C1	US	13/942,126	7/15/2013	9118555	8/25/2015	In Force	Secure Unauthenticated Virtual Local Area Network
MP0601	MP0601	US	11/098,129	4/4/2005	7594127	9/22/2009	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601aWDE	DE	05025065.3	11/16/2005	602005051172	1/18/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601aWFR	FR	05025065.3	11/16/2005	1662645	1/18/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601aWGB	GB	05025065.3	11/16/2005	1662645	1/18/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601bUDE	DE	05025066.1	11/16/2005	602005052833	10/4/2017	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L

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MP0601	MP0601bUFR	FR	05025066.1	11/16/2005	1662646	10/4/2017	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L
MP0601	MP0601bUGB	GB	05025066.1	11/16/2005	1662646	10/4/2017	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY '
MP0601	MP0601C1	US	11/179,904	7/12/2005	7788510	8/31/2010	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C1EP	EP	EP05025065.3	11/16/2005	EP1662645	1/18/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C1HK	HK	71007807	11/16/2005			Pending	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C1TW	TW	94140474	11/17/2005	I400866	7/1/2013	In Force	NETWORK DEVICE
MP0601	MP0601C2	US	11/179,106	7/12/2005	7702929	4/20/2010	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C2EP	EP	EP05025066.1	11/16/2005	EP1662646	10/4/2017	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY
MP0601	MP0601C2HK	HK	71007576	11/16/2005			Pending	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C3	US	11/179,100	7/12/2005	7788509	8/31/2010	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C3EP	EP	EP05024935.8	11/15/2005	EP1662643	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601C3HK	HK	71007816	11/16/2005			Pending	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601CN	CN	200510114984.[illegible]	11/16/2005	ZL2005101149	7/18/2012	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY
MP0601	MP0601CN2	CN	200510114983.[illegible]	11/16/2005	ZL2005101149	4/13/2011	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L
MP0601	MP0601CN3	CN	200510114988.[illegible]	11/16/2005	ZL2005101149	4/13/2011	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L
MP0601	MP0601CN4	CN	200510114989.[illegible]	11/16/2005	ZL2005101149	4/13/2011	In Force	LOW VOLTAGE LOGIC OPERATION USING HIGHER VOLTAGE SUPPLY L
MP0601	MP0601EP	EP	EP05024946.5	11/15/2005	EP1662644	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601EPDE	DE	05024946.5	11/15/2005	602005051043	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601EPFR	FR	05024946.5	11/15/2005	1662644	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601EPGB	GB	05024946.5	11/15/2005	1662644	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601eUDE	DE	05024935.8	11/15/2005	602005051042	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601eUFR	FR	05024935.8	11/15/2005	1662643	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601eUGB	GB	05024935.8	11/15/2005	1662643	1/4/2017	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601HK	HK	71007790	11/16/2005			Pending	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601JP	JP	2005-332225	11/16/2005	JP4777747	7/8/2011	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601JP02	JP	2005-332226	11/16/2005	JP5095099	9/28/2012	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601JP04	JP	2005-332228	11/16/2005	JP5036171	7/13/2012	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601JP3	JP	2005-332227	11/16/2005	JP4690177	2/25/2011	In Force	Low Voltage Logic Operation Using Higher Voltage Supply Levels
MP0601	MP0601TW	TW	94140259	11/16/2005	I416856	11/21/2013	In Force	A PROCESS SYSTEM FOR PERFORMING LOAD BALANCING AND THRO
MP0601	MP0601TW3	TW	94140475	11/17/2005	I400867	7/1/2013	In Force	A PROCESSING SYSTEM AND METHOD FOR REDUCING A DIFFERENCE
MP0602	MP0602	US	11/056,753	2/11/2005	7864816	1/4/2011	In Force	Integrated Circuit For Network Delay And Jitter Testing
MP0604	MP0604	US	11/169,957	6/28/2005	7233182	6/19/2007	In Force	CIRCUITRY FOR ELIMINATING FALSE LOCK IN DELAY-LOCKED LOOPS
MP0605	MP0605	US	11/109,372	4/19/2005	8035446	10/11/2011	In Force	Natural Sampling For A Digital Class-D Power Amplifier
MP0606	MP0606	US	11/109,988	4/20/2005	7599456	10/6/2009	In Force	Input/Output Data Rate Synchronization Using First In First Out Data

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MP0606	MP0606C1	US	12/574,133	10/6/2009	7924960	4/12/2011	In Force	Input/Output Data Rate Synchronization Using First In First Out Data
MP0607	MP0607aUDE	DE	06002269.6	2/3/2006	602006049098	5/18/2016	In Force	Hard Disk Drive System For Distributing Protected Content
MP0607	MP0607aUFR	FR	06002269.6	2/3/2006	1705593	5/18/2016	In Force	Hard Disk Drive System For Distributing Protected Content
MP0607	MP0607aUGB	GB	06002269.6	2/3/2006	1705593	5/18/2016	In Force	Hard Disk Drive System For Distributing Protected Content
MP0607	MP0607C1cA	CN	201310090941	3/20/2006			Pending	DVD SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607C1CN	CN	2006100648927	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607C1EP	EP	EP06002269.6	2/3/2006	EP1705593	5/18/2016	In Force	Hard Disk Drive System for Distributing Protected Content
MP0607	MP0607C1JP	JP	2006-077577	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607C1SG	SG	2006008262	2/8/2006	126031	11/28/2008	In Force	Hard Disk Drive System For Distributing Protected Content
MP0607	MP0607C1TW	TW	95109456	3/20/2006			Pending	HARD DISK DRIVE SYSTEM FOR DISTRIBUTING PROTECTED CONTEN
MP0607	MP0607C2CN	CN	200610064893.:	3/20/2006			Pending	DVD SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607C2DE	DE	06002265.4	2/3/2006	60 2006 015 5(	7/21/2010	In Force	DVD System For Distributing Protected Content
MP0607	MP0607C2EP	EP	EP06002265.4	2/3/2006	EP1708112	7/21/2010	In Force	DVD System for Distributing Protected Content
MP0607	MP0607C2FR	FR	06002265.4	2/3/2006	1708112	7/21/2010	In Force	DVD System For Distributing Protected Content
MP0607	MP0607C2GB	GB	06002265.4	2/3/2006	1708112	7/21/2010	In Force	DVD System For Distributing Protected Content
MP0607	MP0607C2JP	JP	2006-077578	3/20/2006	JP4691462	2/25/2011	In Force	DVD System For Distributing Protected Content
MP0607	MP0607C2SG	SG	2006008247	2/8/2006	126030	10/31/2008	In Force	DVD System For Distributing Protected Content
MP0607	MP0607C2TW	TW	95109457	3/20/2006			Pending	DVD SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607CN	CN	2006100648912	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607EP	EP	60022464	2/3/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607FFa	US	11/725,387	3/19/2007	9197434	11/24/2015	In Force	Network System For Distributing Protected Content
MP0607	MP0607FFTW	TW	96109594	3/20/2007	I479847	4/1/2015	In Force	Network System For Distributing Protected Content
MP0607	MP0607FFWE	EP	07753528.4	3/3/2007	EP1997296	8/14/2013	In Force	Network System for Distributing Protected Content
MP0607	MP0607FWOJP	JP	2008503151	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607HK	HK	71029133	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607HK1	HK	71029106	3/20/2006			Pending	Hard Disk Drive System For Distributing Protected Content
MP0607	MP0607HK2	HK	71029115	3/20/2006			Pending	DVD System For Distributing Protected Content
MP0607	MP0607HK4	HK	81046658	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607I1	US	11/384,571	3/20/2006	7991887	8/2/2011	In Force	Network System For Distributing Protected Content
MP0607	MP0607I1I1WO	WO	PCT/US2007/00	3/3/2007			Pending	Network System For Distributing Protected Content
MP0607	MP0607I1TW	TW	96109593	3/20/2007	I462549	11/21/2014	In Force	Network System For Distributing Protected Content
MP0607	MP0607I1WO	WO	PCT/US2006/01	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP0607IQDE	DE	07753528.4	3/3/2007	1997296	8/14/2013	In Force	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607IQFR	FR	07753528.4	3/3/2007	1997296	8/14/2013	In Force	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT

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MP0607	MP0607IQGB	GB	07753528.4	3/3/2007	1997296	8/14/2013	In Force	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607JP	JP	2006-077576	3/20/2006			Pending	Network System For Distributing Protected Content
MP0607	MP06075G	SG	2006008270	2/8/2006	126032	1/30/2009	In Force	Network System For Distributing Protected Content
MP0607	MP0607TW	TW	95109459	3/20/2006			Pending	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607WCN4	CN	200680001352	3/20/2006	ZL2006800013	5/19/2010	In Force	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0607	MP0607WOJP	JP	2009-501509	3/3/2007	JP4778088	7/8/2011	In Force	NETWORK SYSTEM FOR DISTRIBUTING PROTECTED CONTENT
MP0608	MP0608	US	11/110,623	4/19/2005	7711060	5/4/2010	In Force	DATA STREAM INTERLEAVING WITH NON-MONOTONICALLY ARRAN
MP0608	MP0608C1	US	12/759,643	4/13/2010	8090045	1/3/2012	In Force	DATA STREAM INTERLEAVING WITH NON-MONOTONICALLY ARRAN
MP0608	MP0608C1aa	US	14/148,353	1/6/2014	8837623	9/16/2014	In Force	DATA STREAM INTERLEAVING WITH NON-MONOTONICALLY ARRAN
MP0608	MP0608C1C1	US	13/338,137	12/27/2011	8625699	1/7/2014	In Force	DATA STREAM INTERLEAVING WITH NON-MONOTONICALLY ARRAN
MP0608	MP0608C1Za	US	14/486,609	9/15/2014	8989297	3/24/2015	In Force	DATA STREAM INTERLEAVING WITH NON-MONOTONICALLY ARRAN
MP0608	MP0608ZZa	US	14/664,601	3/20/2015	9215049	12/15/2015	In Force	DATA STREAM INTERLEAVING WITH DIFFERENT ROTATION APPLIED
MP0608	MP0608ZZaa	US	14/968,489	12/14/2015	9882686	1/30/2018	In Force	Data Stream Interleaving with Different Rotations Applied to Chang
MP0609	MP0609C1C1	US	11/690,513	3/23/2007	7365510	4/29/2008	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP0612	MP0612	US	11/114,842	4/26/2005	7599391	10/6/2009	In Force	Media And Speed Independent Interface
MP0612	MP0612bAa	US	12/836,226	7/14/2010	8320400	11/27/2012	In Force	Media And Speed Independent Interface
MP0612	MP0612bAaa	US	13/685,257	11/26/2012	8565261	10/22/2013	In Force	Media And Speed Independent Interface
MP0612	MP0612bAZa	US	14/060,011	10/22/2013	8953637	2/10/2015	In Force	Media And Speed Independent Interface
MP0612	MP0612C1	US	11/155,394	6/17/2005	7710998	5/4/2010	In Force	Media And Speed Independent Interface
MP0612	MP0612C2	US	11/156,059	6/17/2005	7593416	9/22/2009	In Force	Media And Speed Independent Interface
MP0612	MP0612C2D1	US	12/152,577	5/15/2008	7760750	7/20/2010	In Force	Media And Speed Independent Interface
MP0613	MP0613	US	11/116,988	4/27/2005	7639687	12/29/2009	In Force	ENCODING SCHEME WITH ARBITRARY CONTROL SYMBOL PLACEMEN
MP0615	MP0615	US	11/034,846	1/13/2005	8347034	1/1/2013	In Force	A TRANSPARENT LEVEL 2 CACHE THAT USES INDEPENDENT TAG AND
MP0616	MP0616	US	11/176,998	7/7/2005	7375559	5/20/2008	In Force	DIFFERENTIAL COMPARATOR WITH A REPLICA INPUT STAGE TO SET T
MP0617	MP0617	US	11/274,854	11/16/2005	8139628	3/20/2012	In Force	METHOD AND DEVICE TO COMPENSATE FOR BASELINE WANDER
MP0617	MP0617C1	US	13/418,013	3/12/2012	8406351	3/26/2013	In Force	METHOD AND DEVICE TO COMPENSATE FOR BASELINE WANDER
MP0618	MP0618C1	US	11/797,196	1/5/2007	7327106	5/2/2008	In Force	SYSTEM AND PROCESS FOR UTILIZING BACK ELECTROMOTIVE FORCE
MP0618	MP0618C1AA	US	12/493,944	6/29/2009	7839102	11/23/2010	In Force	SYSTEM AND PROCESS FOR UTILIZING BACK ELECTROMOTIVE FORCE
MP0618	MP0618C1C1	US	12/026,274	5/2/2008	7560883	7/14/2009	In Force	SYSTEM AND PROCESS FOR UTILIZING BACK ELECTROMOTIVE FORCE
MP0621	MP0621	US	11/311,890	12/19/2005	7751374	7/6/2010	In Force	WLAN TDM Protocol
MP0621	MP0621bJP2	JP	2006-009289	1/17/2006	JP4841959	10/14/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621bUDE	DE	06000610.3	1/12/2006	6.02006E+12	10/24/2012	In Force	New WLAN TDM Protocol
MP0621	MP0621bUFR	FR	06000610.3	1/12/2006	1681806	10/24/2012	In Force	New WLAN TDM Protocol
MP0621	MP0621bUGB	GB	06000610.3	1/12/2006	1681806	10/24/2012	In Force	New WLAN TDM Protocol

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MP0621	MP0621C1	US	11/321,914	12/29/2005	7920530	4/5/2011	In Force	WLAN TDM IFS Time Selection Protocol
MP0621	MP0621C1/HK	HK	71023479	1/18/2006			Pending	New WLAN TDM Protocol
MP0621	MP0621C1aa	US	13/934,267	7/3/2013	9301247	3/29/2016	In Force	Wireless Local Area Network (WLAN) Time Division Multiplexed (TD
MP0621	MP0621C1CN	CN	200610001533.7	1/18/2006	ZL2006100015	6/15/2011	In Force	NEW WLAN TDM PROTOCOL
MP0621	MP0621C1DE	DE	06000609.5	1/12/2006	602006026315	12/14/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621C1EP	EP	06000609.5	1/12/2006	EP1681805	12/14/2011	In Force	New WLAN TDM protocol
MP0621	MP0621C1FR	FR	06000609.5	1/12/2006	1681805	12/14/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621C1GB	GB	06000609.5	1/12/2006	1681805	12/14/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621C1JP	JP	2006-009288	1/17/2006	JP4863718	11/18/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621C1SG	SG	2006005151	1/17/2006	124398	7/31/2008	In Force	New WLAN TDM Protocol
MP0621	MP0621C1TW	TW	95101900	1/18/2006	I385973	2/11/2013	In Force	NEW WLAN TDM PROTOCOL
MP0621	MP0621C2	US	11/321,915	12/29/2005	8483190	7/9/2013	In Force	Wireless Local Area Network (WLAN) Time Division Multiplexed (TD
MP0621	MP0621C2CN	CN	200610001532.2	1/18/2006	ZL2006100015	4/22/2015	In Force	A WIRELESS NETWORK DEVICE AND A METHOD FOR TRANSMITTINC
MP0621	MP0621C2EP	EP	06000610.3	1/12/2006	EP1681806	10/24/2012	In Force	New WLAN TDM Protocol
MP0621	MP0621C2HK	HK	71010349	1/18/2006			Pending	New WLAN TDM Protocol
MP0621	MP0621C2SG	SG	2006005193	1/17/2006	124399	1/30/2009	In Force	New WLAN TDM Protocol
MP0621	MP0621C2TW	TW	95101901	1/18/2006	I389507	3/11/2013	In Force	NEW WLAN TDM PROTOCOL
MP0621	MP0621CN	CN	200610001534.2	1/18/2006	ZL2006100015	6/15/2011	In Force	NEW WLAN TDM PROTOCOL
MP0621	MP0621DE	DE	06000608.7	1/12/2006	602006005735	3/18/2009	In Force	New WLAN TDM Protocol
MP0621	MP0621EP	EP	06000608.7	1/12/2006	EP1722511	3/18/2009	In Force	New WLAN TDM protocol
MP0621	MP0621FR	FR	06000608.7	1/12/2006	1722511	3/18/2009	In Force	New WLAN TDM Protocol
MP0621	MP0621GB	GB	06000608.7	1/12/2006	1722511	3/18/2009	In Force	New WLAN TDM Protocol
MP0621	MP0621JP	JP	2006-009287	1/17/2006	JP4829620	9/22/2011	In Force	New WLAN TDM Protocol
MP0621	MP0621SG	SG	2006005144	1/17/2006	124397	7/31/2008	In Force	New WLAN TDM Protocol
MP0621	MP0621TW	TW	95101898	1/18/2006	I424714	1/21/2014	In Force	WIRELESS NETWORK DEVICE,WIRELESS NETWORK COMPRISING A PL
MP0624	MP0624C1	US	12/950,722	11/19/2010	8452001	5/28/2013	In Force	Class A-B Line Driver For Gigabit Ethernet
MP0625	MP0625C1CN	CN	200610065143.8	3/21/2006	ZL2006100651	9/5/2012	In Force	WIRELESS PHONE SYSTEM
MP0625	MP0625C1EP	EP	60057643	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625C1JP	JP	2006-046004	2/22/2006	JP5208370	3/1/2013	In Force	WIRELESS PHONE SYSTEM
MP0625	MP0625C1SG	SG	2006014328	3/7/2006	126825	10/31/2008	In Force	Wireless Phone System With Wireless Modes For Short Range And Lo
MP0625	MP0625C1TW	TW	95109636	3/21/2006	I391019	3/21/2013	In Force	WIRELESS PHONE SYSTEM
MP0625	MP0625C2CN	CN	2006100651440	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625C2EP	EP	60057627	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625C2JP	JP	200646005	2/22/2006			Pending	Wireless Phone System

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MP0625	MP0625C2JP02	JP	2006-046005	2/22/2006			Pending	WIRELESS PHONE SYSTEM
MP0625	MP0625C2SG	SG	200601359 3	3/7/2006	126824	12/31/2008	In Force	Wireless Phone System With Wireless Modes For Short Range And I
MP0625	MP0625C2TW	TW	95109633	3/21/2006			Pending	WIRELESS PHONE SYSTEM
MP0625	MP0625EP	EP	60057601	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625HK	HK	71029124	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625HK1	HK	71029142	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625HK2	HK	71029151	3/21/2006			Pending	Wireless Phone System
MP0625	MP0625JP	JP	2006-046003	2/22/2006	JP4963845	4/6/2012	In Force	WIRELESS PHONE SYSTEM
MP0626	MP0626	US	11/135,557	5/23/2005	7610013	10/27/2009	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626/HK	HK	71039907	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626/HK2	HK	71039890	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626/HK3	HK	71039881	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626aaaJP	JP	2012139821	6/21/2012			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626CN	CN	200610057074.4	3/17/2006	ZL200610057074	11/21/2012	In Force	WIRELESS AUDIO FOR ENTERAINMENT SYSTEMS
MP0626	MP0626CN01	CN	200610057075.9	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626CN02	CN	200610057069.3	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626EP	EP	EP06005402	3/16/2006	EP1717995	5/6/2015	In Force	Wireless Audio and Video for Entertainment Systems
MP0626	MP0626EP01	EP	60053840	3/16/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626EP02	EP	60054038	3/16/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626EPDE	DE	06005402.0	3/16/2006	602006045335	5/6/2015	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626EPFR	FR	06005402.0	3/16/2006	1717995	5/6/2015	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626EPGB	GB	06005402.0	3/16/2006	1717995	5/6/2015	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626JP	JP	2006075365	3/17/2006	JP5089062	9/21/2012	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626JP01	JP	200675372	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626JP02	JP	200675476	3/17/2006			Pending	Wireless Audio For Entertainment Systems
MP0626	MP0626SG	SG	200601680 2	3/17/2006	126834	10/31/2008	In Force	Wireless Audio And Video For Entertainment Systems
MP0626	MP0626SG01	SG	200601679 4	3/17/2006	126833	10/31/2008	In Force	Wireless Audio For Entertainment Systems
MP0626	MP0626SG4	SG	200807794 3	3/17/2006			Pending	Wireless Audio And Video For Entertainment Systems
MP0626	MP0626TW	TW	95108996	3/16/2006	I422237	1/1/2014	In Force	WIRELESS AUDIO FOR ENTERTAINMENT SYSTEMS
MP0626	MP0626TW01	TW	95108997	3/16/2006			Pending	WIRELESS AUDIO FOR ENTERTAINMENT SYSTEMS
MP0626	MP0626TW02	TW	95108999	3/16/2006			Pending	WIRELESS AUDIO FOR ENTERTAINMENT SYSTEMS
MP0627	MP0627	US	11/112,808	4/22/2005	7560866	7/14/2009	In Force	Control System For Fluorescent Light Fixture
MP0627	MP0627/HK	HK	71039872	3/29/2006			Pending	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627/HK/01	HK	71039863	3/28/2006			Pending	Improved Control System For Fluorescent Light Fixture

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MP0627	MP0627C1	US	12/502,570	7/14/2009	8120286	2/21/2012	In Force	Control System For Fluorescent Light Fixture
MP0627	MP0627CN	CN	200610066830.2	3/29/2006	ZL200610066	5/18/2011	In Force	IMPROVED CONTROL SYSTEM FOR FLUORESCENT LIGHT FIXTURE
MP0627	MP0627CN01	CN	200610058476.(	3/28/2006	ZL200610058	6/29/2011	In Force	IMPROVED CONTROL SYSTEM FOR FLUORESCENT LIGHT FIXTURE
MP0627	MP0627DE	DE	60063070	3/27/2006	6.02006e+12	10/8/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627EP	EP	60063070	3/27/2006	1718133	10/8/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627EP01	EP	60063088	3/27/2006	1720382	9/24/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627FR	FR	60063070	3/27/2006	1718133	10/8/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627GB	GB	60063070	3/27/2006	1718133	10/8/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627I1	US	11/190,025	7/26/2005	7414369	8/19/2008	In Force	Control System For Fluorescent Light Fixture
MP0627	MP0627I1DE	DE	60063088	3/27/2006	6.02006E+12	9/24/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627I1FR	FR	60063088	3/27/2006	1720382	9/24/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627I1GB	GB	60063088	3/27/2006	1720382	9/24/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627JP	JP	2006099049	3/31/2006	JP5204379	2/22/2013	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627JP01	JP	2006098164	3/31/2006	JP4800083	8/12/2011	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627JPD1	JP	2012040573	2/27/2012	JP5379875	10/4/2013	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627SG	SG	2006018683	3/22/2006	126839	10/31/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627SG01	SG	2006018675	3/22/2006	126838	10/31/2008	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627TW	TW	95111645	3/31/2006	I405503	8/11/2013	In Force	Improved Control System For Fluorescent Light Fixture
MP0627	MP0627TW2	TW	95111643	3/31/2006	I426827	2/11/2014	In Force	Improved Control System For Fluorescent Light Fixture
MP0630	MP0630	US	11/170,904	6/29/2005	8028160	9/27/2011	In Force	Data Link Layer Switch With Protection Against Internet Protocol Spo
MP0630	MP0630C1	US	13/245,501	9/26/2011	8661241	2/25/2014	In Force	Data Link Layer Switch With Protection Against Internet Protocol Spo
MP0630	MP0630C1C1	US	14/187,662	2/24/2014	9241005	1/19/2016	In Force	METHOD AND APPARATUS FOR UPDATING TRAFFIC PATTERNS OF PA
MP0631	MP0631	US	11/180,652	7/14/2005	8081715	12/20/2011	In Force	DEVICE AND METHOD FOR SAMPLING BASED ON MATCHED FILTERIN
MP0633	MP0633	US	11/084,364	3/18/2005	8195922	6/5/2012	In Force	SYSTEM FOR DYNAMICALLY ALLOCATING PROCESSING TIME TO MUL
MP0633	MP0633C1	US	13/485,642	5/31/2012	8468324	6/18/2013	In Force	DUAL THREAD PROCESSOR
MP0633	MP0633CN	CN	200610065331.(	3/17/2006	ZL200610065	12/3/2008	In Force	DUAL THREAD PROCESSOR
MP0633	MP0633EP	EP	6005551.4	3/17/2006			Pending	DUAL THREAD PROCESSOR
MP0633	MP0633FWDE	DE	06005552.2	3/17/2006	1703375	8/26/2015	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP0633FWFR	FR	06005552.2	3/17/2006	1703375	8/26/2015	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP0633FWGB	GB	06005552.2	3/17/2006	1703375	8/26/2015	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP0633HK	HK	7100730.8	3/17/2006			Pending	DUAL THREAD PROCESSOR
MP0633	MP0633I1CN	CN	200610065528.(	3/20/2006	ZL 200610065	12/22/2010	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP0633I1EP	EP	EP06005552.2	3/17/2006	EP1703375	8/26/2015	In Force	Real-time control apparatus having a multi-thread processor
MP0633	MP0633I1HK	HK	7100729.1	3/17/2006			Pending	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES

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MP0633	MP0633I1JP	JP	2006-072084	3/16/2006	JP4410768	11/20/2009	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCES
MP0633	MP0633I1SG	SG	200601676-0	3/17/2006	126073	8/31/2009	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCE
MP0633	MP0633I1TW	TW	95108584	3/14/2006	I326428	6/21/2010	In Force	REAL-TIME CONTROL APPARATUS HAVING A MULTI-THREAD PROCE
MP0633	MP0633JP	JP	2006-071828	3/15/2006			Pending	DUAL THREAD PROCESSOR
MP0633	MP0633JPD1	JP	2007-242577	3/15/2006	JP4610593	10/22/2010	In Force	DUAL THREAD PROCESSOR
MP0633	MP0633SG	SG	200601675-2	3/17/2006	126072	3/31/2009	In Force	DUAL THREAD PROCESSOR
MP0633	MP0633TW	TW	95108580	3/14/2006	I 315848	10/11/2009	In Force	DUAL THREAD PROCESSOR
MP0634	MP0634	US	11/070,780	3/1/2005	8074056	12/6/2011	In Force	VARIABLE LENGTH PIPELINE PROCESSOR ARCHITECTURE
MP0634	MP0634C1	US	13/311,184	12/5/2011	8909903	12/9/2014	In Force	PROVIDING DATA TO REGISTERS BETWEEN EXECUTION STAGES
MP0635	MP0635	US	11/068,441	2/28/2005	7385959	6/10/2008	In Force	Power-Efficient Channel Condition Feedback For OFDM Channels
MP0635	MP0635C1	US	12/154,031	5/20/2008	8045535	10/25/2011	In Force	Power-Efficient Channel Condition Feedback For OFDM Channels
MP0635	MP0635C1aa	US	14/138,644	12/23/2013	9143216	9/22/2015	In Force	Apparatus And Method For Transitioning A Circuit For Determining
MP0635	MP0635C1C1	US	13/279,697	10/24/2011	8619656	12/31/2013	In Force	Power-Efficient Channel Condition Feedback For OFDM Channels
MP0636	MP0636	US	11/218,147	9/1/2005	7539694	5/26/2009	In Force	Concurrently Searching And Manipulating Binary Trees
MP0638	MP0638	US	11/206,384	8/17/2005	7449958	11/11/2008	In Force	OPEN LOOP DC CONTROL FOR A TRANSIMPEDANCE FEEDBACK AMPL
MP0638	MP0638C1	US	12/174,882	7/17/2008	7545219	6/9/2009	In Force	OPEN LOOP DC CONTROL FOR A TRANSIMPEDANCE FEEDBACK AMPL
MP0639	MP0639	US	11/204,069	8/15/2005	7636297	12/22/2009	In Force	Transmit Diversity Technique Based On Channel Randomization For
MP0639	MP0639C1	US	12/643,169	12/21/2009	8045450	10/25/2011	In Force	Transmit Diversity Technique Based On Channel Randomization For
MP0639	MP0639C1aa	US	14/281,368	5/19/2014	9001640	4/7/2015	In Force	Transmit Diversity Technique Based On Channel Randomization For
MP0639	MP0639C1C1	US	13/278,893	10/21/2011	8730789	5/20/2014	In Force	Transmit Diversity Technique Based On Channel Randomization For
MP0639	MP0639Zaa	US	14/679,630	4/6/2015	9246621	1/26/2016	In Force	Transmit Diversity Technique Based On Channel Randomization For
MP0640	MP0640	US	11/194,278	8/1/2005	7533240	5/12/2009	In Force	Device With Mapping Between Non-Programmable And Programmab
MP0641	MP0641C1	US	11/806,394	5/31/2007	7385364	6/10/2008	In Force	SYSTEM AND METHOD FOR DETECTING BACK ELECTROMOTIVE FORC
MP0642	MP0642	US	11/199,840	8/9/2005	7778336	8/17/2010	In Force	TIMING AND FREQUENCY SYNCHORNIZATION OF OFDM SIGNALS FO
MP0643	MP0643	US	11/218,809	9/2/2005	7529179	5/5/2009	In Force	Joint Maximum Likelihood Estimation Of Integer Carrier Frequency O
MP0645	MP0645	US	11/143,049	6/2/2005	7529178	5/5/2009	In Force	Automatic Gain Control For OFDM Receivers For Transmission Of Bur
MP0645	MP0645C1	US	12/434,924	5/4/2009	7889634	2/15/2011	In Force	Method And Apparatus For Transmitting An OFDM Signal
MP0646	MP0646	US	11/185,563	7/20/2005	7440209	10/21/2008	In Force	Communications Channel With Programmable Coupling
MP0650	MP0650	US	11/200,659	8/10/2005	7817661	10/19/2010	In Force	Dual-Media Network Interface That Automatically Disables Inactive
MP0650	MP0650C1	US	12/906,934	10/18/2010	8472470	6/25/2013	In Force	Method And Apparatus For Automatically Disabling An Interface To
MP0650	MP0650C1C1	US	13/921,987	6/19/2013	8982906	3/17/2015	In Force	Dual-Media Network Interface That Automatically Disables Inactive
MP0654	MP0654	US	11/204,484	8/16/2005	7916640	3/29/2011	In Force	Buffer Overflow Prevention For Network Devices
MP0654	MP0654C1	US	13/074,340	3/29/2011	8385204	2/26/2013	In Force	Buffer Overflow Prevention For Network Devices
MP0654	MP0654C1aa	US	14/077,819	11/12/2013	8867359	10/21/2014	In Force	Preventing Buffer Overflow By Adjusting Transmit Window Size

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MP0654	MP0654C1C1	US	13/774,379	2/22/2013	8582439	11/12/2013	In Force	Buffer Overflow Prevention For Network Devices
MP0655	MP0655	US	11/195,183	8/1/2005	7454690	11/18/2008	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON LIST DECODING
MP0655	MP0655D1	US	12/256,652	10/23/2008	7590924	9/15/2009	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON LIST DECODING
MP0655	MP0655D1D1	US	12/541,720	8/14/2009	8132083	3/6/2012	In Force	ARCHITECTURE AND CONTROL OF REED-SOLOMON LIST DECODING
MP0656	MP0656	US	11/188,661	7/26/2005	7560309	7/14/2009	In Force	Drop-In Heat Sink And Exposed Die-Back For Molded Flip Die Packa
MP0657	MP0657	US	11/171,563	6/30/2005	9047765	6/2/2015	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657C3	US	11/239,915	9/30/2005	7260472	8/21/2007	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657CN	CN	200610083623.!	5/29/2006	ZL2006100836	1/12/2011	In Force	GPS-BASED TRAFFIC MONITORING SYSTEM
MP0657	MP0657EP	EP	06011022.8	5/29/2006	EP1742191	7/6/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657EPDE	DE	06011022.8	5/29/2006	602006022865	7/6/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657EPFR	FR	06011022.8	5/29/2006	1742191	7/6/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657EPGB	GB	06011022.8	5/29/2006	1742191	7/6/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657FaTW	TW	96130864	8/21/2007	I442352	6/21/2014	In Force	GPS-BASED TRAFFIC MONITORING SYSTEM
MP0657	MP0657FFa	US	11/787,384	4/16/2007	8064931	11/22/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657FFaWO	WO	PCT/US2007/02	9/21/2007			Pending	GPS-Based Traffic Monitoring System
MP0657	MP0657FFb	US	11/787,281	4/16/2007	7885759	2/8/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657FFe	US	11/787,296	4/16/2007	7885760	2/8/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657FFeWO	WO	PCT/US2007/02	10/9/2007			Pending	GPS-Based Traffic Monitoring System
MP0657	MP0657FFh	US	11/787,350	4/16/2007	7983839	7/19/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657FFTW	TW	96130861	8/21/2007	I434238	4/11/2014	In Force	GPS-BASED TRAFFIC MONITORING SYSTEM
MP0657	MP0657I1i1	US	11/637,173	12/11/2006	7885758	2/8/2011	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657I1i1WO	WO	PCT/US2007/01	8/23/2007			Pending	GPS-Based Traffic Monitoring System
MP0657	MP0657ieTW	TW	96130868	8/21/2007	I451366	9/1/2014	In Force	GPS-Based Traffic Monitoring System, And Vehicle And Cellular Phon
MP0657	MP0657JP	JP	2006149908	5/30/2006	JP5025993	6/29/2012	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657SG	SG	2006034532	5/30/2006	128558	2/27/2009	In Force	GPS-Based Traffic Monitoring System
MP0657	MP0657TW	TW	95119042	5/29/2006	I407392	9/1/2013	In Force	GPS-Based Traffic Monitoring System
MP0658	MP0658	US	11/372,865	3/9/2006	7586199	9/8/2009	In Force	STRUCTURES, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS A
MP0658	MP0658D1	US	12/542,619	8/17/2009	7956474	6/7/2011	In Force	STRUCTURES, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS A
MP0658	MP0658D1aa	US	13/905,896	5/30/2013	8963342	2/24/2015	In Force	Structures, Architectures, Systems, Methods, Algorithms and Softwar
MP0658	MP0658D1C1	US	13/154,180	6/6/2011	8455347	6/4/2013	In Force	Structures, Architectures, Systems, Methods, Algorithms and Softwar
MP0658	MP0658I1	US	11/867,615	10/4/2007	8405220	3/26/2013	In Force	STRUCTURES, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS A
MP0658	MP0658I1C1	US	13/850,144	3/25/2013	9524927	12/20/2016	In Force	STRUCTURES, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS A
MP0659	MP0659	US	11/223,087	9/9/2005	7788570	8/31/2010	In Force	Optimized Reed-Solomon Decoder
MP0659	MP0659C1	US	11/327,934	1/9/2006	7721185	5/18/2010	In Force	Optimized Reed-Solomon Decoder

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MP0661	MP0661	US	11/196,993	8/4/2005	7548563	6/16/2009	In Force	Data Transmission Using Address Encoding
MP0661	MP0661C1	US	12/480,123	6/8/2009	8077742	12/13/2011	In Force	Data Transmission Using Address Encoding
MP0662	MP0662	US	11/196,961	8/4/2005	7826447	11/2/2010	In Force	Preventing Denial-Of-Service Attacks Employing Broadcast Packets
MP0662	MP0662C1	US	12/917,417	11/1/2010	8830997	9/9/2014	In Force	Preventing Denial-Of-Service Attacks Employing Broadcast Packets
MP0663	MP0663	US	11/388,343	3/23/2006	8022522	9/20/2011	In Force	SEMICONDUCTOR PACKAGE
MP0663	MP0663D1	US	13/234,390	9/16/2011	8482112	7/9/2013	In Force	SEMICONDUCTOR PACKAGE
MP0663	MP0663D1C1	US	13/937,051	7/8/2013	9224677	12/29/2015	In Force	SEMICONDUCTOR PACKAGE
MP0664	MP0664C1	US	12/619,761	11/17/2009	7885192	2/8/2011	In Force	Generalized Auto Media Selector
MP0664	MP0664C1C1	US	13/023,389	2/8/2011	8582434	11/12/2013	In Force	Generalized Auto Media Selector
MP0668	MP0668	US	11/247,941	10/10/2005	8265255	9/11/2012	In Force	End-To-End Session Without Signaling Protocol
MP0668	MP0668C1	US	13/608,710	9/10/2012	8817967	8/26/2014	In Force	End-To-End Session Without Signaling Protocol
MP0672	MP0672aaaa	US	13/942,172	7/15/2013	9596025	3/14/2017	In Force	Method For Providing Internet Access To Network Devices Via A Mc
MP0672	MP0672C1	US	12/711,577	2/24/2010	8036721	10/11/2011	In Force	Wearable Access Point
MP0672	MP0672C1aa	US	13/532,357	6/25/2012	8489059	7/16/2013	In Force	Wearable Access Point
MP0672	MP0672C1C1	US	13/269,766	10/10/2011	8208975	6/26/2012	In Force	Wearable Access Point
MP0674	MP0674	US	11/256,465	10/21/2005	7646718	1/12/2010	In Force	Flexible Port Rate Limiting
MP0674	MP0674C1	US	12/685,310	1/11/2010	8593969	11/26/2013	In Force	Method And Apparatus For Rate-Limiting Traffic Flow Of Packets in A
MP0675	MP0675CN	CN	200610083503.!	5/30/2006			Pending	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0675	MP0675CN03	CN	200610083505.4	5/30/2006	ZL2006100835	1/23/2013	In Force	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0675	MP0675CN2	CN	200610083502.(	5/30/2006	ZL2006100835	8/28/2013	In Force	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0675	MP0675D1EP	EP	60107612	5/24/2006	1737069	1/11/2017	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D1EPDE	DE	06010761.2	5/24/2006	602006051486	1/11/2017	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D1EPFR	FR	06010761.2	5/24/2006	1737069	1/11/2017	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D1EPGB	GB	06010761.2	5/24/2006	1737069	1/11/2017	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D2EP	EP	EP06010742.2	5/24/2006	EP1737067	12/28/2016	In Force	Wireless local area network communications module and integrated
MP0675	MP0675D2EPDE	DE	06010742.2	5/24/2006	602006051342	12/28/2016	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D2EPFR	FR	06010742.2	5/24/2006	1737067	12/28/2016	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D2EPGB	GB	06010742.2	5/24/2006	1737067	12/28/2016	In Force	Wireless Local Area Network Communications Module And Integrate
MP0675	MP0675D3EP	EP	EP06010755.4	5/24/2006	1737068	3/21/2018	In Force	Wireless Local Area Network Communications Module and Integrate
MP0675	MP0675EP	EP	EP06010553.3	5/22/2006	EP1737065	11/1/2017	In Force	Wireless Local Area Network Communications Module and Integrate
MP0675	MP0675EPDE	DE	EP06010553.3	5/22/2006	602006053975	11/1/2017	In Force	Wireless Local Area Network Communications Module and Integrate
MP0675	MP0675EPFR	FR	EP06010553.3	5/22/2006	1737065	11/1/2017	In Force	Wireless Local Area Network Communications Module and Integrate
MP0675	MP0675EPGB	GB	EP06010553.3	5/22/2006	1737065	11/1/2017	In Force	Wireless Local Area Network Communications Module and Integrate
MP0675	MP0675EUDE	DE	EP06010755.4	5/24/2006	602006054941	3/21/2018	In Force	Wireless Local Area Network Communications Module and Integrate

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MP0675	MP0675EUFR	FR	EP06010755.4	5/24/2006	1737068	3/21/2018	In Force	Wireless Local Area Network Communications Module and Integrate
MP0675	MP0675EUGB	GB	EP06010755.4	5/24/2006	1737068	3/21/2018	In Force	Wireless Local Area Network Communications Module and Integrat
MP0675	MP0675HK	HK	71054088	5/30/2006			Pending	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675HK01	HK	71054113	5/30/2006			Pending	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675HK02	HK	71054024	5/30/2006			Pending	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675HK03	HK	71054079	5/30/2006			Pending	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675JP	JP	2006158710	6/7/2006	JP4728172	4/22/2011	In Force	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675JP02	JP	2006158725	6/7/2006	JP4660428	1/7/2011	In Force	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675JP03	JP	2006158732	6/7/2006	JP4732959	4/28/2011	In Force	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675SG	SG	2006036842	6/5/2006	128568	4/30/2009	In Force	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675SG01	SG	2006036768	6/5/2006	128566	5/29/2009	In Force	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675SG02	SG	2006036750	6/5/2006	128565	1/30/2009	In Force	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675SG03	SG	2006036867	6/5/2006	128569	6/30/2009	In Force	Wireless Local Area Network Communications Module And Integrat
MP0675	MP0675TW	TW	95120024	6/6/2006	I446517	7/21/2014	In Force	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0675	MP0675TW01	TW	95120026	6/6/2006	I495278	8/1/2015	In Force	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0675	MP0675TW02	TW	95120028	6/6/2006	I438873	5/21/2014	In Force	WIRELESS LOCAL AREA NETWORK COMMUNICATIONS MODULE AND
MP0676	MP0676	US	11/214,933	8/31/2005	7312662	12/25/2007	In Force	Cascode Gain Boosting System And Method For A Transmitter
MP0676	MP0676C1	US	12/004,261	12/20/2007	7737788	6/15/2010	In Force	Cascode Gain Boosting System And Method For A Transmitter
MP0677	MP0677	US	11/227,387	9/15/2005	7782039	8/24/2010	In Force	Mixed Mode Digital Control For Switching Regulator
MP0677	MP0677C1	US	12/858,549	8/18/2010	7973524	7/5/2011	In Force	Mixed Mode Digital Control For Switching Regulator
MP0678	MP0678	US	11/351,936	2/9/2006	8149863	4/3/2012	In Force	Detection Of Strong High Frequency Sinusoidal Interference In Wirele
MP0678	MP0678C1	US	13/437,396	4/2/2012	8958437	2/17/2015	In Force	Method And Apparatus For Preventing A First Station From Transmitt
MP0679	MP0679	US	11/293,319	12/1/2005	7548047	6/16/2009	In Force	PULSE WIDTH MODULATED BUCK VOLTAGE REGULATOR WITH STAB
MP0680	MP0680	US	11/359,944	2/21/2006	8126145	2/28/2012	In Force	ENHANCED ASSOCIATION FOR ACCESS POINTS
MP0680	MP0680C1	US	13/397,536	2/15/2012	8787572	7/22/2014	In Force	ENHANCED ASSOCIATION FOR ACCESS POINTS
MP0681	MP0681	US	11/322,447	12/29/2005	7788427	8/31/2010	In Force	Flash Memory Interface For Disk Drive
MP0681	MP0681C1	US	12/861,325	8/23/2010	7979614	7/12/2011	In Force	Flash Memory/Disk Drive Interface And Method For Same
MP0686	MP0686	US	11/282,342	11/18/2005	7362192	4/22/2008	In Force	Low Noise Voltage-Controlled Oscillator
MP0687	MP0687	US	11/349,460	2/7/2006	7359261	4/15/2008	In Force	Memory Repair System And Method
MP0687	MP0687aaaa	US	13/544,482	7/9/2012	8462569	6/11/2013	In Force	Memory Repair System And Method
MP0687	MP0687aaaZ	US	13/913,676	6/10/2013	8767491	7/1/2014	In Force	Apparatus And Method For Remapping Addresses Of Defective Mem
MP0687	MP0687aaC1	US	13/114,866	5/24/2011	8218383	7/10/2012	In Force	Memory Repair System And Method
MP0687	MP0687C1	US	12/082,760	4/14/2008	7751264	7/6/2010	In Force	Memory Repair System And Method
MP0687	MP0687C1C1	US	12/827,446	6/30/2010	7948818	5/24/2011	In Force	Memory Repair System And Method

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MP0689	MP0689	US	11/291,584	11/30/2005	7646833	1/12/2010	In Force	CHANNEL EQUALIZATION IN RECEIVERS
MP0689	MP0689C1C1	US	12/916,167	10/29/2010	8059773	11/15/2011	In Force	CHANNEL EQUALIZATION IN RECEIVERS
MP0691	MP0691	US	11/269,272	11/8/2005	8909304	12/9/2014	In Force	Wireless Network With Power Save Mode
MP0692	MP0692	US	11/351,232	2/9/2006	7881755	2/1/2011	In Force	Wireless LAN Power Savings
MP0692	MP0692C1	US	13/019,110	2/1/2011	8559361	10/15/2013	In Force	Wireless LAN Power Savings
MP0692	MP0692C1C1	US	14/053,133	10/14/2013	9351247	5/24/2016	In Force	Wireless LAN Power Savings
MP0693	MP0693C1	US	12/723,122	3/12/2010	7960976	6/14/2011	In Force	Advanced Time Domain Reflection Cable Testing
MP0694	MP0694	US	11/216,843	8/31/2005	7610498	10/27/2009	In Force	Very Low Voltage Power Distribution For Mobile Devices
MP0694	MP0694C1C1JP	JP	2012172879	8/3/2012			Pending	Very Low Voltage Power Distribution For Mobile Devices
MP0694	MP0694C1JP	JP	2006144471	5/24/2006	5080026	9/7/2012	In Force	Medical Device
MP0694	MP0694CN	CN	200610078231.0	5/16/2006	ZL2006100782	5/30/2012	In Force	VERY LOW VOLTAGE POWER DISTRIBUTION FOR MOBILE DEVICES
MP0694	MP0694EP	EP	EP06009374.7	5/5/2006	EP1729203	7/6/2016	In Force	Power distribution system for mobile computing devices
MP0694	MP0694EPDE	DE	06009374.7	5/5/2006	602006049502	7/6/2016	In Force	Power Distribution System For Mobile Computing Devices
MP0694	MP0694EPFR	FR	06009374.7	5/5/2006	1729203	7/6/2016	In Force	Power Distribution System For Mobile Computing Devices
MP0694	MP0694EPGB	GB	06009374.7	5/5/2006	1729203	7/6/2016	In Force	Power Distribution System For Mobile Computing Devices
MP0694	MP0694HK	HK	71054033	5/16/2006			Pending	Very Low Voltage Power Distribution For Mobile Devices
MP0694	MP0694I	US	11/324,450	1/3/2006	7725182	5/25/2010	In Force	Power Distribution System For A Medical Device
MP0694	MP0694I1EP	EP	EP06009378.8	5/5/2006	EP1728533	3/18/2009	In Force	Medical Device
MP0694	MP0694ICN	CN	200610078232.4	5/16/2006	ZL2006100782	11/7/2012	In Force	MEDICAL DEVICE
MP0694	MP0694ISG	SG	2006031595	5/16/2006	127816	1/30/2009	In Force	Battery Power Distribution System For A Medical Device
MP0694	MP0694ITW	TW	95117464	5/17/2006	I389716	3/21/2013	In Force	MEDICAL DEVICE
MP0694	MP0694JP	JP	2006144510	5/24/2006			Pending	Very Low Voltage Power Distribution For Mobile Devices
MP0694	MP0694SG	SG	2006031579	5/16/2006	127815	12/31/2008	In Force	Very Low Voltage Power Distribution For Mobile Devices
MP0694	MP0694TW	TW	95117465	5/17/2006	I448881	8/11/2014	In Force	Very Low Voltage Power Distribution For Mobile Devices
MP0695	MP0695	US	11/328,532	1/10/2006	8045491	10/25/2011	In Force	Signal Handling For Wireless Clients
MP0695	MP0695D1	US	11/448,929	6/7/2006	8094608	1/10/2012	In Force	Method And Apparatus For Generating And Transmitting Packets On
MP0695	MP0695D2	US	11/449,415	6/8/2006	8050276	11/1/2011	In Force	Signal Handling For Wireless Clients
MP0698	MP0698	US	11/438,918	5/23/2006	7768981	8/3/2010	In Force	Bluetooth Coexistence Timing Synchronization
MP0698	MP0698C1	US	12/844,859	7/28/2010	8218516	7/10/2012	In Force	Bluetooth Coexistence Timing Synchronization
MP0698	MP0698C1aa	US	14/143,305	12/30/2013	9999011	6/12/2018	In Force	Systems And Methods For Bluetooth And WLAN Traffic Synchronizati
MP0698	MP0698C1C1	US	13/544,438	7/9/2012	8619739	12/31/2013	In Force	Bluetooth Coexistence Timing Synchronization
MP0699	MP0699	US	11/352,115	2/9/2006	7332883	2/19/2008	In Force	Method And Apparatus For Initializing Operation Of A Disk Drive
MP0699	MP0699C1	US	12/069,806	2/12/2008	7468590	12/23/2008	In Force	Method And Apparatus For Initializing Operation Of A Disk Drive
MP0699	MP0699C1C1	US	12/340,944	12/22/2008	8044616	10/25/2011	In Force	Method And Apparatus For Initializing Operation Of A Disk Drive

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MP0700	MP0700	US	11/265,074	11/1/2005	8831552	9/9/2014	In Force	RF band-pass filter with feedback control
MP0701	MP0701	US	11/326,039	1/5/2006	7423483	9/9/2008	In Force	Increasing Amplifier Bandwidth By Positive Capacitive Feedback
MP0702	MP0702	US	11/285,903	11/23/2005	7289381	10/30/2007	In Force	PROGRAMMABLE BOOSTING AND CHARGE NEUTRALIZATION
MP0703	MP0703C1	US	13/027,501	2/15/2011	8154816	4/10/2012	In Force	MAGNETO RESISTIVE VOLTAGE BIASING SCHEME WITH FAST RECOV
MP0705	MP0705	US	11/394,586	3/31/2006	7330064	2/12/2008	In Force	GEOMETRIC LADDER CIRCUIT WITH LINEAR-IN-dB TRANSFER FUNCT
MP0705	MP0705C1	US	12/030,068	2/12/2008	7557636	7/7/2009	In Force	GEOMETRIC LADDER CIRCUIT WITH LINEAR-IN-dB TRANSFER FUNCT
MP0706	MP0706	US	11/394,561	3/31/2006	7339434	3/4/2008	In Force	LINEAR-IN-dB VARIABLE GAIN AMPLIFIER USING GEOMETRIC LADDE
MP0707	MP0707	US	11/433,128	5/12/2006	7394278	7/1/2008	In Force	METHODS AND APPARATUS FOR INTEGRATED CIRCUIT LOOPBACK T
MP0707	MP0707C1	US	12/027,895	2/7/2008	7868639	1/11/2011	In Force	METHODS AND APPARATUS FOR INTEGRATED CIRCUIT LOOPBACK T
MP0709	MP0709	US	11/305,875	12/16/2005	7583649	9/1/2009	In Force	Rate Adaptation
MP0709	MP0709aaa	US	13/657,968	10/23/2012	8787405	7/22/2014	In Force	Device And Method For Adapting Transmission Parameters Based C
MP0709	MP0709C1	US	12/550,841	8/31/2009	7881275	2/1/2011	In Force	Rate Adaptation
MP0709	MP0709C1C1	US	13/019,127	2/1/2011	8295259	10/23/2012	In Force	Rate Adaptation
MP0710	MP0710	US	11/355,491	2/16/2006	7653038	1/26/2010	In Force	Dual MAC Arbitration
MP0710	MP0710C1	US	12/693,184	1/25/2010	7773573	8/10/2010	In Force	Dual MAC Arbitration
MP0710	MP0710WAKR	KR	1020137011300	2/13/2007	101309754	9/11/2013	In Force	Dual MAC Arbitration
MP0710	MP0710WO	WO	PCT/US2007/00	2/13/2007			Pending	Dual MAC Arbitration
MP0710	MP0710WOCN	CN	200780012726.!	2/13/2007	ZL2007800127	5/30/2012	In Force	DUAL MAC ARBITRATION
MP0710	MP0710WOEP	EP	EP07750547.7	2/13/2007	EP1994720	10/24/2012	In Force	Dual MAC Arbitration
MP0710	MP0710WOJP	JP	2008-555302	2/13/2007	JP4859935	11/11/2011	In Force	DUAL MAC ARBITRATION
MP0710	MP0710WOKR	KR	1020087022646	2/13/2007	101297819	8/12/2013	In Force	Dual MAC Arbitration
MP0710	MP0710WUDE	DE	07750547.7	2/13/2007	1994720	10/24/2012	In Force	DUAL MAC ARBITRATION
MP0710	MP0710WUFR	FR	07750547.7	2/13/2007	1994720	10/24/2012	In Force	DUAL MAC ARBITRATION
MP0710	MP0710WUGB	GB	07750547.7	2/13/2007	1994720	10/24/2012	In Force	DUAL MAC ARBITRATION
MP0711	MP0711	US	11/389,293	3/24/2006	8249064	8/21/2012	In Force	Remote Switching
MP0711	MP0711C1	US	13/590,455	8/21/2012	8848706	9/30/2014	In Force	Console With Network Device Based Frame Routing According To Re
MP0712	MP0712	US	11/437,560	5/19/2006	7738904	6/15/2010	In Force	SYSTEMS AND METHODS FOR CALIBRATING POWER REGULATED CO
MP0712	MP0712C1	US	12/775,694	5/7/2010	7890132	2/15/2011	In Force	SYSTEMS AND METHODS FOR CALIBRATING POWER REGULATED CO
MP0714	MP0714	US	11/355,459	2/15/2006	7522369	4/21/2009	In Force	CIRCUITS, ARCHITECTURES, APPARATUS, SYSTEMS, AND METHODS F
MP0716	MP0716	US	11/361,930	2/24/2006	7644340	1/5/2010	In Force	General Convolutional Interleaver And Deinterleaver
MP0716	MP0716C1	US	12/645,593	12/23/2009	8132076	3/6/2012	In Force	Method And Apparatus For Interleaving Portions Of A Data Block In A
MP0717	MP0717	US	11/345,885	2/2/2006	9225584	12/29/2015	In Force	Alternative Network Address Port Translation
MP0718	MP0718C1	US	11/449,190	6/8/2006	8223001	7/17/2012	In Force	Two Way Remote Control
MP0718	MP0718C1C1	US	13/550,271	7/16/2012	8638197	1/28/2014	In Force	Two Way Remote Control

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MP0718	MP0718C1D1JP	JP	2011209527	7/5/2006	JP5254414	4/26/2013	In Force	Two Way Remote Control
MP0718	MP0718C1JP	JP	2006185860	7/5/2006			Pending	Two Way Remote Control
MP0718	MP0718CN	CN	200610090180.[illegible]	7/3/2006	ZL2006100901	9/29/2010	In Force	TWO WAY REMOTE CONTROL
MP0718	MP0718CN1	CN	200610090179.[illegible]	7/3/2006	ZL2006100901	6/15/2011	In Force	TWO WAY REMOTE CONTROL
MP0718	MP0718EP	EP	EP06013123.2	6/26/2006	EP1746555	8/10/2011	In Force	Two Way Remote Control
MP0718	MP0718EP01	EP	EP06013122.4	6/26/2006	EP1746554	8/24/2011	In Force	Two Way Remote Control
MP0718	MP0718EP01DE	DE	06013122.4	6/26/2006	602006023930	8/24/2011	In Force	Two Way Remote Control
MP0718	MP0718EP01FR	FR	06013122.4	6/26/2006	1746554	8/24/2011	In Force	Two Way Remote Control
MP0718	MP0718EP01GB	GB	06013122.4	6/26/2006	1746554	8/24/2011	In Force	Two Way Remote Control
MP0718	MP0718EPDE	DE	06013123.2	6/26/2006	602006023607	8/10/2011	In Force	Two Way Remote Control
MP0718	MP0718EPFR	FR	06013123.2	6/26/2006	1746555	8/10/2011	In Force	Two Way Remote Control
MP0718	MP0718EPGB	GB	06013123.2	6/26/2006	1746555	8/10/2011	In Force	Two Way Remote Control
MP0718	MP0718JP	JP	2006185881	7/5/2006			Pending	Two Way Remote Control
MP0718	MP0718JP01	JP	2011209599	7/5/2006	JP5389881	10/18/2013	In Force	Two Way Remote Control
MP0718	MP0718SG	SG	2006043038	6/28/2006	129359	2/27/2009	In Force	Two Way Remote Control
MP0718	MP0718SG01	SG	2006043020	6/28/2006	129358	2/27/2009	In Force	Two Way Remote Control
MP0718	MP0718TW	TW	95124327	7/4/2006	I392380	4/1/2013	In Force	Two Way Remote Control
MP0719	MP0719	US	11/325,760	1/4/2006	7256957	8/14/2007	In Force	CIRCUITS, SYSTEMS, AND METHODS FOR CAPACITIVE FLY HEIGHT ME
MP0719	MP0719C1	US	11/827,010	7/9/2007	7394611	7/1/2008	In Force	CIRCUITS, SYSTEMS, AND METHODS FOR CAPACITIVE FLY HEIGHT ME
MP0719	MP0719C1C1	US	12/124,123	5/20/2008	7623315	11/24/2009	In Force	CIRCUITS, SYSTEMS, AND METHODS FOR CAPACITIVE FLY HEIGHT ME
MP0720	MP0720	US	11/342,481	1/30/2006	7787390	8/31/2010	In Force	CUSTOM AUTOMATIC REMOTE MONITORING FOR NETWORK DEVICE
MP0720	MP0720C1	US	12/861,294	8/23/2010	8305923	11/6/2012	In Force	Custom Automatic Remote Monitoring For Network Devices
MP0722	MP0722	US	11/324,597	1/3/2006	7636356	12/22/2009	In Force	Processor Traffic Segregation For Network Switching And Routing
MP0722	MP0722C1	US	12/644,428	12/22/2009	8537823	9/17/2013	In Force	PROCESSOR TRAFFIC SEGREGATION FOR NETWORK SWITCHING AND
MP0743	MP0743	US	11/326,727	1/5/2006	7714748	5/11/2010	In Force	SYSTEMS AND METHODS FOR CONSTRUCTING HIGH-RATE CONSTRAI
MP0743	MP0743C1	US	12/772,891	5/3/2010	8049648	11/1/2011	In Force	SYSTEMS AND METHODS FOR CONSTRUCTING HIGH-RATE CONSTRAI
MP0744	MP0744C1CN	CN	200610098833.[illegible]	7/13/2006	ZL2006100988	10/21/2009	In Force	PACKAGING FOR HIGN SPEED INTEGEATED CIRCUITS
MP0744	MP0744C1EP	EP	60140613	7/6/2006			Pending	Packaging For High Speed Integrated Circuits
MP0744	MP0744C1HK	HK	71098344	7/13/2006			Pending	Packaging For High Speed Integrated Circuits
MP0744	MP0744C1JP	JP	2006194500	7/14/2006	JP4764277	6/17/2011	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744C1SG	SG	2006046882	7/13/2006	129384	2/27/2009	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744C1TW	TW	095125822	7/14/2006			Pending	PACKAGING FOR HIGH SPEED INTEGRATED CIRCUITS
MP0744	MP0744C2CN	CN	200610098832.[illegible]	7/13/2006	ZL2006100988	10/14/2009	In Force	PACKAGING FOR HIGH SPEED INTEGRATED CIRCUITS
MP0744	MP0744C2EP	EP	60142593	7/10/2006			Pending	Packaging For High Speed Integrated Circuits

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MP0744	MP0744C2HK	HK	71098273	7/13/2006			Pending	Packaging For High Speed Integrated Circuits
MP0744	MP0744C2JP	JP	2006194503	7/14/2006	JP5110814	10/19/2012	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744C2SG	SG	2006046858	7/13/2006	129383	2/27/2009	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744C3	US	11/472,904	6/22/2006	7884451	2/8/2011	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744C6	US	11/473,702	6/23/2006	7638870	12/29/2009	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744CN	CN	200610098834.(	7/13/2006	ZL2006100988	12/2/2009	In Force	PACKAGING FOR HIGH SPEED INTEGRATED CIRCUITS
MP0744	MP0744EP	EP	60142601	7/10/2006			Pending	Packaging For High Speed Integrated Circuits
MP0744	MP0744HK	HK	71098371	7/13/2006			Pending	Packaging For High Speed Integrated Circuits
MP0744	MP0744JP	JP	2006194499	7/14/2006	JP5149493	12/7/2012	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744SG	SG	2006046890	7/13/2006	129385	1/30/2009	In Force	Packaging For High Speed Integrated Circuits
MP0744	MP0744TW	TW	95125825	7/14/2006	I418012	12/1/2013	In Force	PACKAGING FOR HIGH SPEED INTEGRATED CIRCUITS
MP0745	MP0745	US	11/327,684	1/6/2006	7567491	7/28/2009	In Force	SLICER BIAS LOOP
MP0746	MP0746	US	11/317,537	12/23/2005	7417474	8/26/2008	In Force	CLOCK FREQUENCY DIVISION METHODS AND CIRCUITS
MP0746	MP0746C1C1	US	12/613,248	11/5/2009	8089304	1/3/2012	In Force	CLOCK FREQUENCY DIVISION METHODS AND CIRCUITS
MP0747	MP0747CN	CN	200610103283.(	7/24/2006	ZL2006101032	10/13/2010	In Force	ON-DIE HEATING CIRCUIT AND CONTROL LOOP FOR RAPID HEATING
MP0747	MP0747EP	EP	06015255.0	7/21/2006			Pending	On-die heating circuit and control loop for rapid heating of the die
MP0747	MP0747HK	HK	HK07113238.8	7/24/2006			Pending	ON-DIE HEATING CIRCUIT AND CONTROL LOOP FOR RAPID HEATING
MP0747	MP0747JP	JP	2006-205994	7/28/2006	JP4758843	6/10/2011	In Force	ON-DIE HEATING CIRCUIT AND CONTROL LOOP FOR RAPID HEATING
MP0747	MP0747SG	SG	SG200604605(	7/12/2006	SG129370	3/31/2009	In Force	ON-DIE HEATING CIRCUIT AND CONTROL LOOP FOR RAPID HEATING
MP0747	MP0747WO	WO	PCT/US2006/03(	8/1/2006			Pending	ON-DIE HEATING CIRCUIT AND CONTROL LOOP FOR RAPID HEATING
MP0748	MP0748	US	11/242,230	10/3/2005	7375597	5/20/2008	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748C1	US	12/114,479	5/2/2008	7893785	2/22/2011	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748CN	CN	200610099600.(	8/1/2006	ZL2006100996	8/10/2011	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748EP	EP	EP06016030.6	8/1/2006			Pending	Low-noise frequency fine-tuning
MP0748	MP0748HK	HK	HK07107785.7	8/1/2006			Pending	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0748	MP0748I1TW	TW	98102050	1/20/2009			Pending	Variable Capacitance With Delay Lock Loop
MP0748	MP0748I1WO	WO	PCT/US2008/08(	11/26/2008			Pending	Variable Capacitance With Delay Lock Loop
MP0748	MP0748JP	JP	2006-208096	7/31/2006	JP4316591	5/29/2009	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748SG	SG	SG200605080(	8/1/2006	SG129424	2/27/2009	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748TW	TW	95128166	8/1/2006	I337804	2/21/2011	In Force	LOW-NOISE FINE-FREQUENCY TUNING
MP0748	MP0748WO	WO	PCT/US2006/03(	8/1/2006			Pending	LOW-NOISE FINE FREQUENCY TUNING
MP0749	MP0749	US	11/242,621	10/3/2005	7292114	11/6/2007	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749C1	US	11/935,271	11/5/2007	7839228	11/23/2010	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749CN	CN	200610099599.(	8/1/2006	ZL2006100995	5/2/2012	In Force	CRYSTAL OSCILLATOR INTEGRATED CIRCUIT AND METHOD FOR GENE

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MP0749	MP0749CND1	CN	201210041799.3	8/1/2006	ZL2012100417	3/2/2016	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749EP	EP	EP06016018.1	8/1/2006	EP1753126	10/5/2011	In Force	Low-Noise High-Stability Crystal Oscillator
MP0749	MP0749EPDE	DE	06016018.1	8/1/2006	1753126	10/5/2011	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749EPFR	FR	06016018.1	8/1/2006	1753126	10/5/2011	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749EPGB	GB	06016018.1	8/1/2006	1753126	10/5/2011	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749HK	HK	HK07107784.8	8/1/2006			Pending	LOW-NOISE FINE-FREQUENCY TUNING
MP0749	MP0749JP	JP	2006-208095	7/31/2006	JP4392007	10/16/2009	In Force	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749JPD1	JP	2009-151923	6/26/2009			Pending	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0749	MP0749SG	SG	SG200605079	8/1/2006	SG129423	2/27/2009	In Force	LOW NOISE HIGH-STATBILITY CRYSTAL OSCILLATOR
MP0749	MP0749TW	TW	TW095128168	8/1/2006	TWI328339	8/1/2010	In Force	LOW NOISE HIGH-STATBILITY CRYSTAL OSCILLATOR
MP0749	MP0749WO	WO	PCT/US2006/03	8/1/2006			Pending	LOW-NOISE HIGH-STABILITY CRYSTAL OSCILLATOR
MP0750	MP0750C1	US	11/803,100	5/11/2007	8090010	1/3/2012	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (I
MP0750	MP0750C1C1	US	13/334,948	12/22/2011	8731021	5/20/2014	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (I
MP0750	MP0750CN	CN	200610099598.2	8/1/2006	ZL2006100995	9/15/2010	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING(DI
MP0750	MP0750EP	EP	06015918.3	7/31/2006	EP1750381	3/8/2017	In Force	All Digital Implementation of Clock Spectrum Spreading (Dither) for L
MP0750	MP0750EPDE	DE	06015918.3	7/31/2006	DE1750381	3/8/2017	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750EPFR	FR	06015918.3	7/31/2006	FR1750381	3/8/2017	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750EPGB	GB	06015918.3	7/31/2006	GB1750381	3/8/2017	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750HK	HK	HK07107783.9	8/1/2006			Pending	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750JP	JP	2006-207502	7/31/2006	JP4234157	12/19/2008	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750SG	SG	SG200604607	7/12/2006	SG129372	2/27/2009	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750TW	TW	95128165	8/1/2006	I318514	12/1/2009	In Force	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0750	MP0750WO	WO	PCT/US2006/03	8/1/2006			Pending	ALL DIGITAL IMPLEMENTATION OF CLOCK SPECTRUM SPREADING (DI
MP0751	MP0751	US	11/324,933	1/4/2006	7676532	3/9/2010	In Force	Processing System And Method For Transform
MP0751	MP0751C1	US	12/719,340	3/8/2010	8484273	7/9/2013	In Force	Processing System And Method For Transform
MP0752	MP0752C1	US	12/573,361	10/5/2009	8041317	10/18/2011	In Force	Radar Detection Algorithm
MP0752	MP0752CN	CN	200610098835.(	7/13/2006	ZL2006100988	2/1/2012	In Force	RADAR DETECTION APPARATUS AND METHOD THEREOF
MP0752	MP0752EP	EP	EP06014168.6	7/7/2006	EP1752787	6/13/2012	In Force	Radar Detection Apparatus and Method Thereof
MP0752	MP0752EPDE	DE	06014168.6	7/7/2006	602006030050	6/13/2012	In Force	Radar Detection Apparatus And Method Thereof
MP0752	MP0752EPFR	FR	06014168.6	7/7/2006	1752787	6/13/2012	In Force	Radar Detection Apparatus And Method Thereof
MP0752	MP0752EPGB	GB	06014168.6	7/7/2006	1752787	6/13/2012	In Force	Radar Detection Apparatus And Method Thereof
MP0752	MP0752JP	JP	2006194491	7/14/2006	JP5075370	8/31/2012	In Force	Radar Detection Apparatus And Method Thereof
MP0752	MP0752KR	KR	1020060066559	7/14/2006	101189817	10/4/2012	In Force	Radar Detection Apparatus And Method Thereof
MP0752	MP0752SG	SG	200604684	7/13/2006	130093	12/31/2009	In Force	Radar Detection Apparatus And Method Thereof

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MP0753	MP0753	US	11/352,806	2/13/2006	7693226	4/6/2010	In Force	Aggregation Over Multiple 64-66 Lanes
MP0754	MP0754WO	WO	PCT/US2007/01	5/16/2007			Pending	Packet Tunneling For Wireless Clients Using Maximum Transmissior
MP0754	MP0754WOCN	CN	200780027818	5/16/2007			Pending	PACKET TUNNELING FOR WIRELESS CLIENTS USING MAXIMUM TRA
MP0754	MP0754WOEP	EP	77949071	5/16/2007			Pending	Packet Tunneling For Wireless Clients Using Maximum Transmissior
MP0754	MP0754WOKR	KR	1020087030881	12/18/2008			Pending	Packet Tunneling For Wireless Clients Using Maximum Transmissior
MP0756	MP0756	US	11/263,186	10/31/2005	7561361	7/14/2009	In Force	Dynamic Synchronization Of Timing Signals
MP0758	MP0758C1	US	11/677,961	2/22/2007	7362063	4/22/2008	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP0758	MP0758C1C1	US	12/107,698	4/22/2008	7504789	3/17/2009	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP0760	MP0760	US	11/264,921	11/2/2005	7202733	4/10/2007	In Force	Transconductance Amplifiers With Compensation
MP0760	MP0760I1EP	EP	EP06022343.5	10/25/2006	1783894	3/29/2019	In Force	Amplifiers with Compensation
MP0760	MP0760I1EPDE	DE	EP06022343.5	10/25/2006	602006057633	3/29/2019	In Force	Amplifiers with Compensation
MP0760	MP0760I1EPFR	FR	EP06022343.5	10/25/2006	1783894	3/29/2019	In Force	Amplifiers with Compensation
MP0760	MP0760I1EPGB	GB	EP06022343.5	10/25/2006	1783894	3/29/2019	In Force	Amplifiers with Compensation
MP0760	MP0760aTW3	TW	95140239	10/31/2006	I426704	2/11/2014	In Force	AMPLIFIERS WITH COMPENSATION
MP0760	MP0760C2	US	11/292,589	12/2/2005	7199656	4/3/2007	In Force	Amplifiers With Compensation
MP0760	MP0760C3	US	11/656,783	1/22/2007	7323930	1/29/2008	In Force	Amplifiers With Compensation
MP0760	MP0760CN	CN	200610150468.4	10/31/2006	ZL2006101504	6/20/2012	In Force	AMPLIFIERS WITH COMPENSATION
MP0760	MP0760EP	EP	06022508.3	10/27/2006	1783895	10/17/2018	In Force	Transconductance Amplifiers With Compensation
MP0760	MP0760EPDE	DE	06022508.3	10/27/2006	602006056589	10/17/2018	In Force	Transconductance Amplifiers With Compensation
MP0760	MP0760EPFR	FR	06022508.3	10/27/2006	1783895	10/17/2018	In Force	Transconductance Amplifiers With Compensation
MP0760	MP0760EPGB	GB	06022508.3	10/27/2006	1783895	10/17/2018	In Force	Transconductance Amplifiers With Compensation
MP0760	MP0760HK	HK	71117573	10/31/2006			Pending	Amplifiers With Compensation
MP0760	MP0760I1	US	11/503,436	8/11/2006	7459972	12/2/2008	In Force	Amplifiers With Compensation
MP0760	MP0760I1C1	US	12/326,197	12/2/2008	7737777	6/15/2010	In Force	Amplifiers With Compensation
MP0760	MP0760IC3	CN	200610150469.9	10/31/2006	ZL2006101504	4/10/2013	In Force	AMPLIFIERS WITH COMPENSATION
MP0760	MP0760JP	JP	2006299307	11/2/2006			Pending	Amplifiers With Compensation
MP0760	MP0760I1HK	HK	71124565	10/31/2006			Pending	Amplifiers With Compensation
MP0760	MP0760I1JP	JP	2006299309	11/2/2006	JP5010239	6/8/2012	In Force	Amplifiers With Compensation
MP0760	MP0760SG	SG	2006073522	10/30/2006	131903	5/29/2009	In Force	Amplifiers With Compensation
MP0760	MP0760SG3	SG	2006073530	10/30/2006	131904	6/30/2009	In Force	Amplifiers With Compensation
MP0760	MP0760TW	TW	95140236	10/31/2006	I387199	2/21/2013	In Force	AMPLIFIERS WITH COMPENSATION
MP0761	MP0761	US	11/321,766	12/28/2005	7408313	8/5/2008	In Force	LOW POWER PREAMPLIFIER WRITER ARCHITECTURE
MP0761	MP0761C1	US	12/186,359	8/5/2008	7656111	2/2/2010	In Force	LOW POWER PREAMPLIFIER WRITER ARCHITECTURE
MP0763	MP0763	US	11/400,476	4/7/2006	7698619	4/13/2010	In Force	Erasure Forecasting And Error-Correction Strategies

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MP0763	MP0763D1	US	12/758,881	4/13/2010	8046659	10/25/2011	In Force	Erasure Forecasting And Error-Correction Strategies
MP0764	MP0764	US	11/337,360	1/23/2006	7411758	8/12/2008	In Force	Disk Drive Servo Control Using Spirals
MP0764	MP0764C1	US	12/228,163	8/8/2008	7881005	2/1/2011	In Force	Disk Drive Servo Control Using Spirals
MP0765	MP0765	US	11/372,821	3/10/2006	7577892	8/18/2009	In Force	High Speed Iterative Decoder
MP0765	MP0765C1	US	12/538,953	8/11/2009	7853855	12/14/2010	In Force	High Speed Iterative Decoder
MP0767	MP0767	US	11/449,066	6/7/2006	7861131	12/28/2010	In Force	TENSOR PRODUCT CODES CONTAINING AN ITERATIVE CODE
MP0767	MP0767C1	US	12/946,112	11/15/2010	8086945	12/27/2011	In Force	TENSOR PRODUCT CODES CONTAINING AN ITERATIVE CODE
MP0769	MP0769	US	07/285,808	12/16/1988	4935825	6/19/1990	In Force	CYLINDER DEFECT MANAGEMENT SYSTEM FOR DATA STORAGE SYS
MP0770	MP0770	US	08/020,058	2/19/1993	5249271	9/28/1993	In Force	Buffer Memory Data Flow Controller
MP0771	MP0771	US	07/736,461	7/26/1991	5313474	5/17/1994	In Force	Method And Apparatus To Determine The Log Of An Element In GF(
MP0772	MP0772	US	07/974,158	11/10/1992	5428627	6/27/1995	In Force	Method And Apparatus For Initializing An ECC Circuit
MP0773	MP0773	US	08/372,072	1/12/1995	5627695	5/6/1997	In Force	System And Method For Generating Unique Sector Identifiers For A
MP0775	MP0775CA	CA	2333386	11/24/2000			Pending	Circuit And Method For Rapid Checking Of Error Correction Codes U
MP0775	MP0775JP	JP	2000-551486	5/27/1999			Pending	Circuit And Method For Rapid Checking Of Error
MP0776	MP0776	US	09/096,709	6/12/1998	6092231	7/18/2000	In Force	Circuit And Method For Rapid Checking Of Error Correction Codes Usi
MP0777	MP0777	US	09/243,295	2/2/1999	6487631	11/26/2002	In Force	Circuit And Method For Monitoring Sector Transfers To And From Sto
MP0778	MP0778	US	09/275,629	3/24/1999	6530000	3/4/2003	In Force	Methods And Systems For Arbitrating Access To A Disk Controller Buf
MP0778	MP0778CA	CA	2364625	3/23/2000	2364625	8/7/2012	In Force	Arbitration Methods And Systems For Arbitrating Access To A Disk Co
MP0778	MP0778EPGB	GB	00918322.9	3/23/2000	1163573	8/1/2007	In Force	Methods And Systems For Arbitrating Access To A Disk Controller Buf
MP0778	MP0778JP	JP	2000607076	3/23/2000	JP4313516	5/22/2009	In Force	Arbitration Methods And Systems For Arbitrating Access To A Disk Co
MP0779	MP0779	US	09/326,851	6/7/1999	6470461	10/22/2002	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/
MP0779	MP0779CA	CA	2375672	6/1/2000	2375672	8/18/2009	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/
MP0779	MP0779JP	JP	2001-502120	6/1/2000	JP4652642	12/24/2010	In Force	Disk Drive Controller Circuit And Method For Skipping
MP0779	MP0779WOEP	EP	EP00941177.8	6/1/2000	EP1188163	8/1/2007	In Force	DISK DRIVE CONTROLLER CIRCUIT AND METHOD FOR SKIPPING DEFE
MP0779	MP0779WOEPDE	DE	00941177.8	6/1/2000	600357805	8/1/2007	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/
MP0779	MP0779WOEPFR	FR	00941177.8	6/1/2000	1188163	8/1/2007	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/
MP0779	MP0779WOEPGB	GB	00941177.8	6/1/2000	1188163	8/1/2007	In Force	Disk Drive Controller Circuit And Method For Skipping Defective And/
MP0780	MP0780	US	09/643,636	8/22/2000	6826650	11/30/2004	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780aaaa	US	12/902,364	10/12/2010	8145835	3/27/2012	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780C122	US	13/430,063	3/26/2012	8769219	7/1/2014	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780C2	US	11/509,285	8/24/2006	7484037	1/27/2009	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780CA	CA	2418334	7/17/2001	2418334	1/24/2012	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780EPDE	DE	01958970.4	7/17/2001	1344213	9/17/2008	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780EPFR	FR	01958970.4	7/17/2001	1344213	9/17/2008	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri

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MP0780	MP0780EPGB	GB	01958970.4	7/17/2001	1344213	9/17/2008	In Force	Disk Controller Configured To Perform Out Of Order Execution Of Wri
MP0780	MP0780WOEP	EP	01958970.4	7/17/2001	1344213	9/17/2008	In Force	DISK CONTROLLER CONFIGURED TO PERFORM OUT OF ORDER EXEC
MP0781	MP0781	US	10/199,911	7/19/2002	6961877	11/1/2005	In Force	System And Method For In-Line Error Correction For Storage Systen
MP0781	MP0781aaa	US	13/846,252	3/18/2013	9195538	11/24/2015	In Force	System And Method For In-Line Error Correction For Storage Systen
MP0781	MP0781C1	US	11/231,312	9/20/2005	8122299	2/21/2012	In Force	SYSTEM AND METHOD FOR IN-LINE ERROR CORRECTION FOR STOR/
MP0781	MP0781C1C1	US	13/400,321	2/20/2012	8402323	3/19/2013	In Force	System And Method For In-Line Error Correction For Storage Systen
MP0782	MP0782	US	10/384,991	3/10/2003	7457903	11/25/2008	In Force	Interrupt Controller For Processing Fast And Regular Interrupts
MP0782	MP0782C1	US	12/271,261	11/14/2008	7870320	1/11/2011	In Force	Interrupt Controller For Prioritizing Interrupt Requests In An Embed
MP0783	MP0783	US	10/385,022	3/10/2003	7080188	7/18/2006	In Force	Method And System For Embedded Disk Controllers
MP0785	MP0785	US	10/385,056	3/10/2003	7219182	5/15/2007	In Force	Method And System For Using An External Bus Controller In Embed
MP0786	MP0786	US	09/547,567	4/12/2000	6330626	12/11/2001	In Force	Systems And Methods For A Disk Controller Memory Architecture
MP0786	MP0786CA	CA	2370596	5/5/2000	2370596	1/12/2010	In Force	Systems And Methods For A Disk Controller Memory Architecture
MP0786	MP0786JP	JP	2000-615882	5/5/2000	JP4741735	5/13/2011	In Force	Systems And Methods For A Disk Controller Memory
MP0786	MP0786WOKR	KR	1020017014078	11/5/2001	100638378	10/18/2006	In Force	Systems And Methods For A Disk Controller Memory Architecture
MP0788	MP0788C1	US	11/592,813	11/3/2006	7471485	12/30/2008	In Force	Method And System For Head Position Control In Embedded Disk Dri
MP0788	MP0788C1C1	US	12/338,071	12/18/2008	8116026	2/14/2012	In Force	Method And System For Head Position Control In Embedded Disk Dri
MP0789	MP0789	US	10/384,992	3/10/2003	7492545	2/17/2009	In Force	Method And System For Automatic Time Base Adjustment For Disk D
MP0789	MP0789C1	US	12/361,121	1/28/2009	8189285	5/29/2012	In Force	Method And System For Automatic Time Base Adjustment For Disk D
MP0790	MP0790C1	US	11/388,410	3/24/2006	7336435	2/26/2008	In Force	Method And System For Collecting Servo Field Data From Programma
MP0791	MP0791C1	US	11/384,657	3/20/2006	7975110	7/5/2011	In Force	Method And System For Supporting Multiple External Serial Port Devi
MP0792	MP0792	US	11/099,746	4/6/2005	7609468	10/27/2009	In Force	Method And System For Read Gate Timing Control For Storage Contr
MP0793	MP0793	US	11/056,320	2/11/2005	7543214	6/2/2009	In Force	Method And System For Performing CRC
MP0794	MP0794	US	10/429,495	5/5/2003	7111228	9/19/2006	In Force	System And Method For Performing Parity Checks In Disk Storage Sys
MP0794	MP0794C1	US	11/521,979	9/15/2006	7559009	7/7/2009	In Force	System And Method For Performing Parity Checks In Disk Storage Sys
MP0795	MP0795	US	10/761,786	1/21/2004	7287102	10/23/2007	In Force	System And Method For Concatenating Data
MP0796	MP0796	US	10/619,954	7/15/2003	7007114	2/28/2006	In Force	SYSTEM AND METHOD FOR PADDING DATA BLOCKS AND/OR REMOV
MP0796	MP0796C1	US	11/232,437	9/21/2005	8713224	4/29/2014	In Force	System And Method For Transferring Data In Storage Controllers
MP0797	MP0797	US	10/867,113	6/14/2004	7120084	10/10/2006	In Force	Integrated Memory Controller
MP0797	MP0797C1	US	11/542,862	10/4/2006	7286441	10/23/2007	In Force	Integrated Memory Controller
MP0797	MP0797C1C1	US	11/977,169	10/23/2007	7535791	5/19/2009	In Force	Integrated Memory Controller
MP0797	MP0797D1	US	11/542,726	10/4/2006	7596053	9/29/2009	In Force	Integrated Memory Controller
MP0798	MP0798	US	10/686,151	10/15/2003	7526691	4/28/2009	In Force	System And Method For Using Tap Controllers
MP0799	MP0799	US	10/878,803	6/28/2004	8166217	4/24/2012	In Force	System And Method For Reading And Writing Data Using Storage Con
MP0800	MP0800	US	10/894,143	7/19/2004	9201599	12/1/2015	In Force	System And Method For Transmitting Data In Storage Controllers

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MP0802	MP0802	US	10/894,208	7/19/2004	8032674	10/4/2011	In Force	System And Method For Controlling Buffer Memory Overflow And Un
MP0804	MP0804	US	10/965,468	10/13/2004	7386661	6/10/2008	In Force	Power Save Module For Storage Controllers
MP0804	MP0804C1	US	12/157,212	6/9/2008	8417900	4/9/2013	In Force	Power Save Module For Storage Controllers
MP0805	MP0805C1	US	12/886,806	9/21/2010	8370541	2/5/2013	In Force	Method And System For Processing Frames In Storage Controllers
MP0806	MP0806	US	10/983,944	11/8/2004	7240267	7/3/2007	In Force	System And Method For Conducting BIST Operations
MP0806	MP0806C1	US	11/820,226	6/19/2007	8015448	9/6/2011	In Force	System And Method For Conducting BIST Operations
MP0808	MP0808	US	09/548,330	4/12/2000	6401149	6/4/2002	In Force	Methods For Context Switching Within A Disk Controller
MP0809	MP0809	US	11/293,917	12/5/2005	7439896	10/21/2008	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809C1	US	11/481,477	7/6/2006	7379012	5/27/2008	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809C2	US	11/481,476	7/6/2006	7443329	10/28/2008	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809C2SG02	SG	2006050850	8/2/2006	131002	1/29/2010	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809CN	CN	200610104264.[illegible]	8/7/2006	ZL2006101042	5/4/2011	In Force	CAPACITIVE DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVEI
MP0809	MP0809CN01	HK	71089620	8/7/2006			Pending	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809CN02	CN	200610104265.[illegible]	8/7/2006	ZL2006101042	5/4/2011	In Force	CAPACITIVE DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVER
MP0809	MP0809CN03	CN	200610104266.[illegible]	8/7/2006	ZL2006101042	4/1/2015	In Force	CAPACITIVE DIGTIAL TO ANALOG AND ANALOG TO DIGTAL CONVERT
MP0809	MP0809EP	EP	EP06016268.2	8/3/2006	EP1763141	5/31/2017	In Force	Capacitive Digital to Analog and Analog to Digital Converters
MP0809	MP0809EP2	EP	06015726.0	7/27/2006	EP1770867	5/31/2017	In Force	Capacitive Digital to Analog and Analog to Digital Converters
MP0809	MP0809EP3	EP	06016259.1	8/3/2006	EP1763140	5/10/2017	In Force	Capacitive Digital to Analog and Analog to Digital Converters
MP0809	MP0809EPDE	DE	06016268.2	8/3/2006	602006052659	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809EPFR	FR	60162682	8/3/2006	1763141	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809EPGB	GB	06016268.2	8/3/2006	1763141	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809HK	HK	71089602	8/7/2006			Pending	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809HK02	HK	71089611	8/7/2006			Pending	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809JP	JP	2006215993	8/8/2006	JP5276782	5/24/2013	In Force	Capacitive Digital To Analog And Analog to Digital Converters
MP0809	MP0809JP01	JP	2006215995	8/8/2006	JP4864594	11/18/2011	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809JP02	JP	2006215996	8/8/2006	JP4965185	4/6/2012	In Force	Capacitive Digital To Analog And Analog to Digital Converters
MP0809	MP0809SG	SG	2006050884	8/2/2006	131004	3/31/2009	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809SG01	SG	2006050868	8/2/2006	131003	12/31/2009	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809TW	TW	95128858	8/7/2006	I407699	9/1/2013	In Force	CAPACITIVE DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVER
MP0809	MP0809TW2	TW	95128859	8/7/2006	I422160	1/1/2014	In Force	CAPACITIVE DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVER
MP0809	MP0809U2DE	DE	06015726.0	7/27/2006	602006052657	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809U2FR	FR	60157260	7/27/2006	1770867	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809U2GB	GB	06015726.0	7/27/2006	1770867	5/31/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809U3DE	DE	06016259.1	8/3/2006	602006052481	5/10/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters

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MP0809	MP0809U3FR	FR	60162591	8/3/2006	1763140	5/10/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0809	MP0809U3GB	GB	06016259.1	8/3/2006	1763140	5/10/2017	In Force	Capacitive Digital To Analog And Analog To Digital Converters
MP0810	MP0810	US	11/376,601	3/14/2006	7742556	6/22/2010	In Force	CIRCUITS, METHODS, APPARATUS, AND SYSTEMS FOR RECOVERY O
MP0813	MP0813	US	11/407,417	4/20/2006	8536874	9/17/2013	In Force	Integrated Circuit Voltage Domain Detection System And Associate
MP0815	MP0815	US	11/491,021	7/20/2006	7576527	8/18/2009	In Force	LOW POWER DC-DC CONVERTER WITH IMPROVED LOAD REGULATI
MP0815	MP0815C1	US	12/507,322	7/22/2009	7990126	8/2/2011	In Force	LOW POWER DC-DC CONVERTER WITH IMPROVED LOAD REGULATI
MP0816	MP0816	US	11/453,219	6/13/2006	7715135	5/11/2010	In Force	METHODS, CIRCUITS, APPARATUS, AND SYSTEMS FOR READ CHANN
MP0817	MP0817CN	CN	200610111845.:	8/25/2006	ZL2006101118	10/6/2010	In Force	AUTOMATIC WRITE STRATEGY CALIBRATION METHOD FOR OPTICAL
MP0817	MP0817EP	EP	EP06017068.5	8/16/2006	EP1768113	2/22/2012	In Force	Automatic Write Strategy Calibration Method for Optical Drive
MP0817	MP0817EPDE	DE	06017068.5	8/16/2006	602006027716	2/22/2012	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817EPFR	FR	60170685	8/16/2006	1768113	2/22/2012	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817EPGB	GB	06017068.5	8/16/2006	1768113	2/22/2012	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817HK	HK	71098282	8/25/2006			Pending	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817JP	JP	2006231162	8/28/2006			Pending	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817JPD1	JP	2011184882	8/28/2006	5437332	12/20/2013	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817SG	SG	200605785S	8/28/2006	131018	6/30/2009	In Force	Automatic Write Strategy Calibration Method For Optical Drive
MP0817	MP0817TW	TW	95131325	8/25/2006	I395210	5/1/2013	In Force	Write Strategy Calibration System And Optical Media Playback Device
MP0818	MP0818	US	11/364,539	2/27/2006	8484178	7/9/2013	In Force	File Sharing
MP0818	MP0818C1	US	13/934,769	7/3/2013	9141638	9/22/2015	In Force	File Sharing
MP0819	MP0819C1	US	12/619,198	11/16/2009	8063670	11/22/2011	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819CN	CN	200610109921.7	8/24/2006	ZL2006101099	4/18/2012	In Force	HIGH VOLTAGE HIGH SIDE TRANSISTOR DRIVER
MP0819	MP0819EP	EP	06016937.2	8/14/2006	EP1768257	6/18/2014	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819EPDE	DE	06016937.2	8/14/2006	602006041936	6/18/2014	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819EPFR	FR	60169372	8/14/2006	1768257	6/18/2014	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819EPGB	GB	06016937.2	8/14/2006	1768257	6/18/2014	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819HK	HK	71104060	8/24/2006			Pending	High Voltage High Side Transistor Driver
MP0819	MP0819JP	JP	2006229479	8/25/2006			Pending	High Voltage High Side Transistor Driver
MP0819	MP0819SG	SG	200605783O	8/29/2006	131017	4/30/2009	In Force	High Voltage High Side Transistor Driver
MP0819	MP0819TW	TW	95131328	8/25/2006	I394368	4/21/2013	In Force	HIGH VOLTAGE HIGH SIDE TRANSISTOR DRIVER
MP0820	MP0820	US	11/433,908	5/12/2006	8018888	9/13/2011	In Force	Mobile WLAN Repeater System
MP0820	MP0820D1	US	13/230,194	9/12/2011	8159991	4/17/2012	In Force	Mobile WLAN Repeater System
MP0823	MP0823	US	11/510,007	8/25/2006	7906973	3/15/2011	In Force	Cable Tester
MP0824	MP0824	US	11/518,020	9/8/2006	7765458	7/27/2010	In Force	ERROR PATTERN GENERATION FOR TRELLIS-BASED DETECTION AND/
MP0824	MP0824C1	US	12/824,517	6/28/2010	8762824	6/24/2014	In Force	ERROR PATTERN GENERATION FOR TRELLIS-BASED DETECTION AND/

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MP0825	MP0825	US	11/481,137	7/5/2006	8310965	11/13/2012	In Force	Buffered Audio System With Synchronizing Bus Controller
MP0829	MP0829	US	11/398,387	4/4/2006	7554360	6/30/2009	In Force	HIGH SPEED LEVEL SHIFTER CIRCUIT IN ADVANCED CMOS TECHNOL
MP0830	MP0830	US	11/821,482	6/23/2007	8238889	8/7/2012	In Force	Server For Wireless Application Service System
MP0830	MP0830C1	US	13/568,617	8/7/2012	8515403	8/20/2013	In Force	Server For Wireless Application Service System
MP0830	MP0830C1C1	US	13/968,933	8/16/2013	9210531	12/8/2015	In Force	Server For Wireless Application Service System
MP0830	MP0830Za	US	14/955,619	12/1/2015	9854380	12/26/2017	In Force	System And Method For Providing One Or More Application Service
MP0831	MP0831	US	11/408,265	4/20/2006	7546103	6/9/2009	In Force	Antenna Diversity System And Method
MP0831	MP0831aaaa	US	14/289,987	5/29/2014	9419697	8/16/2016	In Force	ANTENNA DIVERSITY SYSTEM AND METHOD
MP0831	MP0831C1	US	12/480,149	6/8/2009	8014744	9/6/2011	In Force	Antenna Diversity System And Method
MP0831	MP0831C1C1	US	13/225,078	9/2/2011	8351888	1/8/2013	In Force	Antenna Diversity System And Method
MP0832	MP0832aaaa	US	13/730,462	12/28/2012	8509045	8/13/2013	In Force	Flexible Optical Write Strategy
MP0832	MP0832C1aa	US	13/405,818	2/27/2012	8345522	1/1/2013	In Force	Flexible Optical Write Strategy
MP0832	MP0832C1C1	US	13/019,011	2/1/2011	8125870	2/28/2012	In Force	Flexible Optical Write Strategy
MP0833	MP0833	US	11/417,414	5/3/2006	8482338	7/9/2013	In Force	SHOCK DETECTOR WITH DC OFFSET SUPPRESSION USING INTERNAL
MP0834	MP0834	US	11/488,437	7/17/2006	7587170	9/8/2009	In Force	DIGITAL RADIO DATA SYSTEM RECEIVER METHODS AND APPARATUS
MP0834	MP0834C1	US	12/544,567	8/20/2009	8099050	1/17/2012	In Force	DIGITAL RADIO DATA SYSTEM RECEIVER METHODS AND APPARATUS
MP0834	MP0834C1aa	US	13/681,200	11/19/2012	8594574	11/26/2013	In Force	DIGITAL RADIO DATA SYSTEM RECEIVER METHODS AND APPARATUS
MP0834	MP0834C1C1	US	13/345,650	1/6/2012	8315568	11/20/2012	In Force	DIGITAL RADIO DATA SYSTEM RECEIVER METHODS AND APPARATUS
MP0834	MP0834C1Za	US	14/076,129	11/8/2013	9065709	6/23/2015	In Force	DIGITAL RADIO DATA SYSTEM RECEIVER METHODS AND APPARATUS
MP0836	MP0836	US	11/390,704	3/28/2006	7996554	8/9/2011	In Force	System For Improving Quality Of Service For Wireless LANS
MP0836	MP0836C1	US	13/205,159	8/8/2011	8209431	6/26/2012	In Force	SYSTEM AND METHOD FOR IMPROVING QUALITY OF SERVICE FOR W
MP0836	MP0836C1C1	US	13/531,896	6/25/2012	8447858	5/21/2013	In Force	System For Improving Quality Of Service For Wireless LANS
MP0836	MP0836Claa	US	13/898,855	5/21/2013	9380587	6/28/2016	In Force	System For Improving Quality Of Service For Wireless LANS
MP0837	MP0837	US	11/483,057	7/6/2006	8040982	10/18/2011	In Force	PHASE-ADJUSTED CHANNEL ESTIMATION FOR FREQUENCY DIVISION
MP0837	MP0837C1	US	13/236,036	9/19/2011	8331502	12/11/2012	In Force	PHASE-ADJUSTED CHANNEL ESTIMATION FOR FREQUENCY DIVISION
MP0837	MP0837C1C1	US	13/706,164	12/5/2012	8611476	12/17/2013	In Force	PHASE-ADJUSTED CHANNEL ESTIMATION FOR FREQUENCY DIVISION
MP0838	MP0838	US	11/447,762	6/5/2006	7872962	1/18/2011	In Force	SYSTEM AND METHOD FOR PRODUCING WEIGHTED SIGNALS IN A DI
MP0839	MP0839	US	11/410,688	4/24/2006	7801253	9/21/2010	In Force	NONLINEAR POST-PROCESSORS FOR CHANNELS WITH SIGNAL-DEPEN
MP0839	MP0839C1	US	12/862,669	8/24/2010	8259872	9/4/2012	In Force	NONLINEAR POST-PROCESSORS FOR CHANNELS WITH SIGNAL-DEPEN
MP0841	MP0841/WO	WO	PCT/US2007/00	3/6/2007			Pending	Personal Lifestyle Device
MP0841	MP0841aaa	US	13/550,227	7/16/2012	8391919	3/5/2013	In Force	Personal Lifestyle Device
MP0841	MP0841C1	US	12/778,229	5/12/2010	7957764	6/7/2011	In Force	Personal Lifestyle Device
MP0841	MP0841C1C1	US	13/155,105	6/7/2011	8224377	7/17/2012	In Force	Personal Lifestyle Device
MP0841	MP0841C1Za	US	13/785,991	3/5/2013	8750920	6/10/2014	In Force	Personal Lifestyle Device

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MP0841	MP0841C1ZZ	US	14/298,420	6/6/2014	9264853	2/16/2016	In Force	Personal Lifestyle Device
MP0841	MP0841TW	TW	96107922	3/7/2007	I436624	5/1/2014	In Force	Personal Lifestyle Device
MP0841	MP0841WOCN	CN	200780016481.[illegible]	3/6/2007	ZL2007800164	3/20/2013	In Force	PERSONAL LIFESTYLE DEVICE
MP0841	MP0841WOJP	JP	2008-558339	3/6/2007			Pending	PERSONAL LIFESTYLE DEVICE
MP0841	MP0841WOKR	KR	1020087024554	3/6/2007	101373790	3/6/2014	In Force	Personal Lifestyle Device
MP0842	MP0842C1	US	12/698,427	2/2/2010	7940489	5/10/2011	In Force	Method And Apparatus For Repeatable Run-Out Compensation
MP0843	MP0843	US	11/493,269	7/25/2006	8160181	4/17/2012	In Force	NONLINEAR DETECTORS FOR CHANNELS WITH SIGNAL-DEPENDENT
MP0843	MP0843C1	US	13/415,465	3/8/2012	8462897	6/11/2013	In Force	NONLINEAR DETECTORS FOR CHANNELS WITH SIGNAL-DEPENDENT
MP0845	MP0845	US	11/339,020	1/25/2006	7388436	6/17/2008	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845C1	US	11/527,983	9/27/2006	7397306	7/8/2008	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845C2	US	11/528,034	9/27/2006	7417575	8/26/2008	In Force	HIGH-BANDWIDTH HIGH-GAIN AMPLIFIER
MP0845	MP0845C3	US	11/528,033	9/27/2006	7397310	8/7/2008	In Force	HIGH-BANDWIDTH HIGH-GAIN AMPLIFIER
MP0845	MP0845C3C1	US	12/217,753	8/7/2008	7760029	7/20/2010	In Force	HIGH-BANDWIDTH HIGH-GAIN AMPLIFIER
MP0845	MP0845CN	CN	200610137974.[illegible]	11/1/2006	ZL2006101379	5/2/2012	In Force	HIGH-BANDWIDTH HIGH-GAIN AMPLIFIER
MP0845	MP0845CN01	CN	200610137973.[illegible]	11/1/2006	ZL2006101379	4/6/2011	In Force	HIGH-BANDWIDTH HIGH-GAIN AMPLIFIER
MP0845	MP0845D1EPDE	DE	15188986.2	10/31/2006	602006056636	10/17/2018	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845D1EPFR	FR	15188986.2	10/31/2006	3002874	10/17/2018	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845D1EPGB	GB	15188986.2	10/31/2006	3002874	10/17/2018	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EP	EP	06022702.2	10/31/2006	EP1783898	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EP01	EP	06022864	11/2/2006	EP1783899	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EPD1	EP	15188986.2	10/31/2006	3002874	10/17/2018	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EPDE	DE	06022702.2	10/31/2006	602006046904	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EPFR	FR	06022702	10/31/2006	1783898	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845EPGB	GB	06022702.2	10/31/2006	1783898	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845HK	HK	71117582	11/1/2006			Pending	High-Bandwidth High-Gain Amplifier
MP0845	MP0845HK01	HK	71117546	11/1/2006			Pending	High-Bandwidth High-Gain Amplifier
MP0845	MP0845SG	SG	2006073472	10/27/2006	131900	6/30/2009	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845SG01	SG	2006073480	10/27/2006			Pending	High-Bandwidth High-Gain Amplifier
MP0845	MP0845TW	TW	95140412	11/1/2006	I342667	5/21/2011	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845TW01	TW	95140411	11/1/2006	I350650	10/11/2011	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845U1DE	DE	06022864.0	11/2/2006	602006046905	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845U1FR	FR	06022864	11/2/2006	1783899	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0845	MP0845U1GB	GB	06022864.0	11/2/2006	1783899	10/14/2015	In Force	High-Bandwidth High-Gain Amplifier
MP0846	MP0846	US	11/453,193	6/14/2006	7627023	12/1/2009	In Force	64/66 Encoder

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MP0847	MP0847	US	11/333,935	1/18/2006	7541962	6/2/2009	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847C1	US	12/475,947	6/1/2009	7880660	2/1/2011	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847CN	CN	200610168332.(	12/18/2006	ZL200610168(	3/27/2013	In Force	PIPELINED ANALOG-TO-DIGTIAL CONVERTERS
MP0847	MP0847EP	EP	EP06024141.1	11/21/2006	EP1814232	5/15/2013	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847EPDE	DE	06024141.1	11/21/2006	602006036246	5/15/2013	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847EPFR	FR	06024141.1	11/21/2006	1814232	5/15/2013	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847EPGB	GB	06024141.1	11/21/2006	1814232	5/15/2013	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847HK	HK	81000849	12/18/2006			Pending	Pipelined Analog-To-Digital Converters
MP0847	MP0847JP	JP	2006340437	12/18/2006	JP4939201	3/2/2012	In Force	Pipelined Analog-To-Digital Converter
MP0847	MP0847SG	SG	2006086094	12/18/2006	134212	7/31/2009	In Force	Pipelined Analog-To-Digital Converters
MP0847	MP0847TW	TW	95147268	12/15/2006	I420827	12/21/2013	In Force	PIPELINED ANALOG-TO-DIGITAL CONVERTERS
MP0850	MP0850	US	11/446,480	6/2/2006	8199804	6/12/2012	In Force	EFFICIENT TAPPED DELAY LINE EQUALIZER METHODS AND APPARAT
MP0850	MP0850C1	US	13/478,853	5/23/2012	8494041	7/23/2013	In Force	EFFICIENT TAPPED DELAY LINE EQUALIZER METHODS AND APPARAT
MP0851	MP0851C1	US	12/712,021	2/24/2010	7978758	7/12/2011	In Force	EFFICIENT BLIND EQUALIZATION FOR QUADRATURE AMPLITUDE MO
MP0852	MP0852C1	US	12/829,750	7/2/2010	8054877	11/8/2011	In Force	CONSTELLATION RECOVERY FOR QUADRATURE AMPLITUDE MODUL
MP0854	MP0854C1	US	12/953,241	11/23/2010	8184511	5/22/2012	In Force	Method And Apparatus For Increasing The Storage Capacity Of An Op
MP0855	MP0855	US	11/489,835	7/19/2006	7528667	5/5/2009	In Force	T-NETWORK CAPBANK
MP0856	MP0856C1	US	12/694,922	1/27/2010	8319547	11/27/2012	In Force	SELF BIASED LOW NOISE HIGH PSRR CONSTANT GM FOR VCO
MP0858	MP0858	US	11/544,163	10/5/2006	7756486	7/13/2010	In Force	TRANSMITTER AND RECEIVER IMPEDANCE CONTROL USING SHUNT S
MP0858	MP0858C1	US	12/827,449	6/30/2010	8442451	5/14/2013	In Force	TRANSMITTER AND RECEIVER IMPEDENCE CONTROL USING SHUNT S
MP0861	MP0861	US	11/415,937	5/2/2006	7729389	6/1/2010	In Force	8/10 And 64/66 Aggregation
MP0861	MP0861C1	US	12/791,130	6/1/2010	8233507	7/31/2012	In Force	8/10 And 64/66 Aggregation
MP0861	MP0861C1aa	US	13/899,877	5/22/2013	8982915	3/17/2015	In Force	8/10 And 64/66 Aggregation
MP0861	MP0861C1C1	US	13/563,342	7/31/2012	8451869	5/28/2013	In Force	8/10 And 64/66 Aggregation
MP0862	MP0862C1	US	12/714,716	3/1/2010	8284747	10/9/2012	In Force	BSS Selection Using Path Loss
MP0862	MP0862C1aa	US	14/061,811	10/24/2013	8908559	12/9/2014	In Force	BSS Selection Using Path Loss
MP0862	MP0862C1C1	US	13/614,214	9/13/2012	8570997	10/29/2013	In Force	BSS Selection Using Path Loss
MP0863	MP0863	US	11/400,982	4/10/2006	7760681	7/20/2010	In Force	Transmit Power Adaptation Algorithm Using 802.11H
MP0863	MP0863aaaa	US	14/521,927	10/23/2014	9510299	11/29/2016	In Force	Method And Apparatus For Controlling A Power At Which A Signal Is
MP0863	MP0863C1	US	12/836,183	7/14/2010	8014802	9/6/2011	In Force	Transmit Power Adaptation Algorithm Using 802.11H
MP0863	MP0863C1aa	US	13/747,141	1/22/2013	8874154	10/28/2014	In Force	Transmit Power Adaptation Algorithm Using 802.11H
MP0863	MP0863C1C1	US	13/224,967	9/2/2011	8359052	1/22/2013	In Force	Transmit Power Adaptation Algorithm Using 802.11H
MP0864	MP0864	US	11/410,938	4/25/2006	7702044	4/20/2010	In Force	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864C1	US	12/761,937	4/16/2010	9366750	6/14/2016	In Force	Radar Detection And Dynamic Frequency Selection

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MP0864	MP0864CN	CN	200610150782.:	10/26/2006	ZL2006101507	1/4/2012	In Force	RADAR DETECTION AND DYNAMIC FREQUENCY SELETION
MP0864	MP0864EP	EP	EP06020170.4	9/26/2006	EP1793241	6/10/2009	In Force	System and Method for Radar Detection and Dynamic Frequency Se
MP0864	MP0864EPDE	DE	06020170.4	9/26/2006	602006007203	6/10/2009	In Force	System And Method For Radar Detection And Dynamic Frequency S
MP0864	MP0864EPFR	FR	60201704	9/26/2006	1793241	6/10/2009	In Force	System And Method For Radar Detection And Dynamic Frequency S
MP0864	MP0864EPGB	GB	06020170.4	9/26/2006	1793241	6/10/2009	In Force	System And Method For Radar Detection And Dynamic Frequency S
MP0864	MP0864HK	HK	71132379	10/26/2006			Pending	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864JP	JP	2006300741	11/6/2006	5214133	3/8/2013	In Force	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864KR	KR	1020060122395	12/5/2006	101256217	4/12/2013	In Force	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864SG	SG	2006069587	10/11/2006			Pending	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864SG01	SG	2009038365	10/11/2006	153130	5/31/2011	In Force	Radar Detection And Dynamic Frequency Selection
MP0864	MP0864TW	TW	95139366	10/25/2006	I401462	7/11/2013	In Force	WIRELESS NETWORK DEVICE AND METHOD FOR DETECTING RADAR
MP0865	MP0865	US	11/455,476	6/19/2006	7466095	12/16/2008	In Force	Voice Coil Motor Control System And Method Using Pulse Width M
MP0865	MP0865C1	US	12/327,860	12/4/2008	7602131	10/13/2009	In Force	Voice Coil Motor Control System And Method Using Pulse Width M
MP0866	MP0866	US	11/590,031	10/31/2006	7872636	1/18/2011	In Force	Virtual Pointing Devices For Displays
MP0866	MP0866C1	US	13/008,179	1/18/2011	8531398	9/10/2013	In Force	Method And Apparatus For Using A Virtual Pointer To Select An Obje
MP0868	MP0868	US	11/368,308	3/3/2006	7835710	11/16/2010	In Force	VARIABLE POWER ADAPTIVE TRANSMITTER
MP0868	MP0868aUDE	DE	07000789.3	1/16/2007	602007021083	3/7/2012	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868aUFR	FR	07000789.3	1/16/2007	1814220	3/7/2012	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868aUGB	GB	07000789.3	1/16/2007	1814220	3/7/2012	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868C1	US	11/649,658	1/4/2007	7761066	7/20/2010	In Force	Variable Power Adaptive Transmitter
MP0868	MP0868C1CN	CN	200710002905.:	1/26/2007	ZL2007100029	2/13/2013	In Force	A VARIABLE POWER ADAPTIVE TRANSMITTER
MP0868	MP0868C1EP	EP	07000789.3	1/16/2007	EP1814220	3/7/2012	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868C1HK	HK	81029926	1/26/2007			Pending	A Variable Power Adaptive Transmitter
MP0868	MP0868C1JP	JP	2007016581	1/26/2007	5010296	6/8/2012	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868C1SG	SG	2007004997	1/26/2007			Pending	A Variable Power Adaptive Transmitter
MP0868	MP0868C1SG03	SG	2009050071	1/26/2007	154521	6/30/2011	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868C1TW	TW	96102999	1/26/2007	I458269	10/21/2014	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868CN	CN	200710002906.:	1/26/2007	ZL2007100029	8/8/2012	In Force	A VARIABLE POWER ADAPTIVE TRANSMITTER
MP0868	MP0868DE	DE	07000803.2	1/16/2007	6.02007E+12	5/11/2011	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868EP	EP	EP07000803.2	1/16/2007	EP1814221	5/11/2011	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868FR	FR	70008032	1/16/2007	1814221	5/11/2011	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868GB	GB	07000803.2	1/16/2007	1814221	5/11/2011	In Force	A Variable Power Adaptive Transmitter
MP0868	MP0868HK	HK	81010963	1/26/2007			Pending	A Variable Power Adaptive Transmitter
MP0868	MP0868JP	JP	2007016577	1/26/2007	4949049	3/16/2012	In Force	A Variable Power Adaptive Transmitter

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MP0868	MP0868SG	SG	2007005002	1/26/2007			Pending	A Variable Power Adaptive Transmitter
MP0868	MP0868SG02	SG	2009050063	1/26/2007	154520	6/30/2011	In Force	A Variable Power Adaptive Transmitter
MP0869	MP0869	US	11/411,616	4/26/2006	7873048	1/18/2011	In Force	Flexible Port Rate Limiting
MP0869	MP0869C1	US	13/008,778	1/18/2011	8634335	1/21/2014	In Force	Flexible Port Rate Limiting
MP0870	MP0870	US	11/481,141	7/5/2006	7774675	8/10/2010	In Force	LDPC Codes And Expansion Method
MP0870	MP0870C1	US	12/852,817	8/9/2010	8281213	10/2/2012	In Force	LDPC Codes And Expansion Method
MP0870	MP0870C1aa	US	13/942,183	7/15/2013	8762809	6/24/2014	In Force	LDPC Codes And Expansion Method
MP0870	MP0870C1C1	US	13/614,065	9/13/2012	8489960	7/16/2013	In Force	LDPC Codes And Expansion Method
MP0870	MP0870C1Za	US	14/309,319	6/19/2014	9009560	4/14/2015	In Force	Apparatus For Encoding And Decoding Using Sparse Matrices
MP0873	MP0873	US	11/586,859	10/26/2006	8094631	1/10/2012	In Force	Coexistence System And Method For Wireless Network Devices
MP0873	MP0873C1	US	13/346,283	1/9/2012	8345652	1/1/2013	In Force	Coexistence System And Method For Wireless Network Devices
MP0873	MP0873C1aa	US	14/101,656	12/10/2013	8923847	12/30/2014	In Force	Apparatus And Method For Communicating With An Access Point V
MP0873	MP0873C1C1	US	13/730,144	12/28/2012	8606263	12/10/2013	In Force	METHOD AND APPARATUS FOR RESPECTIVELY TRANSMITTING SIGN
MP0873	MP0873PCT	WO	PCT/US2006/04	12/11/2006			Pending	Coexistence System And Method For Wireless Network Devices
MP0873	MP0873WJAA	JP	2014-076691	4/3/2014	5778307	7/17/2015	In Force	COEXISTENCE SYSTEM AND METHOD FOR WIRELESS NETWORK DEVI
MP0873	MP0873WJD1	JP	2012-039936	12/11/2006	5548716	5/23/2014	In Force	COEXISTENCE SYSTEM AND METHOD FOR WIRELESS NETWORK DEVI
MP0873	MP0873WOCN	CN	200680052593.3	12/11/2006	ZL2006800525	3/14/2012	In Force	COEXISTENCE SYSTEM AND METHOD FOR WIRELESS NETWORK DEVI
MP0873	MP0873WOEP	EP	EP06839241.4	12/11/2006	1969772	2/20/2019	In Force	Coexistence System and Method for Wireless Network Devices
MP0873	MP0873WOEPDE	DE	EP06839241.4	12/11/2006	602006057459	2/20/2019	In Force	Coexistence System and Method for Wireless Network Devices
MP0873	MP0873WOEPFR	FR	EP06839241.4	12/11/2006	1969772	2/20/2019	In Force	Coexistence System and Method for Wireless Network Devices
MP0873	MP0873WOEPGB	GB	EP06839241.4	12/11/2006	1969772	2/20/2019	In Force	Coexistence System and Method for Wireless Network Devices
MP0873	MP0873WOIN	IN	2881CHENP200	12/11/2006	284330	6/19/2017	In Force	Coexistence System And Method For Wireless Network Devices
MP0873	MP0873WOJP	JP	2008-544544	12/11/2006	4994388	5/18/2012	In Force	COEXISTENCE SYSTEM AND METHOD FOR WIRELESS NETWORK DEVI
MP0873	MP0873WOKR	KR	10-2008-701673	12/11/2006	10-1298244	8/13/2013	In Force	COEXISTENCE SYSTEM AND METHOD FOR WIRELESS NETWORK DEVI
MP0874	MP0874	US	11/607,116	11/30/2006	7853578	12/14/2010	In Force	High-Performance Pattern Matching
MP0874	MP0874C1	US	12/966,749	12/13/2010	8301604	10/30/2012	In Force	High Performance Pattern Matching
MP0876	MP0876C1	US	12/755,893	4/7/2010	7984246	7/19/2011	In Force	MULTICORE MEMORY MANAGEMENT SYSTEM
MP0877	MP0877	US	11/602,665	11/21/2006	7423833	9/9/2008	In Force	Servo Loop Employing Correction Vector From A Secondary Disturba
MP0877	MP0877C1	US	12/231,921	9/8/2008	7529057	5/5/2009	In Force	Servo Loop Employing Correction Vector From A Secondary Disturba
MP0879	MP0879	US	11/503,564	8/11/2006	7560961	7/14/2009	In Force	Voltage Driver Circuit
MP0879	MP0879C1	US	12/502,475	7/14/2009	7893733	2/22/2011	In Force	Voltage Driver Circuit
MP0881	MP0881	US	11/495,295	7/28/2006	7764462	7/27/2010	In Force	Thermal Solution For Drive Systems Such As Hard Disk Drives And Dig
MP0881	MP0881D1AA	US	13/540,171	7/2/2012	8355221	1/15/2013	In Force	Thermal Solution For Drive Systems Such As Hard Disk Drives And Dig
MP0881	MP0881D1C1	US	13/154,339	6/6/2011	8218261	7/10/2012	In Force	Thermal Solution For Drive Systems Such As Hard Disk Drives And Dig

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MP0883	MP0883	US	11/450,693	6/9/2006	7660272	2/9/2010	In Force	Parallel Detection To Full Duplex
MP0883	MP0883C1	US	12/697,594	2/1/2010	7924750	4/12/2011	In Force	Method And Apparatus For Establishing A Communication Mode Be
MP0884	MP0884	US	11/590,073	10/30/2006	7486154	2/3/2009	In Force	FLICKER NOISE DEGENERATION TECHNIQUE FOR VCO
MP0886	MP0886C1	US	12/574,110	10/6/2009	7761668	7/20/2010	In Force	Processor Architecture Having Multi-Ported Memory
MP0886	MP0886CN	CN	200610168331.:	12/18/2006	ZL2006101683	5/30/2012	In Force	PROCESSOR ARCHITECTURE
MP0886	MP0886EP	EP	EP06024608.9	11/28/2006	EP1811398	4/6/2011	In Force	Data Processing System with a Multiported Memory for Interproces
MP0886	MP0886EPDE	DE	06024608.9	11/28/2006	602006021129	4/6/2011	In Force	Data Processing System With A Multiported Memory For Interproce
MP0886	MP0886EPFR	FR	06024608.9	11/28/2006	1811398	4/6/2011	In Force	Data Processing System With A Multiported Memory For Interproce
MP0886	MP0886EPGB	GB	06024608.9	11/28/2006	1811398	4/6/2011	In Force	Data Processing System With A Multiported Memory For Interproce
MP0886	MP0886HK	HK	81000858	12/18/2006			Pending	Processor Architecture
MP0886	MP0886JP	JP	2006340439	12/18/2006	5302507	6/28/2013	In Force	Processor Architecture
MP0886	MP0886SG	SG	2006086086	12/18/2006	134211	7/31/2009	In Force	Processor Architecture
MP0886	MP0886TW	TW	95147276	12/15/2006	I514165	12/21/2015	In Force	DATA PROCESSING SYSTEM
MP0887	MP0887	US	11/384,855	3/20/2006	7212144	5/1/2007	In Force	Flash ADC
MP0887	MP0887C1	US	11/799,018	4/30/2007	7605739	10/20/2009	In Force	Differential Flash ADC With Dual Resistance Ladder Legs Receiving A
MP0887	MP0887C1C1	US	12/476,369	6/2/2009	7760127	7/20/2010	In Force	Flash ADC
MP0887	MP0887CN	CN	200610168330.7	12/18/2006	ZL2006101683	9/5/2012	In Force	IMPROVED FLASH ADC
MP0887	MP0887EP	EP	EP06024652.7	11/28/2006	EP1814233	10/21/2009	In Force	Improved Flash ADC
MP0887	MP0887EPDE	DE	06024652.7	11/28/2006	602006009894	10/21/2009	In Force	Improved Flash ADC
MP0887	MP0887EPFR	FR	06024652.7	11/28/2006	1814233	10/21/2009	In Force	Improved Flash ADC
MP0887	MP0887EPGB	GB	06024652.7	11/28/2006	1814233	10/21/2009	In Force	Improved Flash ADC
MP0887	MP0887HK	HK	81000867	12/18/2006			Pending	Improved Flash ADC
MP0887	MP0887JP	JP	2006340442	12/18/2006	JP4799391	8/12/2011	In Force	Improved Flash ADC
MP0887	MP0887SG	SG	2006084016	11/30/2006	134209	8/31/2009	In Force	Improved Flash ADC
MP0887	MP0887TW	TW	95147272	12/15/2006	I403093	7/21/2013	In Force	IMPROVED FLASH ADC
MP0888	MP0888	US	10/866,327	6/9/2004	7643582	1/5/2010	In Force	METHOD AND SYSTEM FOR DETERMINING SYMBOL BOUNDARY TIMI
MP0888	MP0888C1	US	12/628,738	12/1/2009	8265215	9/11/2012	In Force	METHOD AND SYSTEM FOR DETERMINING SYMBOL BOUNDARY TIMI
MP0888	MP0888C1C1	US	13/608,011	9/10/2012	8817919	8/26/2014	In Force	METHOD AND SYSTEM FOR DETERMINING SYMBOL BOUNDARY TIMI
MP0888	MP0888WO	WO	PCT/US2005/02	6/6/2005			Pending	METHOD AND SYSTEM FOR DETERMINING SYMBOL BOUNDARY TIMI
MP0889	MP0889	US	10/865,256	6/9/2004	7817730	10/19/2010	In Force	TRAINING SEQUENCE FOR SYMBOL BOUNDARY DETECTION IN A MUL
MP0889	MP0889WO	WO	PCT/US2005/02	6/6/2005			Pending	TRAINING SEQUENCE FOR SYMBOL BOUNDARY DETECTION IN A MUL
MP0890	MP0890	US	10/865,093	6/9/2004	7561626	7/14/2009	In Force	METHOD AND SYSTEM FOR CHANNEL ESTIMATION IN A DATA TRANS
MP0890	MP0890WO	WO	PCT/US2005/02	6/9/2005			Pending	METHOD AND SYSTEM FOR CHANNEL ESTIMATION IN A DATA TRANS
MP0891	MP0891	US	10/865,133	6/9/2004	7697619	4/13/2010	In Force	TRAINING SEQUENCE FOR CHANNEL ESTIMATION IN A DATA TRANS

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MP0891	MP0891WO	WO	PCT/US2005/01	6/6/2005			Pending	TRAINING SEQUENCE FOR CHANNEL ESTIMATION IN A DATA TRANS
MP0892	MP0892	US	11/031,221	1/6/2005	7561627	7/14/2009	In Force	METHOD AND SYSTEM FOR CHANNEL EQUALIZATION AND CROSSTA
MP0892	MP0892WO	WO	PCT/US2005/04	12/20/2005			Pending	METHOD AND SYSTEM FOR CHANNEL EQUALIZATION AND CROSSTA
MP0894	MP0894C1C1	US	14/157,977	1/17/2014	9490810	11/8/2016	In Force	LOW-NOISE, TEMPERATURE-INSENSITIVE, VOLTAGE OR CURRENT IN
MP0895	MP0895C1	US	12/860,440	8/20/2010	8026742	9/27/2011	In Force	PHASE DETECTOR
MP0898	MP0898	US	11/523,407	9/19/2006	8432920	4/30/2013	In Force	Direct Link Setup Mechanisms For Wireless LANs
MP0898	MP0898C1	US	13/871,490	4/26/2013	9198212	11/24/2015	In Force	Direct Link Setup Mechanisms For Wireless LANs
MP0898	MP0898WO	WO	PCT/US2007/02	9/19/2007			Pending	Direct Link Setup Mechanisms For Wireless LANs
MP0898	MP0898WOCN	CN	200780042934.0	9/19/2007	ZL2007800429	7/11/2012	In Force	DIRECT LINK SETUP MECHANISMS FOR WIRELESS LANS
MP0898	MP0898WOEP	EP	07838498.9	9/19/2007	EP2064837	11/22/2017	In Force	Direct Link Setup Mechanisms for Wireless LANs
MP0898	MP0898WOJP	JP	2009-529222	9/19/2007	4921558	2/10/2012	In Force	DIRECT LINK SETUP MECHANISMS FOR WIRELESS LANS
MP0898	MP0898WUDE	DE	07838498.9	9/19/2007	602007053146	11/22/2017	In Force	Direct Link Setup Mechanisms for Wireless LANs
MP0898	MP0898WUFR	FR	07838498.9	9/19/2007	2064837	11/22/2017	In Force	Direct Link Setup Mechanisms for Wireless LANs
MP0898	MP0898WUGB	GB	07838498.9	9/19/2007	2064837	11/22/2017	In Force	Direct Link Setup Mechanisms for Wireless LANs
MP0899	MP0899	US	11/528,176	9/26/2006	7388531	6/17/2008	In Force	CURRENT STEERING DAC USING THIN OXIDE DEVICES
MP0900	MP0900	US	11/835,952	8/8/2007	7576667	8/18/2009	In Force	HIERARCHIED CALIBRATION CIRCUIT
MP0901	MP0901	US	11/786,886	4/12/2007	7492219	2/17/2009	In Force	POWER EFFICIENT AMPLIFIER
MP0901	MP0901C1	US	12/350,735	1/8/2009	7737776	6/15/2010	In Force	POWER EFFICIENT AMPLIFIER
MP0901	MP0901C1C1	US	12/723,366	3/12/2010	7932778	4/26/2011	In Force	POWER EFFICIENT AMPLIFIER
MP0902	MP0902	US	11/504,523	8/15/2006	7822160	10/26/2010	In Force	Digitally-Assisted Power Reduction Technique For IQ Pipeline ADCs U
MP0902	MP0902C1	US	12/907,842	10/19/2010	8031821	10/4/2011	In Force	Digitally Assisted Power Reduction Technique For IQ Pipeline ADCs U
MP0903	MP0903	US	11/486,906	7/14/2006	7397412	7/8/2008	In Force	Low Power Analog To Digital Converter
MP0904	MP0904	US	11/481,109	7/5/2006	7646555	1/12/2010	In Force	Testing Storage System Electronics Using Loopback
MP0904	MP0904C1	US	12/685,249	1/11/2010	8031424	10/4/2011	In Force	Method And Apparatus For Testing Electronics Of A Storage System
MP0904	MP0904CN	CN	200710000255.1	1/16/2007	ZL2007100002	7/6/2011	In Force	TESTING STORAGE SYSTEM ELECTRONICS USING LOOPBACK
MP0904	MP0904EP	EP	70004635	1/10/2007			Pending	Testing Storage System Electronic Using Loopback
MP0904	MP0904HK	HK	81000876	1/16/2007			Pending	Testing Storage System Electronics Using Loopback
MP0904	MP0904JP	JP	2007008412	1/17/2007			Pending	Testing Storage System Electronic Using Loopback
MP0904	MP0904SG	SG	2007002256	1/16/2007			Pending	Testing Storage System Electronic Using Loopback
MP0904	MP0904SG01	SG	2009048406	1/16/2007	154489	4/15/2011	In Force	Testing Storage System Electronics Using Loopback
MP0904	MP0904TW	TW	96101812	1/17/2007	I406272	8/21/2013	In Force	TESTING STORAGE SYSTEM ELECTRONICS USING LOOPBACK
MP0905	MP0905	US	11/471,057	6/19/2006	7675157	3/9/2010	In Force	THERMAL ENHANCED PACKAGE
MP0905	MP0905CN	CN	200780012013.5	1/29/2007	ZL2007800120	6/20/2012	In Force	THERMAL ENHANCED PACKAGE
MP0905	MP0905CND1	CN	201210124148.7	1/29/2007	ZL2012101241	2/17/2016	In Force	THERMAL ENHANCED PACKAGE

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MP0905	MP0905D1	US	12/496,379	7/1/2009	8357568	1/22/2013	In Force	THERMAL ENHANCED PACKAGE
MP0905	MP0905D1C1	US	13/613,886	9/13/2012	8900932	12/2/2014	In Force	Thermal enhanced package
MP0905	MP0905TW	TW	96103334	1/30/2007	I343102	6/1/2011	In Force	THERMAL ENHANCED PACKAGE
MP0905	MP0905WO	WO	PCT/US2007/00	1/29/2007			Pending	THERMAL ENHANCED PACKAGE
MP0907	MP0907	US	11/521,182	9/14/2006	7813421	10/12/2010	In Force	Order Recursive Computation For A MIMO Equalizer
MP0907	MP0907C1	US	12/902,394	10/12/2010	8340169	12/25/2012	In Force	Order Recursive Computation For A MIMO Equalizer
MP0907	MP0907C1aa	US	14/252,860	4/15/2014	9001873	4/7/2015	In Force	Method And Apparatus For Recursively Computing Equalizer Param
MP0907	MP0907C1C1	US	13/725,167	12/21/2012	8699556	4/15/2014	In Force	Method And Apparatus For Recursively Computing Equalizer Coeffi
MP0907	MP0907PCT	WO	PCT/US2007/00	1/16/2007			Pending	Order Recursive Computation For A MIMO Equalizer
MP0907	MP0907WJP2	JP	2008-550466	1/17/2007	JP5160449	12/21/2012	In Force	RECURSIVE COMPUTATION OF A CHANNEL MATRIX FOR A MIMO EC
MP0907	MP0907WO2IN	IN	3393CHENP200	1/17/2007	283233	5/11/2017	In Force	Recursive Computation For A Channel Matrix For A MIMO Equalizer
MP0907	MP0907WO2KR	KR	1020087020091	1/17/2007	101373788	3/6/2014	In Force	Recursive Computation Of A Channel Matrix For A MIMO Equalizer
MP0907	MP0907WOCN	CN	200780002519.:	1/17/2007	ZL2007800025	7/18/2012	In Force	RECURSIVE COMPUTATION OF A CHANNEL MATRIX FOR A MIMO EC
MP0907	MP0907WOEP	EP	EP07716730.2	1/17/2007	1974512	8/22/2018	In Force	Recursive Computation for a Channel Matrix For a MIMO Equalizer
MP0907	MP0907WOEPDE	DE	EP07716730.2	1/17/2007	602007055836	8/22/2018	In Force	Recursive Computation for a Channel Matrix For a MIMO Equalizer
MP0907	MP0907WOEPFR	FR	EP07716730.2	1/17/2007	1974512	8/22/2018	In Force	Recursive Computation for a Channel Matrix For a MIMO Equalizer
MP0907	MP0907WOEPGB	GB	EP07716730.2	1/17/2007	1974512	8/22/2018	In Force	Recursive Computation for a Channel Matrix For a MIMO Equalizer
MP0907	MP0907WOPOB	WO	PCT/US2007/00	1/17/2007			Pending	Recursive Computation For A Channel Matrix For A MIMO Equalizer
MP0908	MP0908	US	11/652,907	1/11/2007	7791406	9/7/2010	In Force	LOW LEAKAGE POWER MANAGEMENT
MP0908	MP0908D1	US	12/873,997	9/1/2010	8004352	8/23/2011	In Force	LOW LEAKAGE POWER MANAGEMENT
MP0908	MP0908D1aa	US	13/548,808	7/13/2012	8436678	5/7/2013	In Force	LOW LEAKAGE POWER MANAGEMENT
MP0908	MP0908D1C1	US	13/213,695	8/19/2011	8248156	8/21/2012	In Force	LOW LEAKAGE POWER MANAGEMENT
MP0909	MP0909	US	11/598,117	11/8/2006	7844879	11/30/2010	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909C1	US	12/946,520	11/15/2010	8473812	6/25/2013	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909C1aa	US	14/217,675	3/18/2014	8856622	7/10/2014	In Force	APPARATUS AND METHOD FOR ENCODING DATA FOR STORAGE IN M
MP0909	MP0909C1C1	US	13/926,514	6/25/2013	8677215	3/18/2014	In Force	Method and System For Error Correction in Flash Memory
MP0909	MP0909C1Za	US	14/505,820	10/3/2014	9053051	6/9/2015	In Force	Multi-Level Memory Controller With Probability-Distribution-Based E
MP0909	MP0909CN	CN	200780009987.:	1/19/2007	ZL2007800099	1/25/2012	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909EP	EP	07718293.9	1/19/2007	EP1987519	8/19/2015	In Force	Method and System for Error Correction in Flash Memory
MP0909	MP0909EPDE	DE	07718293.9	1/19/2007	1987519	8/19/2015	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909EPFR	FR	07718293.9	1/19/2007	1987519	8/19/2015	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909EPGB	GB	07718293.9	1/19/2007	1987519	8/19/2015	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909JP	JP	2008-551451	1/19/2007	5232013	3/29/2013	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION FLASH MEMORY
MP0909	MP0909KR	KR	1020087020392	1/19/2007	101410434	6/16/2014	In Force	Method And System For Error Correction In Flash Memory

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MP0909	MP0909TW	TW	96102213	1/19/2007	I367492	7/1/2012	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909TWD1	TW	99144839	1/19/2007	I455137	10/1/2014	In Force	METHOD AND SYSTEM FOR ERROR CORRECTION IN FLASH MEMORY
MP0909	MP0909WO	WO	PCT/US2007/00	1/19/2007			Pending	Method And System For Error Correction In Flash Memory
MP0910	MP0910	US	11/607,641	12/1/2006	7619457	11/17/2009	In Force	PROGRAMMABLE DELAY CIRCUIT
MP0910	MP0910C1	US	12/616,393	11/11/2009	8242828	8/14/2012	In Force	PROGRAMMABLE DELAY CIRCUIT
MP0911	MP0911	US	11/501,338	8/9/2006	7388529	6/17/2008	In Force	Interference Detection Using A Moving Window
MP0911	MP0911C1	US	12/157,255	6/9/2008	7864091	1/4/2011	In Force	Interference Detection Using A Moving Window
MP0911	MP0911C1C1	US	12/983,794	1/3/2011	7978106	7/12/2011	In Force	Interference Detection Using A Moving Window
MP0912	MP0912	US	11/501,341	8/9/2006	7623052	11/24/2009	In Force	Strong Short-Pulse Interference Detection Method
MP0912	MP0912C1	US	12/623,799	11/23/2009	7773014	8/10/2010	In Force	Strong Short-Pulse Interference Detection Method
MP0914	MP0914C1	US	12/724,250	3/15/2010	7974367	7/5/2011	In Force	SYMBOL TIMING METHOD AND DIGITAL RECEIVER APPARATUS
MP0916	MP0916	US	11/598,178	11/8/2006	8055979	11/8/2011	In Force	Flash Memory With Coding And Signal Processing
MP0916	MP0916CN	CN	200780010188.(	1/19/2007	ZL2007800101	1/4/2012	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916EP	EP	07718319.2	1/19/2007	EP1984923	8/14/2013	In Force	Flash Memory with Coding and Signal Processing
MP0916	MP0916EPDE	DE	07718319.2	1/19/2007	1984923	8/14/2013	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916EPFR	FR	07718319.2	1/19/2007	1984923	8/14/2013	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916EPGB	GB	07718319.2	1/19/2007	1984923	8/14/2013	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916JP	JP	2008-551453	1/19/2007	JP5232014	3/29/2013	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916KR	KR	1020087020393	1/19/2007	101373789	3/6/2014	In Force	Flash Memory With Coding And Signal Processing
MP0916	MP0916TW	TW	96102390	1/22/2007	I352284	11/11/2011	In Force	FLASH MEMORY WITH CODING AND SIGNAL PROCESSING
MP0916	MP0916WO	WO	PCT/US2007/00	1/19/2007			Pending	Flash Memory With Coding And Signal Processing
MP0917	MP0917	US	11/504,093	8/14/2006	9363346	6/7/2016	In Force	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0917	MP0917/JP	JP	2007102557	4/10/2007	JP5292717	6/21/2013	In Force	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0917	MP0917CN	CN	200710093723	4/5/2007	ZL2007100937	12/18/2013	In Force	REMOTE CONTROL OF NETWORK APPLIANCES USING VOICE OVER IN
MP0917	MP0917EPDE	DE	07005937.3	3/22/2007	60200705115.	3/7/2018	In Force	Remote Control of Network Appliances Using Voice Over Internet Pro
MP0917	MP0917EPFR	FR	07005937.3	3/22/2007	1855450	3/7/2018	In Force	Remote Control of Network Appliances Using Voice Over Internet Pro
MP0917	MP0917EPGB	GB	07005937.3	3/22/2007	1855450	3/7/2018	In Force	Remote Control of Network Appliances Using Voice Over Internet Pro
MP0917	MP0917HK	HK	81046649	4/5/2007			Pending	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0917	MP0917SG	SG	2007020654	3/22/2007			Pending	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0917	MP0917SG01	SG	2009070640	3/22/2007			Pending	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0917	MP0917TW	TW	96112272	4/9/2007	I435582	4/21/2014	In Force	Remote Control Of Network Appliances Using Voice Over Internet Pr
MP0922	MP0922	US	11/505,513	8/16/2006	7358876	4/15/2008	In Force	MIXED-MODE ANALOG OFFSET CANCELLATION FOR DATA CONVERSI
MP0923	MP0923	US	11/505,555	8/16/2006	7372329	5/13/2008	In Force	METHOD AND APPARATUS TO REMOVE THE DC COMPONENT OF A F
MP0923	MP0923C1	US	12/114,491	2/5/2008	7944292	5/17/2011	In Force	METHOD AND APPARATUS TO REMOVE THE DC COMPONENT OF A F

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MP0924	MP0924	US	11/505,782	8/16/2006	7808075	10/5/2010	In Force	INTEGRATED CIRCUIT DEVICES WITH ESD AND I/O PROTECTION
MP0924	MP0924C1	US	12/858,194	8/17/2010	7883947	2/8/2011	In Force	METHOD OF FABRICATING A DEVICE WITH ESD AND I/O PROTECTIO
MP0925	MP0925	US	11/523,389	9/19/2006	8089907	1/3/2012	In Force	Duplex Mismatch Detection
MP0925	MP0925D1	US	13/315,800	9/12/2011	8705416	4/22/2014	In Force	DUPLEX MISMATCH DETECTION
MP0925	MP0925TW	TW	96106323	2/16/2007	I466521	12/21/2014	In Force	DUPLEX MISMATCH DETECTION
MP0925	MP0925WO	WO	PCT/US2007/00	2/15/2007			Pending	Duplex Mismatch Detection
MP0925	MP0925WOCN	CN	200780019972	2/15/2007			Pending	DUPLEX MISMATCH DETECTION
MP0925	MP0925WOEP	EP	EP20070750897	2/15/2007	1999882	4/10/2019	In Force	Duplex Mismatch Detection
MP0925	MP0925WOEPDE	DE	EP20070750897	2/15/2007	602007058074	4/10/2019	In Force	Duplex Mismatch Detection
MP0925	MP0925WOEPFR	FR	EP20070750897	2/15/2007	1999882	4/10/2019	In Force	Duplex Mismatch Detection
MP0925	MP0925WOEPGB	GB	EP20070750897	2/15/2007	1999882	4/10/2019	In Force	Duplex Mismatch Detection
MP0925	MP0925WOJP	JP	2009-502781	2/15/2007	4972154	4/13/2012	In Force	DUPLEX MISMATCH DETECTION
MP0927	MP0927WO	WO	PCT/US2006/00	1/20/2006			Pending	Limiter Based Analog Demodulator
MP0927	MP0927WOCN	CN	200680002790.(	1/20/2006	ZL2006800027	11/14/2012	In Force	LIMITER BASED ANALOG DEMODULATOR
MP0927	MP0927WOJP	JP	2007-552261	1/20/2006	4939437	3/2/2012	In Force	Limiter Based Analog Demodulator
MP0928	MP0928	US	11/417,778	5/4/2006	7720179	5/18/2010	In Force	Method For Timing Detection
MP0928	MP0928_PCT	WO	PCT/US2006/01	5/9/2006			Pending	Method For Timing Detection
MP0928	MP0928TW	TW	95118591	5/25/2006	I416912	11/21/2013	In Force	Method For Timing Detection
MP0929	MP0929CN	CN	200680002387.(	1/12/2006	ZL2006800023	2/1/2012	In Force	METHOD AND APPARATUS FOR A TRANSMISSION SIGNAL UP-CONVE
MP0929	MP0929WO	WO	PCT/US2006/00	1/12/2006			Pending	Method And Apparatus For A Transmission Signal Up-Converting Filte
MP0929	MP0929WOHK	HK	81068580	6/20/2008			Pending	Method And Apparatus For A Transmission Signal Up-Converting Filte
MP0930	MP0930	US	11/546,671	10/12/2006	8121065	2/21/2012	In Force	TDMA Controller For A Wireless Communication System And Method
MP0930	MP0930C1	US	13/400,326	2/20/2012	8675541	3/18/2014	In Force	TDMA Controller For A Wireless Communication System And Method
MP0930	MP0930C1C1	US	14/215,831	3/17/2014	9161358	10/13/2015	In Force	Method And Apparatus For Synchronizing Timing Of A First Station T
MP0930	MP0930CN	CN	200780003981.(	1/9/2007	ZL2007800039	5/25/2016	In Force	TDMA CONTROLLER FOR A WIRELESS COMMUNICATION SYSTEM AN
MP0930	MP0930PCT	WO	PCT/US2007/00	1/9/2007			Pending	TDMA Controller For A Wireless Communication System And Method
MP0930	MP0930TW	TW	96103257	1/29/2007	I451727	9/1/2014	In Force	TDMA CONTROLLER FOR A WIRELESS COMMUNICATION SYSTEM AN
MP0931	MP0931WO	WO	PCT/US2006/00	1/31/2006			Pending	Improved Precision Cordic Processor
MP0931	MP0931WOCN	CN	2006800037230	1/31/2006			Pending	Improved Precision Cordic Processor
MP0931	MP0931WOJP	JP	2007553372	1/31/2006			Pending	Improved Precision Cordic Processor
MP0932	MP0932C1CN	CN	200680002742.(	1/19/2006	ZL2006800027	11/16/2011	In Force	STORAGE EFFICIENT SLIDING WINDOW SUM
MP0932	MP0932WO	WO	PCT/US2006/00	1/19/2006			Pending	Storage Efficient Sliding Window Sum
MP0932	MP0932WOJP	JP	2007552276	1/19/2006	4970283	4/13/2012	In Force	Storage Efficient Sliding Window Sum
MP0934	MP0934	US	11/442,838	5/30/2006	7839923	11/23/2010	In Force	Method And System For Equalizing Received Signals In Communicatio

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MP0934	MP0934PWO	WO	PCT/US2006/02	6/9/2006			Pending	Receiving System With Carrier Recovery, Timing Correction And Equa
MP0934	MP0934TW	TW	95122284	6/21/2006	I403107	7/21/2013	In Force	METHOD AND SYSTEM FOR EQUALIZING RECEIVED SIGNAL IN COMI
MP0935	MP0935	US	11/642,817	12/19/2006	8660617	2/25/2014	In Force	WIMAX ENHANCED SLEEP MODE
MP0935	MP0935C1	US	14/188,509	2/24/2014	9037198	5/19/2015	In Force	WIMAX ENHANCED SLEEP MODE
MP0935	MP0935WO	WO	PCT/US2007/00	2/9/2007			Pending	WIMAX ENHANCED SLEEP MODE
MP0936	MP0936	US	11/519,121	9/11/2006	7865784	1/4/2011	In Force	Write Validation
MP0937	MP0937	US	11/649,982	1/5/2007	7921243	4/5/2011	In Force	System And Method For A DDR SDRAM Controller
MP0941	MP0941	US	11/516,191	9/6/2006	8078687	12/13/2011	In Force	System And Method For Data Management
MP0943	MP0943C1	US	12/787,484	5/26/2010	7945812	5/17/2011	In Force	System And Method For Error Correction
MP0944	MP0944	US	11/519,979	9/12/2006	7423597	9/9/2008	In Force	Dual Band WLAN Antenna
MP0944	MP0944C1	US	11/581,502	10/16/2006	7394433	7/1/2008	In Force	Dual Band WLAN Antenna
MP0944	MP0944C1C1	US	12/214,165	6/17/2008	7800547	9/21/2010	In Force	Dual Band WLAN Antenna
MP0944	MP0944C2	US	11/581,540	10/16/2006	7423599	9/9/2008	In Force	Dual Band WLAN Antenna
MP0944	MP0944C3	US	11/581,286	10/16/2006	7495621	2/24/2009	In Force	Dual Band WLAN Antenna
MP0944	MP0944C4	US	11/581,717	10/16/2006	7403162	7/22/2008	In Force	Dual Band WLAN Antenna
MP0944	MP0944C4C1	US	12/215,819	6/30/2008	7825864	11/2/2010	In Force	Dual Band WLAN Antenna
MP0944	MP0944C5	US	12/231,804	9/5/2008	7872608	1/18/2011	In Force	Dual Band WLAN Antenna
MP0944	MP0944WO	WO	PCT/US2007/00	2/9/2007			Pending	Dual Band WLAN Antenna
MP0945	MP0945C1	US	12/917,426	11/1/2010	8090932	1/3/2012	In Force	Communication Bus With Hidden Pre-Fetch Registers
MP0946	MP0946	US	11/522,726	9/18/2006	7613887	11/3/2009	In Force	System And Method For Managing A Memory Storage Device
MP0946	MP0946C1	US	12/611,131	11/3/2009	7949838	5/24/2011	In Force	System And Method For Managing A Memory Storage Device
MP0947	MP0947	US	11/507,161	8/21/2006	7805652	9/28/2010	In Force	Methods For Generating And Implementing Quasi-Cyclic Irregular Lo
MP0947	MP0947C1	US	12/891,251	9/27/2010	8051357	11/1/2011	In Force	Methods For Generating And Implementing Quasi-Cyclic Irregular Lo
MP0950	MP0950	US	11/582,014	10/16/2006	7752523	7/6/2010	In Force	REDUCED-COMPLEXITY DECODING OF PARITY CHECK CODES
MP0952	MP0952	US	11/526,398	9/25/2006	7793063	9/7/2010	In Force	Method And System For Automatic Calibration Of A DQS Signal In A S
MP0952	MP0952C1	US	12/874,349	9/2/2010	8019957	9/13/2011	In Force	Method And System For Automatic Calibration Of A DQS Signal In A S
MP0954	MP0954	US	11/541,977	10/2/2006	7471482	12/30/2008	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954C1	US	12/336,771	12/17/2008	7777981	8/17/2010	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954TW	TW	96106324	2/16/2007	I396193	5/11/2013	In Force	System And Method For Improved Insitu Spiral Writing
MP0954	MP0954WO	WO	PCT/US2007/00	2/14/2007			Pending	Method For Improved Insitu Spiral Writing
MP0954	MP0954WOCN	CN	200780006025.(	2/14/2007	ZL2007800060	3/14/2012	In Force	METHOD FOR IMPROVED INSITU SPIRAL WRITING
MP0954	MP0954WODE	DE	07750571.7	2/14/2007	1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954WOEP	EP	EP07750571.7	2/14/2007	EP1987514	12/23/2009	In Force	Method for Improved Insitu Spiral Writing
MP0954	MP0954WOEPDE	DE	07750571.7	2/14/2007	602007003957	12/23/2009	In Force	Method For Improved Insitu Spiral Writing

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MP0954	MP0954WOEPFR	FR	07750571.7	2/14/2007	1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954WOEPGB	GB	07750571.7	2/14/2007	1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954WOFR	FR	07750571.7	2/14/2007	1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954WOGB	GB	07750571.7	2/14/2007	1987514	12/23/2009	In Force	Method For Improved Insitu Spiral Writing
MP0954	MP0954WOIN	IN	1921MUMNP20	2/14/2007	270743	1/14/2016	In Force	Self-Servo-Write (SSW) System And Method For A Hard Disk Drive
MP0954	MP0954WOJP	JP	2008-555304	2/14/2007	4866919	11/18/2011	In Force	METHOD FOR IMPROVED INSITU SPIRAL WRITING
MP0954	MP0954WOKR	KR	1020087023141	2/14/2007	101321559	10/17/2013	In Force	Method For Improved Insitu Spiral Writing
MP0957	MP0957	US	11/520,531	9/12/2006	7974366	7/5/2011	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND
MP0957	MP0957CN	CN	200710100616.(	2/25/2007	ZL2007101006	2/11/2015	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEM AND M
MP0957	MP0957EP	EP	EP07003582.9	2/21/2007	EP1833052	4/10/2013	In Force	Low-Latency Baseline-Wander Compensation Systems and Method
MP0957	MP0957EPDE	DE	07003582.9	2/21/2007	6.02007E+11	4/10/2013	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND
MP0957	MP0957EPFR	FR	07003582.9	2/21/2007	1833052	4/10/2013	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND
MP0957	MP0957EPGB	GB	07003582.9	2/21/2007	1833052	4/10/2013	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND
MP0957	MP0957JP	JP	2007-037771	2/19/2007	5010938	6/8/2012	In Force	LOW -LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND
MP0957	MP0957SG	SG	200701060-6	2/16/2007			Pending	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND M
MP0957	MP0957TW	TW	96106322	2/16/2007	I 441488	6/11/2014	In Force	LOW-LATENCY BASELINE-WANDER COMPENSATION SYSTEMS AND M
MP0958	MP0958	US	11/527,940	9/26/2006	7538616	5/26/2009	In Force	A WIDEBAND LOW DISTORTION/LOW POWER DRIVER
MP0959	MP0959D1	US	13/303,926	11/23/2011	9407875	8/2/2016	In Force	Secure Video Distribution
MP0959	MP0959QADE	DE	12003430.1	10/25/2007	2485454	8/15/2018	In Force	Secure Video Distribution
MP0959	MP0959QAFR	FR	12003430.1	10/25/2007	2485454	8/15/2018	In Force	Secure Video Distribution
MP0959	MP0959QAGB	GB	12003430.1	10/25/2007	2485454	8/15/2018	In Force	Secure Video Distribution
MP0959	MP0959WO	WO	PCT/US2007/02	10/25/2007			Pending	Secure Video Distribution
MP0959	MP0959WOEP	EP	07867276.3	10/25/2007	EP2074792	12/11/2013	In Force	Secure Video Distribution
MP0959	MP0959WOJP	JP	2009-534643	10/25/2007	5245181	4/19/2013	In Force	SECURE VIDEO DISTRIBUTION
MP0959	MP0959WUD1	EP	12003430.1	10/25/2007	2485454	8/15/2018	In Force	Secure Video Distribution
MP0959	MP0959WUD2	EP	12003431.9	10/25/2007			Pending	Secure Video Distribution
MP0959	MP0959WUDE	DE	07867276.3	10/25/2007	602007034259	12/11/2013	In Force	SECURE VIDEO DISTRIBUTION
MP0959	MP0959WUFR	FR	07867276.3	10/25/2007	2074792	12/11/2013	In Force	SECURE VIDEO DISTRIBUTION
MP0959	MP0959WUGB	GB	07867276.3	10/25/2007	2074792	12/11/2013	In Force	SECURE VIDEO DISTRIBUTION
MP0960	MP0960CN	CN	200780006573.:	2/23/2007	ZL2007800065	7/16/2014	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0960	MP0960EP	EP	7789526.6	2/23/2007			Pending	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0960	MP0960JP	JP	2008-555902	2/23/2007	4929293	2/17/2012	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0960	MP0960JPD1	JP	2011-148140	2/23/2007	4929410	2/17/2012	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0960	MP0960KR	KR	10-2008-702283	2/23/2007	10-1021633	3/4/2011	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A

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MP0960	MP0960TW	TW	96105647	2/15/2007	I437560	5/11/2014	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP0960	MP0960WO	WO	PCT/IB2007/002	2/23/2007			Pending	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS '
MP0961	MP0961	US	11/521,731	9/14/2006	8049249	11/1/2011	In Force	INTEGRATED CIRCUIT DEVICES WITH ESD PROTECTION IN SCRIBE LI
MP0961	MP0961D1	US	13/285,928	10/31/2011	8372729	2/12/2013	In Force	INTEGRATED CIRCUIT DEVICES WITH ESD PROTECTION IN SCRIBE LI
MP0961	MP0961D1C1	US	13/762,208	2/7/2013	8753960	6/17/2014	In Force	INTEGRATED CIRCUIT DEVICES WITH ELECTROSTATIC DISCHARGE (E
MP0962	MP0962	US	11/541,994	10/2/2006	7400464	7/15/2008	In Force	Synchronous RRO Write
MP0962	MP0962C1	US	12/218,138	7/11/2008	7602568	10/13/2009	In Force	Synchronous RRO Write
MP0963	MP0963	US	11/504,524	8/15/2006	8457007	6/4/2013	In Force	Global Switch Resource Manager
MP0963	MP0963C1	US	13/905,776	5/30/2013	8787197	7/22/2014	In Force	Global Switch Resource Manager
MP0963	MP0963CN	CN	200710092305.0	2/25/2007	ZL2007100923	3/13/2013	In Force	GLOBAL SWITCH RESOURCE MANAGER
MP0963	MP0963DE	DE	07003496.2	2/20/2007	6.02007E+12	6/22/2011	In Force	Global Switch Resource Manager
MP0963	MP0963EP	EP	07003496.2	2/20/2007	EP1826962	6/22/2011	In Force	Global Switch Resource Manager
MP0963	MP0963FR	FR	07003496.2	2/20/2007	1826962	6/22/2011	In Force	Global Switch Resource Manager
MP0963	MP0963GB	GB	07003496.2	2/20/2007	1826962	6/22/2011	In Force	Global Switch Resource Manager
MP0963	MP0963HK	HK	81011007	2/25/2007			Pending	Global Switch Resource Manager
MP0963	MP0963SG	SG	2007011885	2/22/2007			Pending	Global Switch Resource Manager
MP0963	MP0963SG1	SG	2009057266	2/22/2007	155253	3/28/2013	In Force	Global Switch Resource Manager
MP0963	MP0963TW	TW	96106398	2/26/2007	I416908	11/21/2013	In Force	Global Switch Resource Manager And Management Method Thereof
MP0964	MP0964	US	11/526,485	9/25/2006	7425910	9/16/2008	In Force	Transmitter Digital-To-Analog Converter With Noise Shaping
MP0964	MP0964C1	US	11/636,283	12/8/2006	7471225	12/30/2008	In Force	Transmitter Digital-To-Analog Converter With Noise Shaping
MP0964	MP0964C2	US	11/636,069	12/8/2006	7436337	10/14/2008	In Force	Transmitter Digital-To-Analog Converter With Noise Shaping
MP0965	MP0965	US	09/675,194	9/29/2000	7268900	9/11/2007	In Force	PRINTER FORMATTER IN A CABLE
MP0965	MP0965C1	US	11/838,830	8/14/2007	7821664	10/26/2010	In Force	PRINTER FORMATTER IN A CABLE
MP0965	MP0965D1C1	US	12/715,022	3/1/2010	8339647	12/25/2012	In Force	PRINTER FORMATTER IN A CABLE
MP0966	MP0966	US	09/675,920	9/29/2000	7301655	11/27/2007	In Force	PRINTER FORMATTER IN A REMOVABLE CARD
MP0966	MP0966C1	US	11/846,495	8/28/2007	7944576	5/17/2011	In Force	PRINTER FORMATTER IN A REMOVABLE CARD
MP0966	MP0966C1C1	US	13/088,617	4/18/2011	9465567	10/11/2016	In Force	PRINTER FORMATTER IN A REMOVABLE CARD
MP0966	MP0966D1	US	11/846,492	8/28/2007	7907293	3/15/2011	In Force	PRINTER FORMATTER IN A REMOVABLE CARD
MP0967	MP0967	US	10/021,957	12/14/2001	6595633	7/22/2003	In Force	PRINTER PEN CARRIAGE SUPPORT
MP0967	MP0967DE	DE	02019017.9	8/26/2002	DE60206722.7	10/19/2005	In Force	PRINTER PEN CARRIAGE SUPPORT
MP0967	MP0967EP	EP	EP02019017.9	8/26/2002	EP1319515	10/19/2005	In Force	Printer pen carriage support
MP0967	MP0967FR	FR	02019017.9	8/26/2002	FR1319515	10/19/2005	In Force	PRINTER PEN CARRIAGE SUPPORT
MP0967	MP0967GB	GB	02019017.9	8/26/2002	GB1319515	10/19/2005	In Force	PRINTER PEN CARRIAGE SUPPORT
MP0967	MP0967JP	JP	2002-347163	11/29/2002	4365577	8/28/2009	In Force	PRINTER PEN CARRIAGE SUPPORT

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MP0968	MP0968	US	09/715,628	11/17/2000	6561618	5/13/2003	In Force	SERVICE STATION FOR PRINTERS HAVING FIRING NOZZLES PERPENDI
MP0968	MP0968DE	DE	01120813.9	8/29/2001	DE60122093.5	8/9/2006	In Force	A SERVICE STATION FOR PRINTERS HAVING FIRING NOZZLES PERPEI
MP0968	MP0968EP	EP	EP01120813.9	8/29/2001	EP1208993	8/9/2006	In Force	A service station for printers having firing nozzles perpendicular to
MP0968	MP0968FR	FR	01120813.9	8/29/2001	FR1208993	8/9/2006	In Force	A SERVICE STATION FOR PRINTERS HAVING FIRING NOZZLES PERPEI
MP0968	MP0968GB	GB	01120813.9	8/29/2001	GB1208993	8/9/2006	In Force	A SERVICE STATION FOR PRINTERS HAVING FIRING NOZZLES PERPEI
MP0968	MP0968JP	JP	2001-353776	11/19/2001	4236079	12/26/2008	In Force	SERVICE STATION FOR PRINTERS HAVING FIRING NOZZLES PERPEND
MP0969	MP0969	US	09/832,583	4/11/2001	6533387	3/18/2003	In Force	INKJET PRINTING SYSTEM USING SINGLE MOTOR FOR PRINT MEDIA
MP0969	MP0969DE	DE	02002179.6	1/29/2002	DE60221599.4	8/8/2007	In Force	INKJET PRINTING SYSTEM USING SINGLE MOTOR FOR PRINT MEDIA
MP0969	MP0969EP	EP	EP02002179.6	1/29/2002	EP1249352	8/8/2007	In Force	Inkjet printing system using single motor for print media advance a
MP0969	MP0969FR	FR	02002179.6	1/29/2002	1249352	8/8/2007	In Force	INKJET PRINTING SYSTEM USING SINGLE MOTOR FOR PRINT MEDIA
MP0969	MP0969GB	GB	02002179.6	1/29/2002	GB1249352	8/8/2007	In Force	INKJET PRINTING SYSTEM USING SINGLE MOTOR FOR PRINT MEDIA
MP0969	MP0969JP	JP	2002-108859	4/11/2002	4363510	8/28/2009	In Force	INKJET PRINTING SYSTEM
MP0970	MP0970DE	DE	DE20125100	6/4/2002	DE60221473.4	8/1/2007	In Force	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL
MP0970	MP0970EP	EP	EP02012510	6/4/2002	EP1291182	8/1/2007	In Force	Pen Wiping Method and System That Employs a Treadmill Belt
MP0970	MP0970EPD1	EP	EP7013413.5	6/4/2002			Pending	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL
MP0970	MP0970EPD2	EP	EP7013412.7	6/4/2002			Pending	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL
MP0970	MP0970FR	FR	FR02012510	6/4/2002	FR1291182	8/1/2007	In Force	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL
MP0970	MP0970GB	GB	GB02012510	6/4/2002	GB1291182	8/1/2007	In Force	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A THREADMILL
MP0970	MP0970JP	JP	2002-259143	9/4/2002			Pending	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS TREADMILL BEL
MP0970	MP0970JPD1	JP	2010-053204	3/10/2010	4939620	3/2/2012	In Force	PEN WIPING METHOD AND SYSTEM THAT EMPLOYS A TREADMILL BE
MP0971	MP0971	US	09/832,607	4/11/2001	6550992	4/22/2003	In Force	CORED ROLLER FOR IMAGE FORMING DEVICE
MP0971	MP0971EP	EP	EP2001703.40	1/24/2002			Pending	CORED ROLLER FOR IMAGE FORMING DEVICE
MP0971	MP0971JP	JP	2002-108860	4/11/2002	4139987	6/20/2008	In Force	CORED ROLLER FOR IMAGE FORMING DEVICE
MP0972	MP0972DE	DE	02019118.5	8/29/2002	DE60235275.4	2/3/2010	In Force	IMPROVED POWER CONNECT FOR A PRINT CARRIAGE
MP0972	MP0972EP	EP	EP02019118.5	8/29/2002	EP1308305	2/3/2010	In Force	Improved power connect for a print carriage
MP0972	MP0972FR	FR	FR02019118.5	8/29/2002	FR1308305	2/3/2010	In Force	IMPROVED POWER CONNECT FOR A PRINT CARRIAGE
MP0972	MP0972GB	GB	GB02019118.5	8/29/2002	GB1308305	2/3/2010	In Force	IMPROVED POWER CONNECT FOR A PRINT CARRIAGE
MP0972	MP0972JP	JP	2002-321052	11/5/2002	4386406	10/9/2009	In Force	IMPROVED POWER CONNECT FOR A PRINT CARRIAGE
MP0975	MP0975	US	10/365,563	2/12/2003	7280246	10/9/2007	In Force	LASER PRINT APPARATUS THAT GENERATES PULSE WIDTH VALUE AN
MP0975	MP0975C1C1	US	12/564,646	9/22/2009	8045212	10/25/2011	In Force	LASER PRINT APPARATUS THAT GENERATES PULSE WITH VALUE AND
MP0975	MP0975DE	DE	DE10350863.5	10/31/2003	DE10350863	6/18/2009	In Force	A laser printing apparatus that generates a pulse width value and an
MP0975	MP0975GB	GB	GB402801.5	2/9/2004	GB2398416	2/15/2006	In Force	LASER PRINT APPARATUS THAT GENERATES PULSE WIDTH VALUE AN
MP0976	MP0976	US	10/650,160	8/27/2003	7483167	1/27/2009	In Force	IMAGE FORMING APPARATUS FOR IDENTIFYING UNDESIRABLE TONE
MP0976	MP0976C1C1	US	13/102,305	5/6/2011	8194282	6/5/2012	In Force	IMAGE FORMING APPARATUS FOR IDENTIFYING UNDESIRABLE TONE

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MP0977	MP0977	US	10/625,971	7/23/2003	7245779	7/17/2007	In Force	IMAGE ENHANCEMENT EMPLOYING PARTIAL TEMPLATE MATCHING
MP0977	MP0977C1	US	11/827,602	7/11/2007	7412108	8/12/2008	In Force	IMAGE ENHANCEMENT EMPLOYING PARTIAL TEMPLATE MATCHINC
MP0977	MP0977JP	JP	2004-211599	7/20/2004	4436202	1/8/2010	In Force	IMAGE ENHANCEMENT EMPLOYING PARTIAL TEMPLATE MATCHINC
MP0978	MP0978	US	10/655,418	9/3/2003	7525679	4/28/2009	In Force	EFFICIENT PRINTER CONTROL ELECTRONICS
MP0978	MP0978C1	US	12/419,815	4/7/2009	8059298	11/15/2011	In Force	EFFICIENT PRINTER CONTROL ELECTRONICS
MP0978	MP0978C1C1	US	13/188,235	7/21/2011	8174723	5/8/2012	In Force	EFFICIENT PRINTER CONTROL ELECTRONICS
MP0979	MP0979C1	US	11/401,525	4/10/2006	7284828	10/23/2007	In Force	METHOD AND APPARATUS FOR COLOR FORMATTING IN A COLOR P
MP0980	MP0980	US	10/689,321	10/20/2003	7359082	4/15/2008	In Force	INDEPENDENT VIDEO HARDWARE BLOCKS TO SUPPORT LASER PRIN
MP0980	MP0980DE	DE	DE10200403173	6/30/2004	DE102004031	2/9/2017	In Force	INDEPENDENT VIDEO HARDWARE BLOCKS TO SUPPORT LASER PRIN
MP0980	MP0980JP	JP	2004-302437	10/18/2004	4883746	12/16/2011	In Force	INDEPENDENT VIDEO HARDWARE BLOCKS TO SUPPORT LASER PRIN
MP0980	MP0980JPD1	JP	2011-143352	6/28/2011	5639012	10/31/2014	In Force	INDEPENDENT VIDEO HARDWARE BLOCKS TO SUPPORT LASER PRIN
MP0980	MP0980JPD2	JP	2014-048557	3/12/2014	5878948	2/5/2016	In Force	INDEPENDENT VIDEO HARDWARE BLOCKS TO SUPPORT LASER PRIN
MP0981	MP0981	US	10/840,696	5/5/2004	7532345	5/12/2009	In Force	HOST-BASED PRINTED DRIVER
MP0981	MP0981C1	US	12/421,347	4/9/2009	8027053	9/27/2011	In Force	HOST-BASED PRINTER DRIVER
MP0983	MP0983DE	DE	DE10200500439	1/31/2005	DE102005004	9/10/2015	In Force	TWO-DIMENSIONAL CMOS SENSOR ARRAY TO IMAGE DOCUMENTS
MP0983	MP0983JPD1	JP	2009-192506	8/21/2009	5322297	7/26/2013	In Force	TWO-DIMENSIONAL CMOS SENSOR ARRAY TO IMAGE DOCUMENTS
MP0983	MP0983JPD2	JP	2013-251220	12/4/2013	5959014	7/1/2016	In Force	TWO-DIMENSIONAL CMOS SENSOR ARRAY TO IMAGE DOCUMENTS
MP0984	MP0984C1C1	US	13/097,220	4/29/2011	8125678	2/28/2012	In Force	HANDHELD PRINTER
MP0984	MP0984GB	GB	GB0610837.7	6/1/2006	GB2428025	1/7/2009	In Force	HANDHELD PRINTER
MP0984	MP0984JP	JP	2006-154187	6/2/2006	4958095	3/30/2012	In Force	HANDHELD PRINTER AND PRINTING METHOD OF USING THE SAME
MP0985	MP0985	US	10/826,738	4/16/2004	8400645	3/19/2013	In Force	PRINTER WITH SELECTABLE CAPABILITIES
MP0985	MP0985C1C1	US	14/326,331	7/8/2014	9753679	9/5/2017	In Force	PRINTER WITH SELECTABLE CAPABILITIES
MP0986	MP0986C1	US	14/200,859	3/7/2014	9237250	1/12/2016	In Force	IMAGE PROCESSING APPARATUS WITH SYMMETRIC PROCESSORS
MP0988	MP0988	US	10/897,945	7/23/2004	7302048	11/27/2007	In Force	PRINTER WITH SPEECH TRANSCRIPTION OF A RECORDED VOICE MES
MP0989	MP0989	US	10/940,863	9/13/2004	7298391	11/20/2007	In Force	LASER PRINTER FOR BRAILLE
MP0990	MP0990C1	US	11/856,478	9/17/2007	8085287	12/27/2011	In Force	LASER PRINTER WITH REDUCED BANDING ARTIFACTS
MP0990	MP0990GB	GB	GB0525467.7	12/14/2005	GB2421340	9/22/2010	In Force	LASER PRINTER WITH REDUCED BANDING ARTIFACTS
MP0990	MP0990JP	JP	2005-361270	12/15/2005	4326522	6/19/2009	In Force	LASER PRINTER WITH REDUCED BANDING ARTIFACTS
MP0991	MP0991GB	GB	GB0516071.8	8/4/2005	GB2416841	4/29/2009	In Force	METHOD AND APPARATUS FOR DETERMINING THE MECHANICAL SIN
MP0991	MP0991JP	JP	2005-226368	8/4/2005	4678677	2/10/2011	In Force	METHOD AND APPARATUS FOR DETERMINING THE MECHANICAL SIN
MP0992	MP0992	US	11/118,591	4/28/2005	7333121	2/19/2008	In Force	SYSTEM AND METHOD FOR SCALING DATA TO COMPENSATE FOR SL
MP0992	MP0992C1	US	12/018,559	1/23/2008	7701477	4/20/2010	In Force	SYSTEM AND METHOD FOR SCALING DATA TO COMPENSATE FOR SL
MP0992	MP0992GB	GB	GB0606218.6	3/28/2006	GB2425672	6/22/2011	In Force	SYSTEM AND METHOD FOR SCALING DATA TO COMPENSATE FOR SL
MP0992	MP0992JP	JP	2006-123357	4/27/2006	4384643	10/2/2009	In Force	SYSTEM AND METHOD FOR SCALING DATA TO COMPENSATE FOR SL

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MP0995	MP0995	US	11/138,128	5/26/2005	7447361	11/4/2008	In Force	SYSTEM AND METHOD FOR GENERATING A CUSTOM FONT
MP0997	MP0997	US	11/087,284	3/22/2005	7274883	9/25/2007	In Force	HYBRID PRINTER AND RELATED SYSTEM AND METHOD
MP0997	MP0997C1	US	11/903,651	9/24/2007	7570890	8/4/2009	In Force	HYBRID PRINTER AND RELATED SYSTEM AND METHOD
MP0999	MP0999GB	GB	GB0523740.9	11/22/2005	GB2420316	7/30/2008	In Force	SYSTEM AND METHOD FOR CREATING DOCUMENT FINISHES
MP0999	MP0999JP	JP	2005-336991	11/22/2005			Pending	SYSTEM AND METHOD FOR CREATING DOCUMENT FINISHES
MP1000	MP1000	US	10/990,022	11/16/2004	8050709	11/1/2011	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1000	MP1000C1	US	13/285,810	10/31/2011	8249653	8/21/2012	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1000	MP1000CN	CN	200510114985.:	11/16/2005	ZL2005101149	6/8/2011	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1000	MP1000GB	GB	GB0521654.4	10/24/2005	GB2420473	5/6/2009	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1000	MP1000JP	JP	2005-331518	11/16/2005	5131722	11/16/2012	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1000	MP1000JP2	JP	2011-182705	11/16/2005	5175376	1/11/2013	In Force	WIRELESS PRINTER CONFIGURATION MODULE
MP1002	MP1002C1	US	12/878,806	9/9/2010	8244172	8/14/2012	In Force	REMOTE HOST-BASED MEDIA PRESENTATION
MP10026	MP10026	US	15/460,676	3/16/2017			Pending	Apparatuses and Methods for Measuring Position and Velocity
MP1004	MP1004C1	US	12/781,565	5/17/2010	8098405	1/17/2012	In Force	LASER PRINT APPARATUS WITH DUAL HALFTONES
MP1004	MP1004C1C1	US	13/350,448	1/13/2012	8405877	3/26/2013	In Force	LASER PRINT APPARATUS WITH DUAL HALF TONES
MP1004	MP1004GB	GB	GB0521853.2	10/26/2005	GB2419766	12/29/2010	In Force	LASER PRINT APPARATUS WITH DUAL HALFTONES
MP1004	MP1004JP	JP	2005-298437	10/13/2005	4605782	10/15/2010	In Force	LASER PRINT APPARATUS WITH DUAL HALFTONES
MP10049	MP10049	US	15/611,332	6/1/2017	10037290	7/31/2018	In Force	Dual-Port Memories And Input/OutPut Circuits For Preventing Failure
MP1005	MP1005CN	CN	200510093895.:	8/31/2005	ZL2005100938	9/16/2009	In Force	LASER PRINT APPARATUS WITH TONER EXPLOSION COMPENSATION
MP1005	MP1005GB	GB	GB0522057.9	10/28/2005	GB2419767	5/26/2010	In Force	LASER PRINT APPARATUS WITH TONER EXPLOSION COMPENSATION
MP1005	MP1005JP	JP	2005-316279	10/31/2005			Pending	LASER PRINT APPARATUS WITH TONER EXPLOSION COMPENSATION
MP10052	MP10052	US	15/791,517	10/24/2017	10412545	9/10/2019	In Force	Device-to-Device Radio Frequency Proximity Determination
MP10052	MP10052C1	US	16/530,317	8/2/2019			Pending	Device-to-Device Radio Frequency Proximity Determination
MP10056	MP10056	US	15/672,117	8/8/2017	10389414	8/20/2019	In Force	Methods and Apparatus for Matrix Compensation to Prevent Rank D
MP1006	MP1006	US	10/976,937	10/29/2004	7538905	5/26/2009	In Force	RESOLUTION DOUBLING FOR PRINTER APPARATUS
MP1006	MP1006C1	US	12/422,001	4/10/2009	8054499	11/8/2011	In Force	RESOLUTION DOUBLING FOR PRINTER APPARATUS
MP10062	MP10062	US	15/611,628	6/1/2017	10037400	7/31/2018	In Force	INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING TH
MP10062	MP10062CN	CN	201710409483.:	6/2/2017			Pending	INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING TH
MP10062	MP10062JP	JP	2017-110491	6/2/2017			Pending	INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING TH
MP10062	MP10062KR	KR	10-2017-006895	6/2/2017			Pending	INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING TH
MP10062	MP10062TW	TW	106118347	6/2/2017			Pending	INTEGRATED CIRCUIT MANUFACTURING PROCESS FOR ALIGNING TH
MP1007	MP1007CN	CN	CN200510117512	11/2/2005			Pending	MULTIPLE-FUNCTION INKJET PRINTING SYSTEM WITH SINGLE MOTO
MP1007	MP1007GB	GB	GB513323.60	6/29/2005			Pending	MULTIPLE-FUNCTION INKJET PRINTING SYSTEM WITH SINGLE MOTO
MP10081	MP10081	US	15/812,797	11/14/2017	10187082	1/22/2019	In Force	SYSTEMS AND METHODS FOR GRAY CODING BASED ERROR CORRECT

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MP10082	MP10082	US	15/812,780	11/14/2017	10218341	2/26/2019	In Force	SYSTEMS AND METHODS FOR PHASE SYNCHRONIZATION OF LOCAL
MP10082	MP10082C1	US	16/284,773	2/25/2019			Pending	SYSTEMS AND METHODS FOR PHASE SYNCHRONIZATION OF LOCAL
MP10082	MP10082WO	WO	PCT/US17/6160	11/14/2017			Pending	SYSTEMS AND METHODS FOR PHASE SYNCHRONIZATION OF LOCAL
MP10082	MP10082WODE	DE	212017000244.:	11/14/2017	212007000244	6/18/2019	In Force	SYSTEMS AND METHODS FOR PHASE SYNCHRONIZATION OF LOCAL
MP10085	MP10085	US	15/688,606	8/28/2017	10469332	11/5/2019	In force	METHOD AND APPARATUS OF REMOTE CONFIGURATION AND MAN
MP10085	MP10085CN	CN	201710752755.(	8/28/2017			Pending	METHOD AND APPARATUS OF REMOTE CONFIGURATION AND MAN
MP10087	MP10087	US	15/692,979	8/31/2017	10244536	3/26/2019	In Force	METHOD AND APPARATUS FOR DETERMINING WHETHER A SECONI
MP1009	MP1009C1	US	11/700,518	1/31/2007	7336049	2/26/2008	In Force	METHOD AND APPARATUS FOR ADAPTIVE MOTOR SPEED CONTROL
MP10091	MP10091	US	15/691,398	8/30/2017	10257328	4/9/2019	In Force	METHOD AND APPARATUS FOR CONTROL FRAME EXTENSION
MP10092	MP10092	US	15/622,713	6/14/2017	10008228	6/26/2018	In Force	METHOD AND APPARATUS FOR DETERMINING READ-HEAD DEVIATI
MP10094	MP10094	US	15/697,230	9/6/2017	10431515	10/1/2019	In Force	Methods and Apparatus for Self-Alignment of Integrated Circuit Die
MP10094	MP10094CN	CN	201710725792.:	8/22/2017			Pending	METHODS AND APPARATUS FOR SELF-ALIGNMENT OF INTEGRATED
MP10094	MP10094TW	TW	106130044	9/1/2017			Pending	Methods and Apparatus for Self-Alignment of Integrated Circuit Die
MP10096	MP10096	US	15/688,913	8/29/2017	10459783	10/29/2019	In force	Low-Latency Decoder for Reed Solomon Codes
MP10096	MP10096CN	CN	201710765440.:	8/30/2017			Pending	LOW-LATENCY DECODER FOR REED SOLOMON CODES
MP10098	MP10098	US	15/691,268	8/30/2017			Pending	METHOD AND APPARATUS FOR RANGING
MP10099	MP10099	US	15/695,928	9/5/2017	10142960	11/27/2018	In Force	MULTI-USER RANGING WITH UNASSOCIATED STATIONS
MP10099	MP10099WO	WO	PCT/US2017/05	9/5/2017			Pending	MULTI-USER RANGING WITH UNASSOCIATED STATIONS
MP10099	MP10099WOCN	CN	201780067467.:	9/5/2017			Pending	MULTI-USER RANGING WITH UNASSOCIATED STATIONS
MP10099	MP10099WODE	DE	112017004472.:	9/5/2017			Pending	MULTI-USER RANGING WITH UNASSOCIATED STATIONS
MP10101	MP10101CN	CN	201710805209.(	9/8/2017			Pending	MULTI-CPU DEVICE WITH TRACKING OF CACHE-LINE OWNER CPU
MP10102	MP10102	US	15/652,747	7/18/2017	10128981	11/13/2018	In Force	MESSAGE-PASSING DECODER WITH FAST CONVERGENCE AND REDU
MP10102	MP10102.C1	US	16/161,328	10/16/2018			Pending	MESSAGE-PASSING DECODER WITH FAST CONVERGENCE AND REDU
MP10104	MP10104	US	15/664,621	7/31/2017	10177810	1/8/2019	In Force	SYSTEMS AND METHODS FOR ECHO OR INTERFERENCE CANCELLATI
MP10104	MP10104C1	US	16/240,037	1/4/2019			Pending	SYSTEMS AND METHODS FOR ECHO OR INTERFERENCE CANCELLATI
MP10104	MP10104CN	CN	201710780452.:	9/1/2017			Pending	SYSTEMS AND METHODS FOR ECHO OR INTERFERENCE CANCELLATI
MP10106	MP10106	US	15/689,882	8/29/2017	10277440	4/30/2019	In Force	Determining Common Phase Error
MP1011	MP1011	US	11/546,600	10/11/2006	7453221	11/18/2008	In Force	SYSTEM FOR CONTROLLING ROTATION OF A DISK DRIVE
MP10111	MP10111	US	15/690,839	8/30/2017			Pending	Methods and Apparatus for Bi-Directional Fine Timing Measurement
MP10113	MP10113	US	15/694,390	9/1/2017	10349213	7/9/2019	In Force	METHOD AND APPARATUS FOR DETERMINING AN ANGLE OF ARRIVA
MP10113	MP10113WO	WO	PCT/US2017/04	9/1/2017			Pending	METHOD AND APPARATUS FOR DETERMINING AN ANGLE OF ARRIVA
MP10113	MP10113WOCN	CN	201780063981.(	9/1/2017			Pending	METHOD AND APPARATUS FOR DETERMINING AN ANGLE OF ARRIVA
MP10113	MP10113WODE	DE	112017004430.:	9/1/2017			Pending	METHOD AND APPARATUS FOR DETERMINING AN ANGLE OF ARRIVA
MP10118	MP10118	US	15/697,464	9/7/2017	10305541	5/28/2019	In Force	Reduction of Far-End Crosstalk in High-Speed Single-Ended Receivers

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MP10118	MP10118KR	KR	10-2017-011512	9/8/2017			Pending	REDUCTION OF FAR-END CROSSTALK IN HIGH-SPEED SINGLE-ENDED
MP1012	MP1012	US	11/614,868	12/21/2006	8219886	7/10/2012	In Force	HIGH DENSITY MULTI-LEVEL MEMORY
MP1012	MP1012C1	US	13/542,353	7/5/2012	8583991	11/12/2013	In Force	HIGH DENSITY MULTI-LEVEL MEMORY
MP10123	MP10123	US	15/844,274	12/15/2017	10439701	10/8/2019	In Force	Low Complexity Beamforming with Compressed Feedback
MP10123	MP10123C1	US	16/594,856	10/7/2019			Pending	Low Complexity Beamforming with Compressed Feedback
MP10123	MP10123WO	WO	PCT/US2017/06	12/15/2017			Pending	LOW COMPLEXITY BEAMFORMING WITH COMPRESSED FEEDBACK
MP10123	MP10123WOCN	CN	201780086676.5	12/15/2017			Pending	LOW COMPLEXITY BEAMFORMING WITH COMPRESSED FEEDBACK
MP10125	MP10125	US	15/783,314	10/13/2017	10341053	7/2/2019	In Force	SYSTEMS AND METHODS FOR A LOG-LIKELIHOOD RATIO BASED DYN
MP10125	MP10125WO	WO	PCT/IB2017/001	10/13/2017			Pending	LOW COMPLEXITY BEAMFORMING WITH COMPRESSED FEEDBACK
MP10125	MP10125WOCN	CN	201780073513.1	5/28/2019			Pending	SYSTEMS AND METHODS FOR A LOG-LIKELIHOOD RATIO BASED DYN
MP10125	MP10125WODE	DE	112017005181.5	10/13/2017			Pending	SYSTEMS AND METHODS FOR A LOG-LIKELIHOOD RATIO BASED DYN
MP10127	MP10127	US	15/664,708	7/31/2017	10126769	11/13/2018	In Force	CLASS-D DRIVEN LOW-DROP-OUTPUT (LDO) REGULATOR
MP10127	MP10127EP	EP	EP17187098.3	8/21/2017			Pending	A Class-D Driven Low-Drop-Output (LDO) Regulator
MP10128	MP10128	US	15/709,186	9/19/2017	10396963	8/27/2019	In Force	FRAME FORMATS FOR MULTI-USER ACKNOWLEDGMENT INFORMATI
MP10129	MP10129	US	15/710,640	9/20/2017	10375695	8/6/2019	In Force	Spatial Reuse Transmissions in Wireless Local Area Networks (WLANS
MP10129	MP10129C1	US	16/532,016	8/5/2019			Pending	Spatial Reuse Transmissions in Wireless Local Area Networks (WLANS
MP10129	MP10129WO	WO	PCT/US2017/05	9/20/2017			Pending	SPATIAL REUSE TRANSMISSIONS IN WLANS
MP10129	MP10129WOCN	CN	201780057521.2	9/20/2017			Pending	SPATIAL REUSE TRANSMISSIONS IN WLANS
MP10129	MP10129WODE	DE	112017004721.9	9/20/2017			Pending	SPATIAL REUSE TRANSMISSIONS IN WLANS
MP1013	MP1013	US	11/455,168	6/16/2006	8028109	9/27/2011	In Force	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet In
MP1013	MP1013C1	US	13/245,269	9/26/2011	8161215	4/17/2012	In Force	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet In
MP1013	MP1013C1aa	US	13/681,885	11/20/2012	8606978	12/10/2013	In Force	Hard Disk Drive With Integrated Ethernet Interface
MP1013	MP1013C1C1	US	13/447,486	4/16/2012	8316167	11/20/2012	In Force	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet In
MP1013	MP1013C1Za	US	14/101,687	12/10/2013	9063670	6/23/2015	In Force	Hard Disk Drive With Integrated Ethernet Network Interface
MP10131	MP10131	US	15/703,940	9/13/2017	9966094	5/8/2018	In Force	QUADRATIC CURRENT SLEW CONTROL CIRCUIT
MP10132	MP10132	US	15/702,371	9/12/2017	10367758	7/30/2019	In Force	MERGING READ REQUESTS IN NETWORK DEVICE ARCHITECTURE
MP10132	MP10132C1	US	16/525,113	7/29/2019			Pending	MERGING READ REQUESTS IN NETWORK DEVICE ARCHITECTURE
MP10132	MP10132WO	WO	PCT/IB2017/055	9/12/2017			Pending	MERGING READ REQUESTS IN NETWORK DEVICE ARCHITECTURE
MP10132	MP10132WOCN	CN	201780063973.1	9/12/2017			Pending	MERGING READ REQUESTS IN NETWORK DEVICE ARCHITECTURE
MP10134	MP10134	US	15/710,707	9/20/2017			Pending	METHODS AND SYSTEMS FOR ENABLING COMMUNICATIONS FROM
MP10135	MP10135	US	15/706,519	9/15/2017			Pending	GROUP ADDRESSING FOR BEAMFORMING TRAINING
MP10136	MP10136	US	15/483,429	4/10/2017			Pending	METHOD AND APPARATUS FOR FULL-DUPLEX OPERATION IN A MULT
MP10136	MP10136WO	WO	PCT/US17/4882	8/28/2017			Pending	METHOD AND APPARATUS FOR FULL-DUPLEX OPERATION IN A MULT
MP10137	MP10137	US	15/487,004	4/13/2017	10135649	11/20/2018	In Force	SYSTEMS AND METHODS FOR PERFORMING MULTI-LEVEL CODING I

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MP10137	MP10137WO	WO	PCT/IB2017/001	8/28/2017			Pending	SYSTEMS AND METHODS FOR PERFORMING MULTI LEVEL CODING IN
MP10137	MP10137WOCN	CN	201780062777.:	8/28/2017			Pending	SYSTEMS AND METHODS FOR PERFORMING MULTI LEVEL CODING I''
MP10138	MP10138	US	15/483,241	4/10/2017	10079672	9/18/2018	In Force	METHODS AND APPARATUS FOR PERFORMING FULL-DUPLEX COMM
MP10138	MP10138WO	WO	PCT/IB2017/001	8/28/2017			Pending	METHODS AND APPARATUS FOR PERFORMING FULL-DUPLEX COMM
MP10138	MP10138WOCN	CN	201780062059.!	8/28/2017			Pending	METHODS AND APPARATUS FOR PERFORMING FULL-DUPLEX COMM
MP1013T	MP1013TW	TW	96108291	3/9/2007	I405190	8/11/2013	In Force	HARD DISK DRIVE INTEGRATED CIRCUIT WITH INTEGRATED GIGABIT
MP1013W	MP1013WO	WO	PCT/US2007/00	3/8/2007			Pending	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet
MP1013W	MP1013WOEP	EP	77524650	3/8/2007			Pending	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet
MP1013W	MP1013WOJP	JP	2008-558368	3/8/2007	4995844	5/18/2012	In Force	HARD DISK DRIVE INTEGRATED CIRCUIT WITH INTEGRATED GIGABIT
MP1013W	MP1013WOKR	KR	1020087024316	3/8/2007	101304558	8/30/2013	In Force	Hard Disk Drive Integrated Circuit With Integrated Gigabit Ethernet
MP1014	MP1014	US	11/701,338	2/1/2007	7423901	9/9/2008	In Force	Calibration System For Writing And Reading Multiple States Into Ph
MP1014	MP1014C1	US	12/283,041	9/9/2008	7679953	3/16/2010	In Force	Calibration System For Writing And Reading Multiple States Into Ph
MP10140	MP10140	US	15/706,712	9/17/2017	9998137	6/12/2018	In Force	Power-Efficient Successive-Approximation Analog-To-Digital Conver
MP10141	MP10141	US	15/703,898	9/13/2017	10330783	6/25/2019	In Force	Location Service Offload for Bluetooth Positioning
MP10142	MP10142	US	15/798,281	10/30/2017	10292012	5/14/2019	In Force	Reverse Locationing with Bluetooth Angle of Arrival (AoA)
MP10143	MP10143	US	15/710,274	9/20/2017	10459868	10/29/2019	In force	MODULAR CHIP EXPANSION BRIDGE AND CORRESPONDING METHO
MP10145	MP10145	US	15/704,724	9/14/2017	10068589	9/4/2018	In Force	SELF-SERVO-WRITE SYSTEMS FOR DETECTING INTERFERENCE CAUSE
MP10146	MP10146	US	15/708,507	9/19/2017			Pending	SYSTEMS AND METHODS FOR TRANSMITTING A WAKE-UP RADIO SIG
MP10146	MP10146WO	WO	PCT/US2017/05	9/19/2017			Pending	SYSTEMS AND METHODS FOR TRANSMITTING A WAKE-UP RADIO SIG
MP10146	MP10146WOCN	CN	201780057509.:	9/19/2017			Pending	SYSTEMS AND METHODS FOR TRANSMITTING A WAKE-UP RADIO SIG
MP10146	MP10146WODE	DE	112017004723.4	9/19/2017			Pending	SYSTEMS AND METHODS FOR TRANSMITTING A WAKE-UP RADIO SIG
MP10147	MP10147	US	15/708,668	9/19/2017	10170142	1/1/2019	In Force	System And Method For Compensating For Radial Incoherence Associ
MP1014T	MP1014TW	TW	96107192	3/2/2007	I441180	6/11/2014	In Force	CALIBRATION SYSTEM FOR WRITING AND READING MULTIPLE STATE
MP1014W	MP1014WO	WO	PCT/US2007/00	3/2/2007			Pending	Calibration System For Writing And Reading Multiple States Into Phas
MP1015	MP1015	US	11/699,138	1/26/2007	7443627	10/28/2008	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE
MP1015	MP1015C1C1	US	14/076,872	11/11/2013	9329660	5/3/2016	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE
MP1015	MP1015CN	CN	200780008171.:	3/5/2007	ZL200780008	8/26/2015	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE IN USB APPLICATION
MP10150	MP10150TW	TW	106137982	11/2/2017			Pending	TECHNIQUES AND STRUCTURES FOR SELECTABLY PASSING TRACES T
MP10152	MP10152	US	15/799,776	10/31/2017	10319727	6/11/2019	In Force	Fabricating Memory Devices with Optimized Gate Oxide Thickness
MP10152	MP10152CN	CN	201711047712.!	10/31/2017			Pending	FABRICATING MEMORY DEVICES WITH OPTIMIZED GATE OXIDE THIC
MP10152	MP10152D1	US	16/397,943	4/29/2019			Pending	Fabricating Memory Devices with Optimized Gate Oxide Thickness
MP10152	MP10152JP	JP	2017-210692	10/31/2017			Pending	FABRICATING MEMORY DEVICES WITH OPTIMIZED OXIDE GATE THIC
MP10152	MP10152KR	KR	10-2017-014255	10/30/2017			Pending	Fabricating Memory Devices with Optimized Gate Oxide Thickness
MP10152	MP10152TW	TW	106137222	10/27/2017			Pending	FABRICATING MEMORY DEVICES WITH OPTIMIZED GATE OXIDE THIC

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MP10153	MP10153	US	15/794,916	10/26/2017			Pending	SYSTEMS AND METHODS FOR EFFICIENT PASSWORD BASED PUBLIC K
MP10156	MP10156	US	15/865,871	1/9/2018			Pending	SELECTION OF TRANSMISSION MODES IN DENSE WIRELESS NETWOI
MP10156	MP10156WO	WO	PCT/US2018/01	1/9/2018			Pending	HE MULTIRATE SELECTION AND 2G4 40MHZ HE DEVICES
MP10156	MP10156WOCN	CN	201880016736.4	1/9/2018			Pending	SELECTION OF TRANSMISSION MODES IN DENSE WIRELESS NETWOI
MP10156	MP10156WOEP	EP	18714086.8	1/9/2018			Pending	SELECTION OF TRANSMISSION MODES IN DENSE WIRELESS NETWOI
MP10156	MP10156WOJP	JP	2019-537229	1/9/2018			Pending	HE MULTIRATE SELECTION AND 2G4 40MHZ HE DEVICES
MP10156	MP10156WOKR	KR	10-2019-702301	1/9/2018			Pending	SELECTION OF TRANSMISSION MODES IN DENSE WIRELESS NETWOI
MP10157	MP10157	US	15/835,200	12/7/2017	10318449	6/11/2019	In Force	SYSTEM AND METHOD FOR MEMORY ACCESS TOKEN REASSIGNMEI
MP10157	MP10157WO	WO	PCT/IB2017/001	12/7/2017			Pending	SYSTEM AND METHOD FOR MEMORY ACCESS TOKEN REASSIGNMEI
MP10157	MP10157WOCN	CN	201780084274.5	12/7/2017			Pending	SYSTEM AND METHOD FOR MEMORY ACCESS TOKEN REASSIGNMEI
MP10159	MP10159	US	15/799,627	10/31/2017			Pending	METHODS AND APPARATUS FOR FORWARDING PACKETS IN A MESH
MP1015J	MP1015JP	JP	2008-558350	3/5/2007	4616395	10/29/2010	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE IN USB APPLICATION
MP1015K	MP1015KR	KR	KR7024555/200	3/5/2007	KR10-1379493	3/24/2014	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE IN USB APPLICATION
MP1015K	MP1015KRD1	KR	KR10-2013-7025	3/5/2007	KR10-1502107	3/6/2015	In Force	LOWEST POWER MODE FOR MOBILE DRIVES IN USB APPLICATION
MP1015T	MP1015TW	TW	TW096107934	3/7/2007	TWI413113	10/21/2013	In Force	LOWEST POWER MODE FOR A MOBILE DRIVE IN USB APPLICATION
MP1015W	MP1015WO	WO	PCT/US2007/00	3/5/2007			Pending	LOWEST POWER MODE FOR A MOBILE DRIVE IN USB APPLICATION
MP1016	MP1016	US	11/820,391	6/19/2007	8086935	12/27/2011	In Force	SOFT ERROR CORRECTION FOR A DATA STORAGE MECHANISM
MP10160	MP10160	US	15/866,216	1/9/2018			Pending	Port Extender with Local Switching
MP10160	MP10160C1	US	15/878,961	1/24/2018	10469382	11/5/2019	In force	Port Extender with Local Switching
MP10160	MP10160C1C1	US	16/673,424	11/4/2019			Pending	Port Extender with Local Switching
MP10160	MP10160WO	WO	PCT/US2018/01	1/9/2018			Pending	PORT EXTENDER WITH LOCAL SWITCHING
MP10165	MP10165	US	15/818,510	11/20/2017			Pending	Methods and Apparatus for Fabricating IC Chips with Tilted Patternin
MP10165	MP10165CN	CN	201711168476.8	11/21/2017			Pending	METHOD OF TILTED PATTERNING
MP10165	MP10165JP	JP	2017-223260	11/21/2017			Pending	Methods and Apparatus for Fabricating IC Chips with Tilted Patternin
MP10165	MP10165KR	KR	10-2017-01557(	11/21/2017			Pending	METHOD OF TILTED PATTERNING
MP10165	MP10165TW	TW	106140536	11/21/2017			Pending	Methods and Apparatus for Fabricating IC Chips with Tilted Patternin
MP10167	MP10167	US	15/867,501	1/10/2018			Pending	SELECTION OF TRANSMISSION PARAMETERS FOR ACKNOWLEDGMEN
MP10168	MP10168	US	15/975,288	5/9/2018	10291437	5/14/2019	In Force	SYSTEMS AND METHODS FOR MEASURING IMPULSE RESPONSES IN A
MP1017	MP1017	US	11/544,253	10/9/2006	7349171	3/25/2008	In Force	Method For Measuring Actuator Velocity During Self-Servo-Write
MP1017	MP1017C1	US	12/077,582	3/20/2008	7567404	7/28/2009	In Force	Method For Measuring Actuator Velocity During Self-Servo-Write
MP10172	MP10172	US	15/867,067	1/10/2018			Pending	SIGNALING OF TRAINING FIELD LENGTH AND GUARD INTERVAL DURA
MP10172	MP10172WO	WO	PCT/US2018/01	1/10/2018			Pending	SIGNALING OF TRAINING FIELD LENGTH AND GUARD INTERVAL DURA
MP10172	MP10172WOCN	CN	201880013489.2	1/10/2018			Pending	SIGNALING OF TRAINING FIELD LENGTH AND GUARD INTERVAL DURA
MP10172	MP10172WOEP	EP	18701996.3	1/10/2018			Pending	SIGNALING OF TRAINING FIELD LENGTH AND GUARD INTERVAL DURA

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MP10175	MP10175	US	15/947,306	4/6/2018			Pending	Ranging Measurements in Wireless Communication Systems
MP10175	MP10175WO	WO	PCT/US2018/02	4/6/2018			Pending	RANGING MEASUREMENTS IN WIRELESS COMMUNICATION SYSTEM
MP10176	MP10176	US	15/849,213	12/20/2017	10397933	8/27/2019	In Force	Interference Cancellation for Receivers with Multiple Antennas
MP10176	MP10176C1	US	16/523,659	7/26/2019			Pending	Interference Cancellation for Receivers with Multiple Antennas
MP10177	MP10177	US	15/849,247	12/20/2017	10276198	4/30/2019	In Force	Method and Apparatus for Multi-Channel Servo Demodulation
MP1017T	MP1017TW	TW	96107634	3/6/2007	I402826	7/21/2013	In Force	System And Method For Measuring Actuator Velocity During Self-Se
MP1017W	MP1017WO	WO	PCT/US2007/00	3/6/2007			Pending	Method For Measuring Actuator Velocity
MP1017W	MP1017WOJP	JP	2008-558338	3/6/2007	4879283	12/9/2011	In Force	METHOD FOR MEASURING ACTUATOR VELOCITY
MP1017W	MP1017WOKR	KR	1020087024317	3/6/2007	101247359	3/19/2013	In Force	Method For Measuring Actuator Velocity
MP1018	MP1018	US	11/704,885	2/9/2007	8457576	6/4/2013	In Force	Cellular Phone With Integrated FM Radio And Remote Low Noise Ar
MP1018	MP1018C1	US	11/725,397	3/19/2007	8768274	7/1/2014	In Force	Cellular Phone With Integrated FM Radio And Remote Low Noise Ar
MP1018	MP1018C2	US	11/725,426	3/19/2007	8457577	6/4/2013	In Force	Cellular Phone With Integrated FM Radio And Remote Low Noise Ar
MP10180	MP10180	US	16/376,709	4/5/2019			Pending	METHOD AND APPARATUS FOR CLOCK RECOVERY
MP10181	MP10181	US	16/376,282	4/5/2019			Pending	PHASE AND FREQUENCY CONTROL FOR CLOCK-DATA RECOVERY
MP10183	MP10183	US	15/880,395	1/25/2018	10079033	9/18/2018	In Force	WRITE STRATEGY IMPROVEMENTS FOR READ CHANNEL AND PRE-A
MP10184	MP10184	US	15/955,449	4/17/2018			Pending	OPERATING MODE NOTIFICATION FOR WIRELESS COMMUNICATION
MP10184	MP10184WO	WO	PCT/US2018/02	4/17/2018			Pending	OPERATING MODE NOTIFICATION FOR WIRELESS COMMUNICATION
MP10186	MP10186	US	15/835,179	12/7/2017			Pending	System and Method for Memory Deallocation
MP10188	MP10188	US	15/947,451	4/6/2018			Pending	NULL DATA PACKET (NDP) RANGING MEASUREMENT FEEDBACK
MP10188	MP10188WO	WO	PCT/US2018/02	4/6/2018			Pending	NULL DATA PACKET (NDP) RANGING MEASUREMENT FEEDBACK
MP1018T	MP1018TW	TW	96110120	3/23/2007	I426768	2/11/2014	In Force	CELLULAR PHONE WITH INTEGRATED FM RADIO AND REMOTE LOW
MP1018W	MP1018WJP4	JP	2009-502875	3/22/2007	4944945	3/9/2012	In Force	CELLULAR PHONE, SYSTEM AND REMOTE DEVICE
MP1018W	MP1018WO	WO	PCT/US2007/00	3/22/2007			Pending	Cellular Phone With Integrated FM Radio
MP1018W	MP1018WOBR	BR	PI0709125-7	3/22/2007			Pending	CELLULAR PHONE WITH INTEGRATED WITH INTERGRATED FM RADIO
MP1018W	MP1018WOCN	CN	200780019120	3/22/2007			Pending	CELLULAR PHONE WITH INTEGRATED FM RADIO AND REMOTE LOW
MP1018W	MP1018WOEP	EP	77537629	3/22/2007	2005604	3/16/2016	In Force	Cellular Phone With Integrated FM Radio
MP1018W	MP1018WOEPDE	DE	60 2007 045 304	3/22/2007	2005604	3/16/2016	In Force	CELLULAR PHONE WITH INTEGRATED WITH INTERGRATED FM RADIO
MP1018W	MP1018WOEPFR	FR	07753762.9	3/22/2007	2005604	3/16/2016	In Force	CELLULAR PHONE WITH INTEGRATED WITH INTERGRATED FM RADIO
MP1018W	MP1018WOEPGB	GB	07753762.9	3/22/2007	2005604	3/16/2016	In Force	CELLULAR PHONE WITH INTEGRATED WITH INTERGRATED FM RADIO
MP1018W	MP1018WOKR	KR	1020087026027	3/22/2007	101424242	7/22/2014	In Force	Cellular Phone With Integrated FM Radio
MP1019	MP1019CN	CN	200780016584.3	3/6/2007	ZL2007800165	8/8/2012	In Force	IMPROVED WRITE SPLICE FOR OPTICAL RECORDING CHANNELS
MP10193	MP10193	US	16/264,020	1/31/2019			Pending	OSCILLATOR CALIBRATION STRUCTURE AND METHOD
MP10193	MP10193CN	CN	201910115151.4	2/14/2019			Pending	OSCILLATOR CALIBRATION STRUCTURE AND METHOD FOR CLOCK-DA
MP10198	MP10198	US	15/868,526	1/11/2018	10368303	7/30/2019	In Force	MULTI-CHANNEL RF SYSTEM WITH OFF-CHANNEL SCANNING

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MP10198	MP10198C1	US	16/441,283	6/14/2019			Pending	MULTI-CHANNEL RF SYSTEM WITH OFF-CHANNEL SCANNING
MP1019E	MP1019EP	EP	7752548.3	3/6/2007			Pending	IMPROVED WRITE SPLICE FOR OPTICAL RECORDING CHANNELS
MP1019J	MP1019JP	JP	2008-558380	3/6/2007	4951630	3/16/2012	In Force	IMPROVED WRITE SPLICE FOR OPTICAL RECORDING CHANNELS
MP1019K	MP1019KR	KR	10-2008-702455	10/7/2008			Pending	IMPROVED WRITE SPLICE FOR OPTICAL RECORDING CHANNELS
MP1019T	MP1019TW	TW	96107927	3/7/2007	I402828	7/21/2013	In Force	IMPROVED WRITE SPLICE FOR OPTICAL RECORDING CHANNELS
MP1019W	MP1019WO	WO	PCT/US2007/00	3/6/2007			Pending	IMPROVED WRITE SPLICE FOR OPTICAL RECORDING CHANNELS
MP1020	MP1020	US	11/640,088	12/15/2006	8160529	4/17/2012	In Force	Adaptive FM/AM Radio Architecture
MP10202	MP10202	US	16/159,334	10/12/2018			Pending	ON-CHIP INDUCTORS
MP10207	MP10207	US	15/927,280	3/21/2018			Pending	Apparatus for Super-Fine Pitch Integrated Circuit Testing and Meth
MP10212	MP10212	US	15/453,547	3/8/2017	9786319	10/10/2017	In Force	Codes and Techniques for Magnetic Recording
MP10214	MP10214	US	15/902,821	2/22/2018	10068609	9/4/2018	In Force	Variable Frequency Write Pattern Generation
MP10215	MP10215	US	15/932,298	2/16/2018	10367487	7/30/2019	In Force	Quadrature clock divider with 25%/75% duty cycle
MP10217	MP10217	US	15/913,475	3/6/2018	10348511	7/9/2019	In Force	METHODS AND APPARATUS FOR ESTABLISHING ENERGY-EFFICIENT
MP10218	MP10218	US	15/895,357	2/13/2018			Pending	PHASE LOCK LOOP CIRCUITS AND METHODS INCLUDING MULTIPLEXE
MP10220	MP10220	US	15/908,329	2/28/2018	10128856	11/13/2018	In Force	DIGITAL LOCKING LOOP CIRCUIT AND METHOD OF OPERATION
MP10220	MP10220.C1	US	16/164,014	10/18/2018	10340925	7/2/2019	In Force	DIGITAL LOCKING LOOP CIRCUIT AND METHOD OF OPERATION
MP10227	MP10227	US	15/885,551	1/31/2018	10062407	8/28/2018	In Force	MUX SELECT CONTROL OF TWO PHASE SHIFTED DATA IN WRITE PRE
MP10228	MP10228	US	15/900,002	2/20/2018	10340927	7/2/2019	In Force	Digital Phase Locked Loop System
MP10228	MP10228C1	US	16/458,672	7/1/2019			Pending	Digital Phase Locked Loop System
MP1023	MP1023	US	11/589,532	10/30/2006	7962827	6/14/2011	In Force	SYSTEMS AND METHODS FOR ACHIEVING HIGHER CODING RATE USI
MP1023	MP1023C1	US	13/112,915	5/20/2011	8225148	7/17/2012	In Force	SYSTEMS AND METHODS FOR ACHIEVING HIGHER CODING RATE USI
MP1023	MP1023C1C1	US	13/536,522	6/28/2012	8484527	7/9/2013	In Force	SYSTEMS AND METHODS FOR ACHIEVING HIGHER CODING RATE USI
MP10231	MP10231	US	15/903,876	2/23/2018	10263582	4/16/2019	In Force	Variable Gain Amplifier with Gain-Based Compensation
MP10235	MP10235	US	15/900,470	2/20/2018			Pending	Method and Apparatus for Updating Error Detection Information in P
MP10235	MP10235CN	CN	201810169266.7	2/28/2018			Pending	METHOD FOR INCREMENTAL CHECKSUM UPDATE OF ADDED VALUES
MP10236	MP10236	US	15/977,643	5/11/2018			Pending	NULL DATA PACKET (NDP) ANNOUNCEMENT FRAME AND TRIGGER F
MP1023T	MP1023TW	TW	96108050	3/8/2007			Pending	SYSTEMS AND METHODS FOR ACHIEVING HIGHER CODING RATE USI
MP1023W	MP1023WO	WO	PCT/US2007/00	3/5/2007			Pending	SYSTEMS AND METHODS FOR ACHIEVING HIGHER CODING RATE USI
MP1024	MP1024C1	US	12/916,052	10/29/2010	8144420	3/27/2012	In Force	VCM PWM CONTROL ARCHITECTURE FOR POWER SAVING IN HARD
MP10242	MP10242	US	15/466,181	3/22/2017			Pending	Wafer Level Chip Scale Package With Photodefinable 5-Sided Protecti
MP10244	MP10244	US	15/468,827	3/24/2017	10128171	11/13/2018	In Force	Leadframe With Improved Half-Etch Layout To Reduce Defects Cause
MP10246	MP10246	US	15/797,489	10/30/2017	10409507	9/10/2019	In Force	Methods and Apparatus for Implementing Virtual Dynamic Library Lo
MP10257	MP10257	US	15/996,681	6/4/2018			Pending	MULTI-TEST TYPE PROBE CARD AND CORRESPONDING TESTING SYST
MP1026	MP1026	US	11/592,357	11/2/2006	8014477	9/6/2011	In Force	RECEIVER EMPLOYING SELECTABLE A/D SAMPLE CLOCK FREQUENCY

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MP1026	MP1026C1	US	13/213,846	8/19/2011	8462887	6/11/2013	In Force	RECEIVER EMPLOYING SELECTABLE A/D SAMPLE CLOCK FREQUENCY
MP1026	MP1026C1C1	US	13/914,111	6/10/2013	8842787	9/23/2014	In Force	RECEIVER EMPLOYING SELECTABLE A/D SAMPLE CLOCK FREQUENCY
MP10260	MP10260	US	15/977,490	5/11/2018			Pending	LOW POWER WAKE UP RADIO SYNCHRONIZATION WITH LOW POW
MP10262	MP10262	US	15/938,995	3/28/2018			Pending	FLEXIBLE PROCESSOR OF A PORT EXTENDER DEVICE
MP10262	MP10262.D1	US	15/969,116	5/2/2018			Pending	FLEXIBLE PROCESSOR OF A PORT EXTENDER DEVICE
MP10262	MP10262WO	WO	PCT/IB2018/052	3/28/2018			Pending	PASSIVE INTELLIGENT PORT EXTENDER
MP10262	MP10262WOCN	CN	201880014848.(	3/28/2018			Pending	Title: FLEXIBLE PROCESSOR OF A PORT EXTENDER DEVICE
MP10263	MP10263	US	15/927,889	3/21/2018			Pending	Memory Chip Design for Manufacturing
MP10264	MP10264	US	16/177,176	10/31/2018			Pending	APPARATUS AND METHODS FOR ENHANCING SIGNALING BANDWI
MP10265	MP10265	US	15/979,100	5/14/2018	10264544	4/16/2019	In Force	STATION SELECTION FOR NULL DATA PACKET (NDP) RANGING
MP10266	MP10266	US	15/986,073	5/22/2018			Pending	SYSTEMS AND METHODS FOR MANAGING BASIC SERVICE SETS THR
MP1027	MP1027C1	US	12/620,902	11/18/2009	7924526	4/12/2011	In Force	METHOD OF RAMP STOP DETECTION FOR VCM VELOCITY-CONTROL
MP10270	MP10270	US	15/985,480	5/21/2018			Pending	WAKEUP RADIO (WUR) PREAMBLE DESIGN
MP10270	MP10270WO	WO	PCT/US2018/03	5/21/2018			Pending	WAKEUP RADIO (WUR) PREAMBLE DESIGN
MP10274	MP10274	US	16/001,648	6/6/2018			Pending	PACKETS WITH MIDAMBLES HAVING COMPRESSED OFDM SYMBOLS
MP10274	MP10274WO	WO	PCT/US2018/03	6/6/2018			Pending	MIMO-OFDM MIDAMBLE WITH COMPRESSED HE-LTF
MP10275	MP10275	US	16/005,437	6/11/2018			Pending	Methods and Apparatus for Secure Fine Timing Measurement with E
MP1028	MP1028	US	11/674,008	2/12/2007	8008137	8/30/2011	In Force	Method For Fabricating 1T-DRAM On Bulk Silicon
MP1028	MP1028CN	CN	200710100618.!	3/15/2007	ZL2007101006	1/4/2012	In Force	METHOD FOR FABRICATING IT-DRAM ON BULK SILICON
MP10283	MP10283	US	16/014,366	6/21/2018	10439695	10/8/2019	In Force	SYSTEMS AND METHODS FOR PERFORMING IMPLICIT SOUNDING IN
MP10283	MP10283C1	US	16/569,262	9/12/2019			Pending	SYSTEMS AND METHODS FOR PERFORMING IMPLICIT SOUNDING IN
MP10284	MP10284	US	16/264,411	1/31/2019			Pending	SYSTEMS AND METHODS FOR CALIBRATING IMPEDANCE OF A LOW P
MP10285	MP10285	US	16/261,116	1/29/2019			Pending	SYSTEMS AND METHODS FOR A SHORT NULL DATA PACKET TRANSMI
MP1028E	MP1028EP	EP	70054069	3/15/2007			Pending	Method For Fabricating 1T-Dram On Bulk Silicon
MP1028H	MP1028HK	HK	81020792	3/15/2007			Pending	Method For Fabricating 1T-Dram On Bulk Silicon
MP1028J	MP1028JP	JP	2007067107	3/15/2007			Pending	Method For Fabricating 1T-Dram On Bulk Silicon
MP1028T	MP1028TW	TW	96108777	3/14/2007	I423422	1/11/2014	In Force	METHOD FOR FABRICATING 1T-DRAM ON BULK SILICON
MP1029	MP1029C1	US	11/709,631	2/22/2007	7622731	11/24/2009	In Force	Cross-Point Memory Array
MP10295	MP10295	US	16/019,997	6/27/2018			Pending	Modular Memory-Like Layout For FinFET Analog Designs
MP1029T	MP1029TW	TW	96108782	3/14/2007	I418069	12/1/2013	In Force	CROSS-POINT MEMORY ARRAY
MP1029W	MP1029WO	WO	PCT/US2007/00	3/14/2007			Pending	Cross-Point Memory Array
MP1030	MP1030C1	US	12/696,558	1/29/2010	8260238	9/4/2012	In Force	SYSTEMS AND METHODS FOR RECOVERY OF A SUB-CARRIER SIGNAL
MP1030	MP1030C1C1	US	13/599,621	8/30/2012	8472906	6/25/2013	In Force	SYSTEMS AND METHODS FOR RECOVERY OF A SUB-CARRIER SIGNAL
MP10300	MP10300	US	16/030,264	7/9/2018			Pending	Wakeup Packet Modulation and Demodulation

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MP10300	MP10300WO	WO	PCT/US2018/04	7/9/2018			Pending	WAKEUP PACKET MODULATION AND DEMODULATION
MP10305	MP10305	US	16/352,886	3/14/2019			Pending	SCALING OF LOG-LIKELIHOOD RATIOS (LLR) BASED ON LONG TRAINI
MP10305	MP10305DE	DE	102019203536.:	3/15/2019			Pending	SCALING OF LOG-LIKELIHOOD RATIOS (LLR) BASED ON LONG TRAINI
MP10307	MP10307	US	16/117,991	8/30/2018			Pending	SYSTEMS AND METHODS FOR HIGH-THROUGHPUT COMPUTATIONS
MP1031	MP1031C1	US	13/288,403	11/3/2011	8792591	7/29/2014	In Force	SYSTEMS AND METHODS FOR I/Q IMBALANCE CORRECTION AND C/
MP10313	MP10313	US	16/133,456	9/17/2018	10469070	11/5/2019	In force	PAM-4 VOLTAGE-MODE DRIVER WITH STABILIZED OUTPUT
MP10314	MP10314	US	16/150,081	10/2/2018			Pending	INTERFACE CIRCUITRY
MP10317	MP10317	US	16/131,789	9/14/2018			Pending	Clear Channel Assessment for Duplex Transmissions in Wireless Loc
MP10317	MP10317CN	CN	201811084126.(	9/17/2018			Pending	CLEAR CHANNEL ASSESSMENT FOR DUPLEX MAC
MP10318	MP10318	US	16/139,778	9/24/2018			Pending	SYSTEMS AND METHODS FOR MULTI-USER RANGING USING A NULL
MP10321	MP10321	US	16/123,037	9/6/2018			Pending	SYSTEMS AND METHODS FOR CARRIER SENSING AND SYMBOL TIMI
MP10322	MP10322	US	16/149,831	10/2/2018			Pending	SYSTEMS AND METHODS FOR ERROR RECOVERY IN NAND MEMORY
MP10324	MP10324	US	16/149,825	10/2/2018			Pending	SYSTEMS AND METHODS FOR MULTI-USER OPERATION WITH DUPL
MP10324	MP10324CN	CN	201811169181.!	10/8/2018			Pending	SYSTEMS AND METHODS FOR MULTI-USER OPERATION WITH DUPLE
MP10324	MP10324DE	DE	102018216944.(	10/2/2018			Pending	SYSTEMS AND METHODS FOR MULTI-USER OPERATION WITH DUPLE
MP10326	MP10326WO	WO	PCT/US2018/04	7/30/2018			Pending	WAKEUP RADIO (WUR) PACKET PREAMBLE DESIGN
MP10328	MP10328	US	16/046,108	7/26/2018			Pending	ANGLE-OF-ARRIVAL PROCESSING IN MULTI-ANTENNA MULTI-CHANN
MP10329	MP10329	US	16/046,114	7/26/2018	10476539	11/12/2019	In force	AUTOMATIC GAIN CONTROL ADJUSTMENT IN MULTI-ANTENNA SHO
MP10331	MP10331	US	16/106,205	8/21/2018			Pending	METHOD OF CREATING ALIGNED VIAS IN ULTRA-HIGH DENSITY INTE
MP10331	MP10331CN	CN	201811116294.!	9/25/2018			Pending	METHOD OF CREATING ALIGNED VIAS IN ULTRA-HIGH DENSITY INTE
MP10331	MP10331JP	JP	2018-160009	8/29/2018			Pending	METHOD OF CREATING ALIGNED VIAS IN ULTRA-HIGH DENSITY INTE
MP10331	MP10331TW	TW	107133634	9/25/2018			Pending	METHOD OF CREATING ALIGNED VIAS IN ULTRA-HIGH DENSITY INTE
MP1034	MP1034	US	11/649,899	1/4/2007	7450040	11/11/2008	In Force	Method And Apparatus For Generating Non-Binary Balanced Codes
MP1034	MP1034C1	US	11/709,385	2/22/2007	7629903	12/8/2009	In Force	Method And Apparatus For Generating Non-Binary Balanced Codes
MP1034	MP1034CN	CN	200780018546.(	3/20/2007	ZL200780018	12/26/2012	In Force	METHOD AND APPARATUS FOR GENERATING NON-BINARY BALANCE
MP1034T	MP1034TW	TW	96109357	3/19/2007	I470940	1/21/2015	In Force	METHOD AND APPARATUS FOR GENERATING N/A-BINARY BALANCE
MP1034W	MP1034WJD1	JP	2011-223798	3/20/2007	5298174	6/21/2013	In Force	METHOD AND APPARATUS FOR GENERATING NON-BINARY BALANCE
MP1034W	MP1034WO	WO	PCT/US2007/00	3/20/2007			Pending	Method And Apparatus For Generating Non-Binary Balanced Codes
MP1034W	MP1034WOIL	IL	194154	3/20/2007	194154	2/1/2013	In Force	Method And Apparatus For Generating Non-Binary Balanced Codes
MP1034W	MP1034WOJP	JP	2009-501507	3/20/2007	4896215	1/6/2012	In Force	METHOD AND APPARATUS FOR GENERATING NON-BINARY BALANCE
MP1034W	MP1034WOKR	KR	1020087025654	3/20/2007	101321560	10/17/2013	In Force	Method And Apparatus For Generating Non-Binary Balanced Codes
MP1035	MP1035	US	11/649,486	1/4/2007	7425907	9/16/2008	In Force	Rate-7/8 Direct-Current Free And Runlength Limited Code
MP10353	MP10353	US	16/169,489	10/24/2018			Pending	Clock Tree Gating for Register Arrays
MP10354	MP10354	US	16/179,320	11/2/2018			Pending	Methods and Apparatus for Generation of Physical Layer Protocol Da

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MP10354	MP10354WO	WO	PCTUS2018/058	11/11/2018			Pending	METHODS AND APPARATUS FOR GENERATION OF PHYSICAL LAYER P
MP10355	MP10355	US	16/162,113	10/16/2018			Pending	WIFI CHANNEL AGGREGATION
MP10355	MP10355WO	WO	PCT/US2018/05	10/16/2018			Pending	WIFI CHANNEL AGGREGATION
MP1036	MP1036	US	11/704,747	2/9/2007	7701660	4/20/2010	In Force	Shock Cancellation System
MP10364	MP10364	US	16/050,082	7/31/2018			Pending	Efficient Ethernet Multi-Mode Coding and Modulation for Twisted-F
MP10369	MP10369	US	16/037,487	7/17/2018			Pending	Self-Aligned Gate-All-Around Transistors
MP1037	MP1037	US	11/710,759	2/26/2007	8054715	11/8/2011	In Force	Track Counting System And Method For Recordable Optical Media
MP1037	MP1037C1	US	13/290,685	11/7/2011	8335138	12/18/2012	In Force	Track Counting System And Method For Recordable Optical Media
MP10373	MP10373	US	16/359,968	3/20/2019			Pending	Correlation-Based Detection of Encoded Address in Packet
MP10373	MP10373DE	DE	102019203940.!	3/22/2019			Pending	CORRELATION-BASED DETECTION OF ENCODED ADDRESS IN PACKE
MP10378	MP10378	US	16/176,104	10/31/2018			Pending	TRIGGER MPDU FOR CLIENT-SELECTABLE TRANSMIT POWER
MP10383	MP10383	US	16/136,329	9/20/2018			Pending	Ethernet Transceiver with PHY-Level Signal-Loss Detector
MP10383	MP10383DE	DE	102018218952.!	11/7/2018			Pending	ETHERNET TRANSCEIVER WITH PHY-LEVEL SIGNAL-LOSS DETECTOR
MP10383	MP10383WO	WO	PCT/IB2018/057	9/26/2018			Pending	ETHERNET TRANSCEIVER WITH PHY-LEVEL SIGNAL-LOSS DETECTOR
MP10383	MP10383WOCN	CN	0	9/26/2018			Not Filed	ETHERNET TRANSCEIVER WITH PHY-LEVEL SIGNAL-LOSS DETECTOR
MP10383	MP10383WOJP	JP	2019-562552	9/26/2018			Pending	ETHERNET TRANSCEIVER WITH PHY-LEVEL SIGNAL-LOSS DETECTOR
MP10388	MP10388	US	16/161,961	10/16/2018			Pending	WAKEUP RADIO PACKET WITH NEIGHBOR ACCESS POINT INFORMATI
MP10390	MP10390	US	16/248,266	1/15/2019			Pending	Using Dipulse Response to Detect Channel Parameters
MP1040	MP1040	US	11/510,006	8/25/2006	7941640	5/10/2011	In Force	Secure Processors Having Encoded Instructions
MP10400	MP10400	US	16/182,160	11/6/2018			Pending	TWO DIMENSIONAL MAGNETIC RECORDING (TDMR) OFF-TRACK PER
MP10403	MP10403	US	16/174,918	10/30/2018			Pending	REGISTER-BASED ASYNCHRONOUS FIFO WITH ASYMMETRIC SIZE
MP10404	MP10404	US	16/176,709	10/31/2018			Pending	Methods and Apparatus for Generation of Physical Layer Protocol Da
MP10404	MP10404WO	WO	PCT/US2018/05	10/31/2018			Pending	METHODS AND APPARATUS FOR GENERATION OF PHYSICAL LAYER P
MP10407	MP10407	US	16/150,356	10/3/2018			Pending	METHOD AND APPARATUS FOR PROCESSING MULTIPLE SERVO CHAN
MP1041	MP1041C1	US	14/246,773	4/7/2014	9405730	8/2/2016	In Force	SYSTEMS AND METHODS FOR A SIGNED MAGNITUDE ADDER IN ONE'
MP10410	MP10410	US	16/037,552	7/17/2018			Pending	PHOTOLITHOGRAPHY MASK DESIGN-RULE CHECK ASSISTANCE
MP10412	MP10412	US	16/051,211	7/31/2018			Pending	BLOCK ACK FRAME WITH FLOW CONTROL
MP10412	MP10412WO	WO	PCT/US2018/04	7/31/2018			Pending	BLOCK ACK FRAME WITH FLOW CONTROL
MP10417	MP10417	US	16/190,991	11/14/2018			Pending	SYSTEMS AND METHODS FOR EFFICIENT DATA RETRIEVAL IN NON-V
MP10419	MP10419	US	16/165,244	10/19/2018	10475482	11/12/2019	In force	METHOD AND APPARATUS FOR CALIBRATING READ-HEAD OFF-TRAC
MP1042	MP1042	US	11/627,250	1/25/2007	7971130	6/28/2011	In Force	MULTI-LEVEL SIGNAL MEMORY WITH LDPC AND INTERLEAVING
MP1042	MP1042C1	US	13/169,790	6/27/2011	8869014	10/21/2014	In Force	MULTI-LEVEL SIGNAL MEMORY WITH LDPC AND INTERLEAVING
MP10421	MP10421	US	16/370,999	3/31/2019			Pending	Plastic MM-wave Waveguide with Integral Electrically-Conductive Tra
MP10421	MP10421WO	WO	PCT/IB2019/052	3/31/2019			Pending	PLASTIC MM-WAVE WAVEGUIDE WITH INTEGRAL ELECTRICALLY-CO

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Family	IP Right ID	Country	Application Number	Filing Date	Patent Number	Issue Date	Status	Title
MP10422	MP10422	US	16/144,595	9/27/2018			Pending	APPARATUS AND METHODS FOR A CONFIGURABLE WIRELESS COMM
MP10423	MP10423	US	16/264,010	1/31/2019			Pending	RAPID SYNC UP MECHANISM FOR TRAFFIC CONTROL BETWEEN TWO
MP10424	MP10424	US	16/427,573	5/31/2019			Pending	DATA TRANSFER INTERFACE WITH REDUCED SIGNALING
MP10427	MP10427	US	16/422,298	5/24/2019			Pending	HARDWARE DATA TRANSPORT IN WIRELESS DATA TRANSCEIVER
MP10428	MP10428	US	16/389,020	4/19/2019			Pending	PACKET HANDLING IN DATA RECEIVER WITH HARDWARE DATA TRA
MP1042D	MP1042DE	DE	07734144.4	3/27/2007			Pending	MULTI-LEVEL SIGNAL MEMORY WITH LDPC AND INTERLEAVING
MP1042F	MP1042FR	FR	07734144.4	3/27/2007			Pending	MULTI-LEVEL SIGNAL MEMORY WITH LDPC AND INTERLEAVING
MP1042I	MP1042IL	IL	194484	3/27/2007	194484	2/1/2013	In Force	Multi-Level Memory with LDPC Bit Interleaved Coded Modulation
MP1042J	MP1042JP	JP	2009-502244	3/27/2007	5356214	9/6/2013	In Force	MULTI-LEVEL MEMORY WITH LDPC BIT INTERLEAVED CODED MODU
MP1043	MP1043	US	11/595,053	11/10/2006	7688749	3/30/2010	In Force	Network Interface With Autonegotiation And Cable Length Measure
MP1043	MP1043C1	US	12/748,884	3/29/2010	8014313	9/6/2011	In Force	Network Interface With Autonegotiation And Cable Length Measure
MP1043	MP1043C1aa	US	13/926,562	6/25/2013	8797909	8/5/2014	In Force	Network Interface With Autonegotiation And Cable Length Measure
MP1043	MP1043C1C1	US	13/224,920	9/2/2011	8472340	6/25/2013	In Force	Network Interface With Autonegotiation And Cable Length Measure
MP10431	MP10431	US	15/973,572	5/8/2018	10469633	11/5/2019	In force	Low-Latency Pipeline for Media-To-Ethernet Frame Packaging
MP10431	MP10431WO	WO	PCT/IB2018/053	5/21/2018			Pending	LOW LATENCY PIPELINE FOR MEDIA-TO-ETHERNET FRAME PACKAGI
MP10432	MP10432	US	16/021,040	6/28/2018			Pending	Network Packet Generator Employing Multiple Header Templates an
MP10432	MP10432WO	WO	PCT/IB2018/054	6/28/2018			Pending	NETWORK PACKET GENERATOR EMPLOYING MULTIPLE HEADER TEM
MP10432	MP10432WODE	DE	202018106531	6/28/2018			Pending	NETWORK PACKET GENERATOR EMPLOYING MULTIPLE HEADER TEM
MP10433	MP10433	US	16/045,781	7/26/2018			Pending	Distributed Checksum Calculation for Communication Packets
MP10433	MP10433WO	WO	PCT/IB2018/055	7/26/2018			Pending	Distributed Checksum Calculation for Communication Packets
MP10433	MP10433WOCN	CN	0	7/26/2018			Not Filed	Distributed Checksum Calculation for Communication Packets
MP10433	MP10433WODE	DE	21 2018 000 056	7/26/2018			Pending	Distributed Checksum Calculation for Communication Packets
MP10434	MP10434	US	15/984,441	5/21/2018	10437773	10/8/2019	In Force	SPI devices with multi-master capabilities
MP10434	MP10434DE	DE	102018214015.4	8/20/2018			Pending	SPI MULTI MASTER CAPABILITY
MP10439	MP10439	US	16/051,084	7/31/2018			Pending	BLOCK ACKNOWLEDGMENT OPERATION
MP10439	MP10439WO	WO	PCT/US2018/04	7/31/2018			Pending	BLOCK ACKNOWLEDGMENT OPERATION
MP10442	MP10442	US	16/133,381	9/17/2018			Pending	COAXIAL-INTERCONNECT STRUCTURE FOR A SEMICONDUCTOR COM
MP10442	MP10442CN	CN	201811279333.[illegible]	10/30/2018			Pending	COAXIAL-INTERCONNECT STRUCTURE FOR A SEMICONDUCTOR COM
MP10443	MP10443	US	16/169,552	10/24/2018			Pending	Methods and Apparatus for Testing an Integrated Circuit
MP10443	MP10443CN	CN	201811285056.8	10/31/2018			Pending	TEST MODE METHOD TO ACCESS AND TEST EMBEDDED SOC INSIDE
MP1045	MP1045	US	11/605,797	11/29/2006	7836379	11/16/2010	In Force	Method For Computing Buffer ECC
MP10451	MP10451	US	16/175,122	10/30/2018			Pending	SYSTEMS AND METHODS FOR ETHERNET PACKET ERROR DETECTION
MP10452	MP10452	US	16/050,674	7/31/2018			Pending	ACTION FRAME TO INDICATE CHANGE IN BLOCK ACKNOWLEDGMEN
MP10452	MP10452WO	WO	PCT/US2018/04	7/31/2018			Pending	ACTION FRAME TO INDICATE CHANGE IN BLOCK ACKNOWLEDGMEN

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MP10463	MP10463	US	16/262,616	1/30/2019			Pending	Synchronizing Clocks in a Wireless Network
MP10465	MP10465	US	16/258,731	1/28/2019			Pending	MM-WAVE WAVEGUIDE PHYSICAL LAYER INTERCONNECT FOR AUTO
MP10465	MP10465.C1	US	16/299,154	3/12/2019			Pending	Accessories for MM-Wave Waveguide Physical Layer Interconnect i
MP10465	MP10465WO	WO	PCT/IB2019/050	1/28/2019			Pending	MM-WAVE WAVEGUIDE PHYSICAL LAYER INTERCONNECT FOR AUT
MP10467	MP10467	US	16/264,431	1/31/2019			Pending	SYSTEMS AND METHODS FOR GENERATING AND TRANSMITTING SI
MP10467	MP10467WO	WO	PCT/US2019/01	1/31/2019			Pending	SYSTEMS AND METHODS FOR GENERATING AND TRANSMITTING SI
MP10468	MP10468	US	16/274,529	2/13/2019			Pending	RADIO-FREQUENCY FRONT END WITH POWER AMPLIFIER DETUNIN
MP1047	MP1047	US	11/655,368	1/19/2007	7596731	9/29/2009	In Force	Test Time Reduction Algorithm
MP10472	MP10472	US	16/176,099	10/31/2018			Pending	SYSTEMS AND METHODS FOR WAKE-UP RADIO MULTI-ANTENNA TE
MP10472	MP10472WO	WO	PCT/US2018/05	10/31/2018			Pending	SYSTEMS AND METHODS FOR WAKE-UP RADIO MULTI-ANTENNA TE
MP10474	MP10474	US	16/262,639	1/30/2019			Pending	SECURE RANGING MEASUREMENT
MP10474	MP10474WO	WO	PCT/US2019/01	1/30/2019			Pending	SECURE RANGING MEASUREMENT
MP10485	MP10485	US	16/406,894	5/8/2019			Pending	FREQUENCY DIVISION MULTIPLE ACCESS (FDMA) SUPPORT FOR WA
MP10486	MP10486	US	16/263,845	1/31/2019	10497390	12/3/2019	In force	DECISION-FREE TIMING DISTURBANCE CANCELLATION CAUSED BY H
MP10487	MP10487	US	16/386,319	4/17/2019			Pending	PILOTS FOR WIRELESS ACCESS IN VEHICULAR ENVIRONMENTS
MP10487	MP10487WO	WO	PCT/IB2019/053	4/17/2019			Pending	Next Generation C-V2X Pilot Design
MP1049	MP1049	US	11/595,142	11/9/2006	8129969	3/6/2012	In Force	Hysteretic Inductive Switching Regulator With Power Supply Compen
MP1049	MP1049C1	US	13/409,294	3/1/2012	8310389	11/13/2012	In Force	Hysteretic Inductive Switching Regulator With Power Supply Compen
MP10491	MP10491	US	16/395,579	4/26/2019			Pending	SYSTEMS AND METHODS FOR DETECTING MOTION USING WIRELESS
MP10495	MP10495	US	16/399,517	4/30/2019			Pending	WIFI MULTI-BAND COMMUNICATION
MP10495	MP10495PR2	US	62/791,519	1/11/2019			Pending	FREQUENCY DIVISION DUPLEX (FDD) OVER CHANNEL AGGREGATION
MP10495	MP10495WO	WO	PCT/US2019/02	4/30/2019			Pending	Wifi multi-band communication
MP10497	MP10497	US	16/399,782	4/30/2019			Pending	PUNCTURED OPERATING CHANNELS IN WLAN
MP10497	MP10497WO	WO	PCT/US2019/03	4/30/2019			Pending	PUNCTURED OPERATING CHANNELS IN WLAN
MP1050	MP1050	US	11/585,566	10/24/2006	7466572	12/16/2008	In Force	Three Phase Voltage Tripler
MP1055	MP1055	US	11/784,482	4/6/2007	7974032	7/5/2011	In Force	NON-LINEAR TRANSITION SHIFT IDENTIFICATION AND COMPENSATI
MP1055	MP1055D1	US	13/176,318	7/5/2011	8358480	1/22/2013	In Force	NON-LINEAR TRANSITION SHIFT IDENTIFICATION AND COMPENSATI
MP1056	MP1056C1	US	12/950,779	11/19/2010	8484537	7/9/2013	In Force	Systems And Methods For Data-Path Protection
MP1056	MP1056C1C1	US	13/936,426	7/8/2013	9129654	9/8/2015	In Force	Systems And Methods For Data-Path Protection
MP1057	MP1057	US	11/728,339	3/26/2007	8495691	7/23/2013	In Force	Content Localization In A Network Device
MP1057	MP1057C1	US	13/947,806	7/22/2013	9032460	5/12/2015	In Force	Content Localization In A Network Device
MP1058	MP1058	US	11/657,492	1/24/2007	7436331	10/14/2008	In Force	Rate-28/30 DC-Free RLL Code
MP1059	MP1059	US	11/736,386	4/17/2007	7661058	2/9/2010	In Force	Efficient RAID ECC Controller for RAID Systems
MP1059	MP1059C1	US	11/805,332	5/23/2007	7657823	2/2/2010	In Force	EFFICIENT RAID ECC CONTROLLER FOR RAID SYSTEMS

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MP1059	MP1059C2	US	11/805,344	5/23/2007	8166370	4/24/2012	In Force	EFFICIENT RAID ECC CONTROLLER FOR RAID SYSTEMS
MP1059	MP1059C2aa	US	13/777,538	2/26/2013	9075745	7/7/2015	In Force	System And Method For Adding A Drive To A Storage System Having
MP1059	MP1059C2C1	US	13/454,831	4/24/2012	8386889	2/26/2013	In Force	DRIVE REPLACEMENT TECHNIQUES FOR RAID SYSTEMS
MP1061	MP1061	US	11/582,906	10/18/2006	7514990	4/7/2009	In Force	Very Low Frequency High Pass Filter
MP1061	MP1061C1	US	12/413,654	3/30/2009	8076969	12/13/2011	In Force	APPARATUS FOR ATTENUATING LOW FREQUENCY SIGNALS
MP1062	MP1062	US	11/696,476	4/4/2007	8553720	10/8/2013	In Force	Adaptive Speed Control For MAC-PHY Interfaces
MP1062	MP1062C1	US	14/047,533	10/7/2013	9210107	12/8/2015	In Force	METHOD AND APPARATUS FOR ADJUSTING A RATE AT WHICH DAT/
MP1062	MP1062C1C1	US	14/944,630	11/18/2015	9740455	8/22/2017	In Force	APPARATUS AND METHOD FOR ADJUSTING A RATE AT WHICH DAT/
MP1062I	MP1062IL	IL	182676	4/19/2007	182676	10/1/2014	In Force	Adaptive Speed Control for MAC-PHY Interfaces
MP1062T	MP1062TW	TW	96113692	4/18/2007	I418196	12/1/2013	In Force	ADAPTIVE SPEED CONTROL FOR MAC-PHY INTERFACES
MP1062W	MP1062WO	WO	PCT/US2007/00	4/19/2007			Pending	Adaptive Speed Control For MAC-PHY Interfaces
MP1062W	MP1062WOCN	CN	200780022894.:	4/19/2007	ZL2007800228	6/5/2013	In Force	ADAPTIVE SPEED CONTROL FOR MAC-PHY INTERFACES
MP1062W	MP1062WOEP	EP	07775773.0	4/19/2007	EP2016725	8/29/2012	In Force	Adaptive Speed Control for MAC-PHY Interfaces
MP1062W	MP1062WOEPDE	DE	07775773.0	4/19/2007	602007025140	8/29/2012	In Force	Adaptive Speed Control For MAC-PHY Interfaces
MP1062W	MP1062WOEPGB	GB	07775773.0	4/19/2007	2016725	8/29/2012	In Force	Adaptive Speed Control For MAC-PHY Interfaces
MP1062W	MP1062WOKR	KR	1020087028182	4/19/2007	101394592	5/7/2014	In Force	Adaptive Speed Control For MAC-PHY Interfaces
MP1063	MP1063C1	US	12/906,886	10/18/2010	8077419	12/13/2011	In Force	METHOD AND APPARATUS FOR ADJUSTING A GAIN OF A VARIABLE G
MP1064	MP1064	US	11/736,071	4/17/2007	8738013	5/27/2014	In Force	802.11 Mesh Architecture
MP1064	MP1064C1	US	11/736,730	4/18/2007	8014370	9/6/2011	In Force	802.11 MESH ARCHITECTURE
MP1064	MP1064C3	US	14/286,320	5/23/2014	9479428	10/25/2016	In Force	Cumulative Route Metrics For Wireless Mesh Network Routing
MP1064J	MP1064J3D1	JP	2012-065431	3/22/2012	5574190	7/11/2014	In Force	IMPROVED 802.11 MESH ARCHITECTURE
MP1064J	MP1064JP03	JP	2009-507750	4/24/2007	4963318	4/6/2012	In Force	IMPROVED 802.11 MESH ARCHITECTURE
MP1064W	MP1064WO	WO	PCT/US2007/00	4/24/2007			Pending	Improved 802.11 Mesh Architecture
MP1064W	MP1064WOCN	CN	200780023801.:	4/24/2007	ZL2007800238	5/28/2014	In Force	IMPROVED 802.11 MESH ARCHITECTURE
MP1064W	MP1064WOEP	EP	EP07755934.2	4/24/2007	EP2011287	3/29/2017	In Force	Improved 802.11 Mesh Architecture
MP1064W	MP1064WOEPDE	DE	07755934.2	4/24/2007	602007050393	3/29/2017	In Force	Improved 802.11 Mesh Architecture
MP1064W	MP1064WOEPFR	FR	07755934.2	4/24/2007	2011287	3/29/2017	In Force	Improved 802.11 Mesh Architecture
MP1064W	MP1064WOEPGB	GB	07755934.2	4/24/2007	2011287	3/29/2017	In Force	Improved 802.11 Mesh Architecture
MP1064W	MP1064WOKR	KR	1020087028743	4/24/2007	101362776	2/5/2014	In Force	Improved 802.11 Mesh Architecture
MP1065	MP1065	US	11/789,334	4/24/2007	7934143	4/26/2011	In Force	Parity Insertion For Inner Architecture
MP1065	MP1065C1	US	13/089,733	4/19/2011	8140943	3/20/2012	In Force	Parity Insertion For Inner Architecture
MP1065	MP1065C1aa	US	14/035,260	9/24/2013	8788911	7/22/2014	In Force	Parity Insertion For Inner Architecture
MP1068	MP1068	US	11/786,086	4/9/2007	7616142	11/10/2009	In Force	SIGMA-DELTA MODULATED ANALOG-TO-DIGITAL CONVERTER HAVIN
MP1069	MP1069	US	11/784,291	4/6/2007	8010883	8/30/2011	In Force	Read Channel Detector For Noise Cancellation

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MP1069	MP1069C1	US	13/219,917	8/29/2011	8413032	4/2/2013	In Force	Read Channel Detector For Noise Cancellation
MP1070	MP1070	US	11/809,670	6/1/2007	8028216	9/27/2011	In Force	Embedded Parity Coding For Data Storage
MP1070	MP1070C1	US	11/827,213	7/11/2007	8255765	8/28/2012	In Force	Embedded Parity Coding For Data Storage
MP1070	MP1070C2	US	11/827,206	7/11/2007	8255764	8/28/2012	In Force	Embedded Parity Coding For Data Storage
MP1070	MP1070C3	US	11/827,339	7/11/2007	8156400	4/10/2012	In Force	Embedded Parity Coding For Data Storage
MP1071	MP1071	US	11/799,488	5/1/2007	8090059	1/3/2012	In Force	Multi-Viterbi Receive Channel Decoder
MP1071	MP1071C1	US	13/335,267	12/22/2011	8594246	11/26/2013	In Force	Multi-Viterbi Receive Channel Decoder
MP1071	MP1071C1C1	US	14/088,777	11/25/2013	8873614	10/28/2014	In Force	METHOD AND APPARATUS FOR USING DUAL-VITERBI ALGORITHMS
MP1077	MP1077	US	11/303,785	12/16/2005	7319356	1/15/2008	In Force	MULTIPLEXER CIRCUIT WITH COMBINED LEVEL SHIFTING AND DELA
MP1082	MP1082	US	11/369,778	3/7/2006	7570725	8/4/2009	In Force	NUMERICALLY CONTROLLED OSCILLATOR
MP1084	MP1084CN	CN	200780014068.:	4/18/2007			Pending	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AN
MP1084I	MP1084IN	IN	2144/MUMNP/2	10/3/2008			Pending	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AN
MP1084J	MP1084JP	JP	2009-506583	4/18/2007			Pending	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AN
MP1084K	MP1084KR	KR	10-2008-702656	10/29/2008			Pending	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AND
MP1084W	MP1084WO	WO	PCT/US2007/00	4/18/2007			Pending	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AND
MP1091	MP1091	US	11/788,250	4/18/2007	7835429	11/16/2010	In Force	TRANSMIT SPECTRUM FOR A DIGITAL SUBSCRIBER LINE SIGNAL
MP1097	MP1097	US	11/729,471	3/29/2007	7724067	5/25/2010	In Force	Enhanced Transmission Gate
MP1098	MP1098	US	11/789,196	4/24/2007	7586429	9/8/2009	In Force	Scrambling System For High Resolution Digital-To-Analog Converter
MP1099	MP1099	US	11/809,373	6/1/2007	7504892	3/17/2009	In Force	Charge-Pump For Phase-Locked Loop
MP1100	MP1100	US	11/738,263	4/20/2007	7649793	1/19/2010	In Force	CHANNEL ESTIMATION FOR MULTI-LEVEL MEMORIES USING PILOT SI
MP1100	MP1100C1	US	12/689,891	1/19/2010	7936630	5/3/2011	In Force	METHOD AND APPARATUS FOR CALIBRATING A READ/WRITE CHANN
MP1100	MP1100C1AA	US	13/752,121	1/28/2013	8531903	9/10/2013	In Force	METHOD AND APPARATUS FOR CALIBRATING A READ/WRITE CHANN
MP1100	MP1100C1C1	US	13/099,997	5/3/2011	8363501	1/29/2013	In Force	METHOD AND APPARATUS FOR CALIBRATING A READ/WRITE CHANN
MP1100	MP1100C1Za	US	14/021,712	9/9/2013	8693275	4/8/2014	In Force	METHOD AND APPARATUS FOR CALIBRATING A READ/WRITE CHANN
MP11005	MP11005	US	16/257,860	1/25/2019			Pending	APPARATUS AND METHODS FOR TEMPERATURE-BASED MEMORY M
MP11007	MP11007	US	16/545,966	8/20/2019			Pending	PULSE-BASED WRITING FOR MAGNETIC STORAGE MEDIA
MP11008	MP11008	US	16/547,282	8/21/2019			Pending	SYSTEMS AND METHODS FOR RATE SELECTION AND RESOURCE UNIT
MP11009	MP11009	US	16/547,337	8/21/2019			Pending	SYSTEMS AND METHODS FOR DETECTING MOTION BASED ON CHAN
MP1101	MP1101	US	11/800,554	5/4/2007	7589649	9/15/2009	In Force	APPARATUS, METHOD, AND SYSTEM FOR CORRECTION OF BASELINE
MP11015	MP11015	US	16/372,905	4/2/2019			Pending	Sub-Device Field-Effect Transistor Architecture for Integrated Circuits
MP11019	MP11019	US	16/262,487	1/30/2019			Pending	RECONFIGURABLE ANALOG FILTER WITH OFFSET COMPENSATION
MP1102	MP1102	US	11/745,254	5/7/2007	7685494	3/23/2010	In Force	ERROR CORRECTION CODING FOR VARYING SIGNAL-TO-NOISE RATIO
MP1102	MP1102C1C1	US	13/179,429	7/8/2011	8683274	3/25/2014	In Force	ERROR CORRECTION CODING FOR VARYING SIGNAL-TO-NOISE RATIO
MP1103	MP1103	US	11/591,326	11/1/2006	7911901	3/22/2011	In Force	Magnetic And Optical Rotating Storage Systems With Audio Monitori

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MP1103	MP1103C1C1	US	13/155,018	6/7/2011	8467279	6/18/2013	In Force	Magnetic And Optical Rotating Storage Systems With Audio Monitori
MP1103	MP1103C2	US	11/652,258	1/11/2007	7890196	2/15/2011	In Force	Magnetic And Optical Rotating Storage Systems With Audio Monito
MP1103	MP1103C2TW	TW		7/20/2007	I455119	10/1/2014	In Force	Rotating Data Storage Device Having Audio Monitoring Module
MP11032	MP11032	US	16/545,486	8/20/2019			Pending	MULTI-RADIO DEVICE AND METHOD OF OPERATION THEREOF
MP1103T	MP1103TW	TW	96126623	7/20/2007	I433140	4/1/2014	In Force	Magnetic And Optical Rotating Storage Systems With Audio Monito
MP1103W	MP1103WO	WO	PCT/US2007/01	7/11/2007			Pending	Magnetic And Optical Rotating Storage Systems With Audio Monito
MP1103W	MP1103WOKR	KR	1020097002546	7/11/2007	101482199	1/7/2015	In Force	Magnetic And Optical Rotating Storage Systems With Audio Monito
MP1104	MP1104C1	US	12/896,358	10/1/2010	8102740	1/24/2012	In Force	OFFSET LOOP FOR WOBBLE
MP1104	MP1104C1C1	US	13/355,652	1/23/2012	8315135	11/20/2012	In Force	OFFSET LOOP FOR WOBBLE
MP1104	MP1104I1	US	12/484,417	6/15/2009	8355302	1/15/2013	In Force	OFFSET LOOP FOR WOBBLE
MP11043	MP11043	US	16/527,302	7/31/2019			Pending	METHOD AND APPARATUS FOR AUTHORIZING UNLOCKING OF A DE
MP11049	MP11049	US	16/263,387	1/31/2019			Pending	SYSTEMS AND METHODS FOR GENERATING METADATA DESCRIBIN(
MP1105	MP1105	US	09/903,201	7/10/2001	7248381	7/24/2007	In Force	POINT-OF-SALE DEMONSTRATION OF COMPUTER PERIPHERALS
MP1105	MP1105C1	US	11/716,096	3/9/2007	8237957	8/7/2012	In Force	POINT-OF-SALE DEMONSTRATION OF COMPUTER PERIPHERALS
MP11050	MP11050	US	16/518,564	7/22/2019			Pending	ARTIFICIAL INTELLIGENCE-ENABLED SEARCH FOR A STORAGE SYSTEM
MP11058	MP11058	US	16/570,360	9/13/2019			Pending	SYSTEMS AND METHODS FOR UPLINK POWER CONTROL AND RATE A
MP1106	MP1106	US	10/630,506	7/29/2003	7025266	4/11/2006	In Force	DEVICE AND METHOD FOR DIGITIZING A SERIALIZED SCANNER OUTP
MP1106	MP1106GB	GB	GB414307.9	6/25/2004	GB2404514	3/14/2007	In Force	DEVICE AND METHOD FOR DIGITIZING A SERIALIZED SCANNER OUTP
MP1106	MP1106RE	US	11/701,272	1/31/2007	RE43511	7/17/2012	In Force	DEVICE AND METHOD FOR DIGITIZING A SERIALIZED SCANNER OUTP
MP11060	MP11060	US	16/264,473	1/31/2019			Pending	STORAGE EDGE CONTROLLER WITH A METADATA COMPUTATIONAL
MP11061	MP11061	US	16/561,047	9/5/2019			Pending	MULTI-PROTOCOL FRAME FORMAT
MP11065	MP11065	US	16/264,248	1/31/2019			Pending	STORAGE AGGREGATOR CONTROLLER WITH METADATA COMPUTATI
MP11066	MP11066	US	16/533,376	8/6/2019			Pending	ENABLING VIRTUAL FUNCTIONS ON STORAGE MEDIA
MP11067	MP11067	US	16/532,272	8/5/2019			Pending	Non-Volatile Memory Switch with Host Isolation
MP11068	MP11068	US	16/533,243	8/6/2019			Pending	VIRTUALIZING ISOLATION AREAS OF SOLID-STATE STORAGE MEDIA
MP1106J	MP1106JP	JP	2004-211476	7/20/2004	JP4582535	9/10/2010	In Force	DEVICE AND METHOD FOR DIGITIZING A SERIALIZED SCANNER OUTP
MP1106J	MP1106JPD1	JP	2010-105857	4/30/2010	4922430	2/10/2012	In Force	DEVICE AND METHOD FOR DIGITIZING A SERIALIZED SCANNER OUTP
MP11070	MP11070	US	16/532,081	8/5/2019			Pending	Managed Switching Between One or More Hosts and Solid State Driv
MP11071	MP11071	US	16/533,399	8/6/2019			Pending	APPARATUS AND METHODS FOR PROVIDING QUALITY OF SERVICE O
MP11072	MP11072	US	16/532,186	8/5/2019			Pending	Modifying NVMe Physical Region Page List Pointers and Data Pointer
MP11073	MP11073	US	16/262,975	1/31/2019			Pending	METADATA GENERATION AT THE STORAGE EDGE
MP11074	MP11074	US	16/262,971	1/31/2019			Pending	METADATA GENERATION FOR MULTIPLE OBJECT TYPES
MP11078	MP11078	US	16/594,025	10/6/2019			Pending	Dispersion Compensation in MM-Wave Communication over Plastic
MP11079	MP11079	US	16/590,581	10/2/2019			Pending	Relay Circuit for Reducing a Voltage Glitch During Device Testing

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MP1108	MP1108	US	10/680,018	10/6/2003	8368950	2/5/2013	In Force	METHOD OF PRE-PROCESSING DATA ON A HOST MACHINE FOR A HO
MP1108D	MP1108DE	DE	DE10200403059	6/24/2004	DE102004030!	8/9/2007	In Force	METHOD OF PRE-PROCESSING DATA ON A HOST MACHINE FOR A H
MP1108J	MP1108JP	JP	2004-291198	10/4/2004			Pending	METHOD OF PRE-PROCESSING DATA ON A HOST MACHINE FOR A H
MP1108J	MP1108JPD1	JP	2011-050630	3/8/2011	5480835	2/21/2014	In Force	METHOD OF PRE-PROCESSING DATA ON A HOST MACHINE FOR A H
MP11092	MP11092	US	16/571,117	9/15/2019			Pending	Adaptive Gain Control for Indoor Positioning
MP1110	MP1110	US	11/087,453	3/23/2005	7733375	6/8/2010	In Force	SETTING IMAGER PARAMETERS BASED ON CONFIGURATION PATTEI
MP11110	MP11110PR	US	62/770,077	11/20/2018			Pending	PROBLEMS IN SYMMETRIC KEY METHOD FOR MULTI-AP NETWORKS
MP11110	MP11110WO	WO	PCT/IB2019/059	11/19/2019			Pending	CRYPTOGRAPHIC SECURITY IN MULTI-ACCESS POINT NETWORKS
MP11112	MP11112	US	16/261,293	1/29/2019			Pending	WiFi Receiver Architecture
MP11112	MP11112PR	US	62/782,177	12/19/2018			Pending	ARCHITECTURE OF RECEIVER FOR WIRELESS NETWORKS
MP11141	MP11141	US	15/929,191	12/19/2019			Pending	NON-DESTRUCTIVE EYE OPENING MONITOR (EOM) IN ANALOG-TO-
MP11141	MP11141PR	US	62/782,192	12/19/2018			Pending	NON-DESTRUCTIVE EYE OPENING MONITOR (EOM) IN ANALOG-TO-
MP11144	MP11144	US	16/715,559	12/16/2019			Pending	Zone Self Servo Writing With Synchronized Parallel Clocks
MP1116	MP1116	US	11/751,687	5/22/2007	8164773	4/24/2012	In Force	Wireless System-In-Package And Image Processing Control Apparatus
MP1116	MP1116C1	US	13/453,607	4/23/2012	8477357	7/2/2013	In Force	Wireless System-In-Package And Corresponding Wireless Printer Syst
MP1116	MP1116C1aa	US	14/154,480	1/14/2014	9244644	1/26/2016	In Force	Methods For Operating A Circuit Board And An Apparatus Each Havin
MP1116	MP1116C1C1	US	13/927,735	6/26/2013	8630012	1/14/2014	In Force	Apparatus And System On A Chip For Printing Based On Processing Of
MP1116	MP1116CN	CN	200780028260	5/24/2007			Pending	WIRELESS SYSTEM-IN-PACKAGE AND IMAGE PROCESSING CONTROL
MP1116	MP1116Zaa	US	15/001,714	1/20/2016	9665329	5/30/2017	In Force	Printer, A System-In-Package And Method For Wirelessly Initiating Pri
MP11161	MP11161PR	US	62/872,980	7/11/2019			Pending	Phase Roll based Accurate Timing Estimation for 11ax
MP11164	MP11164	US	16/262,355	1/30/2019			Pending	operation with bandwidth-limited devices in a wireless network
MP11164	MP11164DE	DE	102019201253.:	1/31/2019			Pending	HE 20MHZ ONLY DEVICES IN 5GHZ BAND
MP11164	MP11164WO	WO	PCT/US2019/01	1/30/2019			Pending	HE 20MHZ ONLY DEVICES IN 5GHZ BAND
MP1116W	MP1116WO	WO	PCT/US2007/01	5/24/2007			Pending	Wireless System-In-Package And Image Processing Control Apparatus
MP1116W	MP1116WOEP	EP	07795293.5	5/24/2007	EP2021183	8/4/2010	In Force	Wireless Printer System
MP1116W	MP1116WOEPDE	DE	07795293.5	5/24/2007	602007008231	8/4/2010	In Force	Wireless Printer System
MP1116W	MP1116WOEPFR	FR	77952935	5/24/2007	2021183	8/4/2010	In Force	Wireless Printer System
MP1116W	MP1116WOEPGB	GB	07795293.5	5/24/2007	2021183	8/4/2010	In Force	Wireless Printer System
MP1116W	MP1116WOJP01	JP	2009-512154	5/24/2007			Pending	WIRELESS SYSTEM-IN-PACKAGE AND IMAGE PROCESSING CONTROL
MP1118	MP1118	US	11/368,656	3/6/2006	7428336	9/23/2008	In Force	EFFICIENT DETECTION OF CONSTANT REGIONS OF AN IMAGE
MP1118	MP1118C1aa	US	13/549,036	7/13/2012	8363954	1/29/2013	In Force	EFFICIENT DETECTION OF CONSTANT REGIONS OF AN IMAGE
MP1118	MP1118C1C1	US	13/016,454	1/28/2011	8224096	7/17/2012	In Force	EFFICIENT DETECTION OF CONSTANT REGIONS OF AN IMAGE
MP1119	MP1119	US	11/229,774	9/19/2005	7517039	4/14/2009	In Force	ENABLING INCREASED PRINT SPEED BY ELIMINATING NOZZLE FIRING
MP1119	MP1119CN	CN	200610127592.!	9/19/2006	ZL2006101275	3/21/2012	In Force	ENABLING INCREASED PRINT SPEED BY ELIMINATING NOZZLE FIRING

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MP1119J	MP1119JP	JP	2006-249285	9/14/2006	5480460	2/21/2014	In Force	ENABLING INCREASED PRINT SPEED BY ELIMINATING NOZZLE FIRING
MP1119J	MP1119JPD1	JP	2013-124041	6/12/2013			Pending	INK SUPPLY FOR A HAND-HELD INK JET PRINTER
MP1120	MP1120CN	CN	CN200610140935	10/17/2006			Pending	SYSTEM AND METHOD FOR INDICATING POSITION OF A MOVEABLE
MP1120J	MP1120JP	JP	2006-274156	10/5/2006			Pending	SYSTEM AND METHOD FOR INDICATING POSITION OF A MOVEABLE
MP1121	MP1121D1	US	12/915,770	10/29/2010	8100510	1/24/2012	In Force	INKJET PRINTHEAD SYSTEM AND METHOD USING LASER-BASED HEA
MP1122	MP1122C1	US	12/836,758	7/15/2010	8333324	12/18/2012	In Force	METHODS AND SYSTEMS FOR TRANSFERRING INFORMATION BETW
MP1122	MP1122C1aa	US	14/094,345	12/2/2013	9167115	10/20/2015	In Force	METHODS AND SYSTEMS FOR TRANSFERRING INFORMATION BETW
MP1124	MP1124C1	US	12/643,634	12/21/2009	7918568	4/5/2011	In Force	LID STRUCTURE, APPARATUS AND METHOD FOR DISPLAYING GRAPI
MP1125	MP1125	US	11/400,082	4/7/2006	7798407	9/21/2010	In Force	SCANNING APPARATUS AND METHOD FOR ENTERING A USER INPU
MP1125	MP1125C1	US	12/871,681	8/30/2010	8496176	7/30/2013	In Force	SCANNING APPARATUS AND METHOD FOR ENTERING A USER INPU
MP1126	MP1126C1	US	13/225,011	9/2/2011	8223407	7/17/2012	In Force	SHEET MEDIA STACK IMAGING SYSTEM
MP1126	MP1126C1C1	US	13/547,891	7/12/2012	8514469	8/20/2013	In Force	SHEET MEDIA STACK IMAGING SYSTEM
MP1134	MP1134	US	11/741,380	4/27/2007	7535305	5/19/2009	In Force	QUIESCENT CURRENT DETECTING CIRCUIT FOR CLASS AB AMPLIFIEF
MP1135	MP1135C1	US	12/842,687	7/23/2010	8085486	12/27/2011	In Force	METHOD FOR INTERFACING NON-VOLATILE MEDIUM CONTROL SYST
MP1138	MP1138	US	11/803,781	5/16/2007	8332201	12/11/2012	In Force	INNOVATIVE VERIFICATION METHODOLOGY FOR DEEPLY EMBEDDED
MP1139	MP1139	US	11/789,170	4/24/2007	7474153	1/6/2009	In Force	Dual Stage Source/Sink Amplifier Circuit With Quiescent Current Dete
MP1139	MP1139C1	US	12/337,955	12/18/2008	7764122	7/27/2010	In Force	DUAL STAGE SOURCE / SINK AMPLIFIER CIRCUIT WITH QUIESCENT C
MP1140	MP1140C1	US	12/730,954	3/24/2010	8121436	2/21/2012	In Force	METHODS AND SYSTEMS FOR IMAGE SCALING
MP1140	MP1140C1C1	US	13/399,411	2/17/2012	8306363	11/6/2012	In Force	METHODS AND SYSTEMS FOR IMAGE SCALING
MP1141	MP1141C1	US	12/714,833	3/1/2010	8214706	7/3/2012	In Force	Method And Apparatus For Testing An Electronic Circuit Integrated
MP1141J	MP1141JP	JP	2006-176036	6/27/2006	5179726	1/18/2013	In Force	SEMICONDUCTOR DEVICE
MP1142	MP1142	US	11/789,867	4/25/2007	7836364	11/16/2010	In Force	Circuits, Architectures, Apparatuses, Systems, Methods, Algorithms,
MP1142	MP1142C1	US	12/916,874	11/1/2010	8117510	2/14/2012	In Force	Circuits, Architectures, Apparatuses, Systems, Methods, Algorithms,
MP1142	MP1142C1aa	US	13/608,748	9/10/2012	8495442	7/23/2013	In Force	Circuits, Architectures, Apparatuses, Systems, Algorithms, Software A
MP1142	MP1142C1C1	US	13/371,890	2/13/2012	8266484	9/11/2012	In Force	Circuits, Architectures, Apparatuses, Systems, Methods, Algorithms,
MP1142	MP1142C1Za	US	13/947,836	7/22/2013	8745453	6/3/2014	In Force	Circuits, Architectures, Apparatuses, Systems, Methods, Algorithms,
MP1144	MP1144	US	11/755,665	5/30/2007	7986445	7/26/2011	In Force	PRINT AND SCAN SHARING PAPER PICK AND PAPER MOTION CONTR
MP1144W	MP1144WO	WO	PCT/US2007/01	6/1/2007			Pending	PRINT AND SCAN SHARING PAPER PICK AND PAPER MOTION CONTR
MP1145W	MP1145WO	WO	PCT/US2007/01	6/1/2007			Pending	FLOATING SCAN SENSOR IN A MULTI-FUNCTION PRINTER
MP1146	MP1146C1	US	12/961,887	12/7/2010	8164363	4/24/2012	In Force	AYSMMETRIC SENSE-AMP FLIP-FLOP
MP1147	MP1147	US	11/655,670	1/19/2007	8359416	1/22/2013	In Force	Multi-Mode USB-Satellite Digital Transceiver Port
MP1148	MP1148	US	11/810,762	6/7/2007	8074126	12/6/2011	In Force	Non-Intrusive Eye Monitor System
MP1148	MP1148C1	US	13/300,354	11/18/2011	8327195	12/4/2012	In Force	Non-Intrusive Eye Monitor System
MP1148	MP1148C1C1	US	13/693,579	12/4/2012	8694837	4/8/2014	In Force	NON-INTRUSIVE EYE MONITOR SYSTEM

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MP1150	MP1150	US	11/810,832	6/6/2007	7974034	7/5/2011	In Force	SYSTEMS AND METHODS FOR SYNC MARK DETECTION USING CORRE
MP1150	MP1150C1	US	13/160,068	6/14/2011	8279546	10/2/2012	In Force	SYSTEMS AND METHODS FOR SYNC MARK DETECTION USING CORR
MP1151	MP1151C1	US	12/972,733	12/20/2010	8331206	12/11/2012	In Force	CALIBRATION BASED DC COUPLED ANALOG FRONT END FOR OPTIC/
MP1152	MP1152	US	11/752,261	5/22/2007	7729679	6/1/2010	In Force	DYNAMIC BANDWIDTH FILTER FOR AN FM RECEIVER
MP1154	MP1154	US	11/801,317	5/9/2007	7535110	5/19/2009	In Force	Stack Die Packages
MP1154	MP1154C1	US	12/434,264	5/1/2009	7825521	11/2/2010	In Force	Stack Die Packages
MP1154	MP1154WOEPDE	DE	07809502.3	6/13/2007	602007059371	10/16/2019	In Force	Stack Die Packages
MP1154	MP1154WOEPFR	FR	07809502.3	6/13/2007	2033220	10/16/2019	In Force	Stack Die Packages
MP1154	MP1154WOEPGB	GB	07809502.3	6/13/2007	2033220	10/16/2019	In Force	Stack Die Packages
MP1154T	MP1154TW	TW	96121889	6/15/2007	I429050	3/1/2014	In Force	STACK DIE PACKAGES
MP1154W	MP1154WO	WO	PCT/US2007/01	6/13/2007			Pending	Stack Die Packages
MP1154W	MP1154WOCN	CN	200780030487.(	6/13/2007	ZL2007800304	4/6/2011	In Force	STACK DIE PACKAGES
MP1154W	MP1154WOEP	EP	07809502.3	6/13/2007			Pending	Stack Die Packages
MP1154W	MP1154WOJP	JP	2009-515464	6/13/2007	5320611	7/26/2013	In Force	STACK DIE PACKAGES
MP1157	MP1157	US	11/803,122	5/11/2007	8284411	10/9/2012	In Force	FASTER MONOCHROME PRINTING FOR IN-LINE ELECTROPHOTOGRA
MP1157	MP1157aaa	US	14/589,580	1/5/2015	9330348	5/3/2016	In Force	FASTER MONOCHROME PRINTING FOR IN-LINE ELECTROPHOTOGRA
MP1157	MP1157C1	US	13/613,465	9/13/2012	8462363	6/11/2013	In Force	FASTER MONOCHROME PRINTING FOR IN-LINE ELECTROPHOTOGRA
MP1158	MP1158C1	US	14/170,188	1/31/2014	9542716	1/10/2017	In Force	NON RASTER ROW PIXEL PROCESSING
MP1160	MP1160	US	11/810,667	6/6/2007	7876746	1/25/2011	In Force	Remote Management For Network Switches
MP1160	MP1160I2	US	11/893,424	8/16/2007	7852768	12/14/2010	In Force	Physical-Layer Device Management For Network Switches
MP1161	MP1161	US	11/801,845	5/11/2007	8161207	4/17/2012	In Force	COMMON BLOCK INTERFACE FOR DATA AND CONTROL WITH HANDS
MP1161	MP1161C1	US	13/446,065	4/13/2012	8443121	5/14/2013	In Force	COMMON BLOCK INTERFACE FOR DATA AND PROTOCOL WITH HAND
MP1163	MP1163	US	11/824,046	6/29/2007	8111259	2/7/2012	In Force	IMAGE PROCESSING APPARATUS HAVING CONTEXT MEMORY CONTR
MP1163	MP1163C1	US	13/287,666	11/2/2011	8294720	10/23/2012	In Force	IMAGE PROCESSING APPARATUS HAVING CONTEXT MEMORY CONTR
MP1164	MP1164C1	US	12/727,088	3/18/2010	8046518	10/25/2011	In Force	USB ISOCHRONOUS DATA TRANSFER FOR A HOST BASED LASER PRIN
MP1165	MP1165C1	US	13/023,251	2/8/2011	8762596	6/24/2014	In Force	DIRECT MEMORY ACCESS CONTROLLER WITH MULTIPLE TRANSACTI
MP1167	MP1167C1	US	12/717,587	3/4/2010	7987301	7/26/2011	In Force	DMA CONTROLLER EXECUTING MULTIPLE TRANSACTIONS AT NON-C
MP1168	MP1168	US	11/400,600	4/7/2006	7814329	10/12/2010	In Force	MACSec Implementation
MP1168	MP1168C1	US	12/902,504	10/12/2010	8250370	8/21/2012	In Force	MACSEC IMPLEMENTATION
MP1168	MP1168C1aa	US	14/035,509	9/24/2013	8935534	1/13/2015	In Force	Macsec Implementation
MP1168	MP1168C1C1	US	13/590,979	8/21/2012	8543826	9/24/2013	In Force	MACSEC IMPLEMENTATION
MP1169	MP1169	US	11/818,079	6/12/2007	8027378	9/27/2011	In Force	CIRCUITS, ARCHITECTURES, APPARATUSES, SYSTEMS, ALGORITHMS A
MP1170	MP1170C1	US	12/623,854	11/23/2009	7834800	11/16/2010	In Force	Systems And Methods For Detecting Radar
MP1171	MP1171	US	11/768,622	6/26/2007	8971325	3/3/2015	In Force	Policy System and Method for a Switching Device

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MP1172	MP1172	US	11/768,613	6/26/2007	7782770	8/24/2010	In Force	SYSTEM AND METHOD OF CROSS-CHIP FLOW CONTROL
MP1172	MP1172C1	US	12/860,801	8/20/2010	8085658	12/27/2011	In Force	System and Method of Cross-Chip Flow Control
MP1173	MP1173	US	11/759,062	6/6/2007	8379644	2/19/2013	In Force	System and Method of Processing Management Frames
MP1173	MP1173D1	US	13/768,049	2/15/2013	8879559	11/4/2014	In Force	System and Method of Processing Management Frames
MP1173	MP1173D1C1	US	14/526,954	10/29/2014	9131287	9/8/2015	In Force	System and Method of Processing Management Frames
MP1174	MP1174	US	11/823,588	6/28/2007	8339689	12/25/2012	In Force	EXPOSURE CORRECTION FOR SCANNERS
MP1175	MP1175	US	11/591,325	11/1/2006	7634686	12/15/2009	In Force	File Server For Redundant Array Of Independent Disks (RAID) Syster
MP1175	MP1175C1	US	11/724,549	3/15/2007	8046629	10/25/2011	In Force	File Server For Redundant Array Of Independent Disks (RAID) Syster
MP1175	MP1175C1C1	US	13/279,971	10/24/2011	8495416	7/23/2013	In Force	File Server For Redundant Array Of Independent Disks (RAID) Syster
MP1175	MP1175C2	US	12/637,119	12/14/2009	8006127	8/23/2011	In Force	File Server For Redundant Array Of Independent Disks (RAID) Syster
MP1175W	MP1175WCN3	CN	201210284451	7/17/2007	ZL2012102844	1/6/2016	In Force	FILE SERVER FOR REDUNDANT ARRAY OF INDEPENDENT DISKS (RAI
MP1175W	MP1175WO	WO	PCT/US2007/01	7/17/2007			Pending	File Server For Redundant Array Of Independent Disks (RAID) Syster
MP1175W	MP1175WOCN	CN	200780035418.4	7/17/2007	ZL2007800354	9/19/2012	In Force	FILE SERVER FOR REDUNDANT ARRAY OF INDEPENDENT DISKS (RAI
MP1175W	MP1175WOJP	JP	2009-521760	7/17/2007	5124792	11/9/2012	In Force	FILE SERVER FOR REDUNDANT ARRAY OF INDEPENDENT DISKS (RAID)
MP1177	MP1177	US	11/765,275	6/19/2007	7895488	2/22/2011	In Force	CONTROL OF CLOCK GATE CELLS DURING SCAN TESTING
MP1177	MP1177C1	US	13/014,921	1/27/2011	8443246	5/14/2013	In Force	CONTROL OF CLOCK GATE CELLS DURING SCAN TESTING
MP1177	MP1177C1C1	US	13/892,700	5/13/2013	8689067	4/1/2014	In Force	CONTROL OF CLOCK GATE CELLS DURING SCAN TESTING
MP1179	MP1179C1	US	13/008,160	1/18/2011	9252119	2/2/2016	In Force	BALL GRID ARRAY INCLUDING REDISTRIBUTION LAYER, PACKAGED IN
MP1182	MP1182	US	11/774,979	7/9/2007	8036625	10/11/2011	In Force	METHOD AND APPARATUS FOR MIXING A SIGNAL
MP1183	MP1183	US	11/773,928	7/5/2007	8121662	2/21/2012	In Force	VIRTUAL FM ANTENNA
MP1183T	MP1183TW	TW	96127454	7/27/2007	I451627	9/1/2014	In Force	VIRTUAL FM ANTENNA
MP1184	MP1184	US	11/820,181	6/18/2007	7768343	8/3/2010	In Force	START-UP CIRCUIT FOR BANDGAP REFERENCE
MP1185	MP1185C1	US	13/029,574	2/17/2011	8269334	9/18/2012	In Force	MULTICHIP PACKAGE LEADFRAME INCLUDING ELECTRICAL BUSSING
MP1186	MP1186	US	11/775,757	7/10/2007	7525460	4/28/2009	In Force	TIMING LOOP BASED ON ANALOG TO DIGITAL CONVERTER OUTPUT
MP1186	MP1186I1	US	12/019,430	1/24/2008	7825836	11/2/2010	In Force	LIMIT EQUALIZER OUTPUT BASED TIMING LOOP
MP1186	MP1186I1C1	US	12/877,779	9/8/2010	8274413	9/25/2012	In Force	LIMIT EQUALIZER OUTPUT BASED TIMING LOOP
MP1187	MP1187I1	US	12/359,855	1/26/2009	8358566	1/22/2013	In Force	METHOD AND DEVICE FOR DETECTING A SYNC MARK
MP1187	MP1187I1FZ	US	14/153,784	1/13/2014	9019804	4/28/2015	In Force	TIMING LOOP WITH LARGE PULL-IN RANGE
MP1187	MP1187I1I1	US	12/625,844	11/25/2009	8264933	11/9/2012	In Force	TIMING LOOP WITH LARGE PULL-IN RANGE
MP1188	MP1188C1	US	12/947,690	11/16/2010	8054320	11/8/2011	In Force	HORIZONTAL STRIP COLUMN-FIRST TWO-DIMENSIONAL SCALING
MP1189	MP1189	US	11/654,841	1/18/2007	8099629	1/17/2012	In Force	System-On-A-Chip (SoC) Test Interface Security
MP1189	MP1189C1	US	13/351,040	1/16/2012	8601321	12/3/2013	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189T	MP1189TW	TW	96125663	7/13/2007	I434196	4/11/2014	In Force	SYSTEM-ON-A-CHIP(SOC) TEST INTERFACE SECURITY
MP1189W	MP1189WID1	JP	2012-061847	3/19/2012	5382555	10/11/2013	In Force	SYSTEM-ON-A-CHIP (SOC) TEST INTERFACE SECURITY

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MP1189W	MP1189WO	WO	PCT/US2007/01	7/11/2007			Pending	System-On-A-Chip (SOC) Test Interface Security
MP1189W	MP1189WOCN	CN	200780034350.(	7/11/2007	ZL2007800343	5/30/2012	In Force	SYSTEM-ON-A-CHIP (SOC) TEST INTERFACE SECURITY
MP1189W	MP1189WOEP	EP	EP20070810322	7/11/2007	2041688	12/19/2018	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189W	MP1189WOEPDE	DE	EP20070810322	7/11/2007	602007057177	12/19/2018	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189W	MP1189WOEPFR	FR	EP20070810322	7/11/2007	2041688	12/19/2018	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189W	MP1189WOEPGB	GB	EP20070810322	7/11/2007	2041688	12/19/2018	In Force	System-On-A-Chip (SOC) Test Interface Security
MP1189W	MP1189WOJP	JP	2009-519507	7/11/2007	4962993	4/6/2012	In Force	SYSTEM-ON-A-CHIP (SOC) TEST INTERFACE SECURITY
MP1191	MP1191	US	11/771,944	6/29/2007	8503503	8/6/2013	In Force	Agile Frequency Synthesizer Scheme For Fast Hopping, Low Phase N
MP1195	MP1195	US	11/777,758	7/13/2007	7929406	4/19/2011	In Force	POWER SAVING CIRCUIT
MP1195	MP1195C1	US	13/073,007	3/28/2011	8115336	2/14/2012	In Force	POWER SAVING CIRCUIT
MP1197	MP1197	US	11/712,687	2/28/2007	7511549	3/31/2009	In Force	COMPACT HIGH-SPEED, HIGH-RESOLUTION COMPARATOR STRUCTU
MP1198	MP1198	US	12/012,426	2/1/2008	7907362	3/15/2011	In Force	MAGNETIC DISK CONTROLLER AND METHOD
MP1198J	MP1198JP	JP	2007-023005	2/1/2007	5221044	3/15/2013	In Force	MAGNETIC DISC CONTROLLER AND METHOD
MP1199	MP1199	US	12/024,914	2/1/2008	8458580	6/4/2013	In Force	MAGNETIC DISC CONTROLLER AND METHOD
MP1199	MP1199C1	US	13/907,645	5/31/2013	8719685	5/6/2014	In Force	MAGNETIC DISC CONTROLLER AND METHOD
MP1199J	MP1199JP	JP	2007-023006	2/1/2007	JP5186112	1/25/2013	In Force	MAGNETIC DISC CONTROLLER AND METHOD
MP12007	MP12007	US	08/971,387	11/17/1997	6055285	4/25/2000	In Force	SYNCHRONIZATION CIRCUIT FOR TRANSFERRING POINTER BETWEEN
MP12008	MP12008	US	08/972,117	11/17/1997	6163550	12/19/2000	In Force	STATE DEPENDENT SYNCHRONIZATION CIRCUIT WHICH SYNCHRONIZ
MP12009	MP12009	US	09/030,865	2/26/1998	6151295	11/21/2000	In Force	OFDM RECEIVING SYSTEM
MP12009	MP12009WOJP	JP	2000-533973	2/25/1999			Pending	OFDM FRAME SYNCHRONISATION AND EQUALISATION SYSTEM
MP1200J	MP1200JP	JP	2007-023007	2/1/2007	JP5064820	8/17/2012	In Force	MAGNETIC DISC CONTROLLER AND METHOD
MP1201	MP1201	US	12/012,321	2/1/2008	7639443	12/29/2009	In Force	MAGNETIC DISK CONTROLLER AND METHOD
MP1201	MP1201C1	US	12/634,300	12/9/2009	8284511	10/9/2012	In Force	MAGNETIC DISK CONTROLLER TO PROCESS PLURAL DATA SETS FOR R
MP1201	MP1201C1C1	US	13/615,287	9/13/2012	8514507	8/20/2013	In Force	PROCESSING DATA FOR RECORDING TO A STORAGE DEVICE
MP12010	MP12010	US	09/104,731	6/25/1998	6167321	12/26/2000	In Force	INTERFACE MODULE WITH PROTECTION CIRCUIT AND METHOD OF P
MP12011	MP12011	US	09/386,109	8/30/1999	6597691	7/22/2003	In Force	HIGH PERFORMANCE SWITCHING
MP12013	MP12013	US	09/249,873	2/16/1999	6317842	11/13/2001	In Force	METHOD AND CIRCUIT FOR RECEIVING DUAL EDGE CLOCKED DATA
MP12014	MP12014	US	09/257,803	2/25/1999	6229845	5/8/2001	In Force	BUS DRIVER WITH DATA DEPENDENT DRIVE STRENGTH CONTROL LO
MP12015	MP12015	US	09/280,506	3/30/1999	6470397	10/22/2002	In Force	SYSTEMS AND METHODS FOR NETWORK AND I/O DEVICE DRIVERS
MP12016	MP12016	US	09/281,369	3/30/1999	6327635	4/12/2001	In Force	ADD-ON CARD WITH AUTOMATIC BUS POWER LINE SELECTION CIRC
MP12017	MP12017	US	09/282,341	3/31/1999	6434630	8/13/2002	In Force	HOST ADAPTER FOR COMBINING I/O COMPLETION REPORTS AND M
MP12018	MP12018	US	09/328,738	6/9/1999	6564271	5/13/2003	In Force	METHOD AND APPARATUS FOR AUTOMATICALLY TRANSFERRING I/O
MP12019	MP12019	US	09/346,793	2/7/1999	6697359	2/24/2004	In Force	HIGH PERFORMANCE SWITCH FABRIC ELEMENT AND SWITCH SYSTE
MP12019	MP12019C1	US	10/752,390	1/6/2004	7408927	8/5/2008	In Force	HIGH PERFORMANCE SWITCH FABRIC ELEMENT AND SWITCH SYSTE

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MP1201J	MP1201JP	JP	2007-023008	2/1/2007	JP5064821	8/17/2012	In Force	MAGNETIC DISC CONTROLLER AND METHOD
MP1202	MP1202	US	11/724,882	3/16/2007	8121209	2/21/2012	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMC
MP1202	MP1202C1	US	13/354,527	1/20/2012	8320509	11/27/2012	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMC
MP1202	MP1202C1C1	US	13/683,910	11/21/2012	8670507	3/11/2014	In Force	CONCATENTATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIM
MP1202	MP1202CN	CN	200780035614.:	7/23/2007	ZL2007800356	5/28/2014	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMC
MP12022	MP12022WOUS	US	10/166,570	6/10/2002	7443794	10/28/2008	In Force	FIBRE CHANNEL CREDIT EXTENDER AND REPEATER
MP12023	MP12023	US	09/587,689	6/5/2000	6708218	3/16/2004	In Force	IPSEC PERFORMANCE ENHANCEMENT USING A HARDWARE-BASED
MP12024	MP12024C1	US	11/765,724	6/20/2007	7684398	3/23/2010	In Force	HARDWARE-ENFORCED LOOP-LEVEL HARD ZONING FOR FIBRE CHAI
MP12027	MP12027	US	09/755,642	1/4/2001	6883099	4/19/2005	In Force	SECURE VIRTUAL INTERFACE
MP12027	MP12027C1	US	11/070,858	3/1/2005	7389399	6/17/2008	In Force	SECURE VIRTUAL INTERFACE
MP12028	MP12028	US	09/792,428	2/21/2001	6976174	12/13/2005	In Force	SECURE MULTIPROTOCOL INTERFACE
MP12029	MP12029	US	10/025,512	12/19/2001	6789147	9/7/2004	In Force	INTERFACE FOR A SECURITY COPROCESSOR
MP12029	MP12029EP	EP	02016424.0	7/22/2002	1282025	7/4/2018	In Force	AN INTERFACE FOR A SECURITY COPROCESSOR
MP12029	MP12029EPDE	DE	02016424.0	7/22/2002	60249496.6	7/4/2018	In Force	AN INTERFACE FOR A SECURITY COPROCESSOR
MP12029	MP12029JP	JP	2002-215500	7/24/2002	4298971	7/22/2009	In Force	INTERFACE FOR SECURITY COPROCESSOR
MP12029	MP12029TW	TW	091116472	7/24/2002	576963	2/21/2004	In Force	AN INTERFACE FOR A SECURITY COPROCESSOR
MP1202E	MP1202EP	EP	07796997.0	7/23/2007	EP2047626	7/5/2017	In Force	Concatenation-Assisted Symbol-Level Combining for Mimo Systems
MP1202E	MP1202EPDE	DE	07796997.0	7/23/2007	6.02007E+11	7/5/2017	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202E	MP1202EPFR	FR	07796997.0	7/23/2007	2047626	7/5/2017	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202E	MP1202EPGB	GB	07796997.0	7/23/2007	2047626	7/5/2017	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202T	MP1202TW	TW	96127135	7/25/2007	I442725	6/21/2014	In Force	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP1202W	MP1202WO	WO	PCT/US2007/01	7/23/2007			Pending	CONCATENATION-ASSISTED SYMBOL-LEVEL COMBINING FOR MIMO
MP12030	MP12030	US	09/938,166	8/23/2001	6954770	10/11/2005	In Force	RANDOM NUMBER GENERATOR
MP12031	MP12031	US	10/120,266	10/18/2001	7200144	4/3/2007	In Force	ROUTER AND METHODS USING NETWORK ADDRESSES FOR VIRTUALI
MP12031	MP12031D1	US	10/284,273	10/29/2002	7362702	4/22/2008	In Force	ROUTER WITH ROUTING PROCESSORS AND METHODS FOR VIRTUALI
MP12031	MP12031D3	US	10/285,226	10/31/2002	7292567	11/6/2007	In Force	ROUTER AND METHODS FOR DISTRIBUTED VIRTUALIZATION
MP12031	MP12031I1	US	11/098,831	4/4/2005	7447197	11/4/2008	In Force	SYSTEM AND METHOD OF PROVIDING NETWORK NODE SERVICES
MP12032	MP12032	US	09/992,637	11/6/2001	7376811	5/20/2008	In Force	METHOD AND APPARATUS FOR PERFORMING COMPUTATIONS AND
MP12034	MP12034	US	10/025,509	12/19/2001	7240203	7/3/2007	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSIONS
MP12034	MP12034DE	DE	02016423.2	7/22/2002	1292082	12/19/2018	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSION
MP12034	MP12034EP	EP	02016423.2	7/22/2002	1292082	12/19/2018	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSION
MP12034	MP12034FR	FR	02016423.2	7/22/2002	1292082	12/19/2018	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSION
MP12034	MP12034GB	GB	02016423.2	7/22/2002	1292082	12/19/2018	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSION
MP12034	MP12034JP	JP	2002-215552	7/24/2002			Pending	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSIONS

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MP12034	MP12034TW	TW	091116476	7/24/2002	I243566	11/11/2005	In Force	METHOD AND APPARATUS FOR ESTABLISHING SECURE SESSION
MP12035	MP12035	US	10/068,677	2/6/2002	7035889	4/25/2006	In Force	METHOD AND APPARATUS FOR MONTGOMERY MULTIPLICATION
MP12036	MP12036	US	10/052,054	1/17/2002	7076059	7/11/2006	In Force	METHOD AND APPARATUS TO IMPLEMENT THE DATA ENCRYPTION
MP12037	MP12037	US	10/086,665	2/28/2002	7096292	8/22/2006	In Force	On-Chip Inter-Subsystem Communication
MP12037	MP12037C1	US	11/422,373	6/6/2006	7243179	7/10/2007	In Force	On-Chip Inter-Subsystem Communication
MP12037	MP12037WO3US	US	10/469,467	4/5/2004	7436954	10/14/2008	In Force	Security System With An Intelligent Dma Controller
MP12037	MP12037WOUS	US	10/469,529	8/28/2003	7653763	1/26/2010	In Force	SUBSYSTEM BOOT AND PERIPHERAL DATA TRANSFER ARCHITECTUF
MP12038	MP12038	US	10/092,328	3/6/2002	7260217	8/21/2007	In Force	SPECULATIVE EXECUTION FOR DATA CIPHERING OPERATIONS
MP12039	MP12039	US	10/234,895	9/4/2002	7305567	12/4/2007	In Force	DECOUPLED ARCHITECTURE FOR DATA CIPHERING OPERATIONS
MP1204	MP1204	US	11/821,231	6/21/2007	7984367	7/19/2011	In Force	METHOD FOR ITERATIVE DECODING IN THE PRESENSE OF BURST ER
MP12040	MP12040	US	10/186,291	6/28/2002	6988150	1/17/2006	In Force	SYSTEM AND METHOD FOR EVENTLESS DETECTION OF NEWLY DELI
MP12041	MP12041	US	10/600,779	6/20/2003	6864814	3/8/2005	In Force	SYSTEM AND METHOD FOR IMPROVING DYNAMIC RANGE OF ANAL
MP12044	MP12044	US	10/200,393	7/22/2002	7230929	12/6/2007	In Force	METHOD AND SYSTEM FOR DYNAMICALLY ASSIGNING DOMAIN IDE
MP12044	MP12044-2	US	10/200,487	7/22/2002	7154886	12/26/2006	In Force	METHOD AND SYSTEM FOR PRIMARY BLADE SELECTION IN A MULTI-
MP12044	MP12044-2C1	US	11/608,634	12/8/2006	7388861	6/17/2008	In Force	METHOD AND SYSTEM FOR PRIMARY BLADE SELECTION IN A MULTI-
MP12045	MP12045	US	10/212,425	8/5/2002	7334046	2/19/2008	In Force	SYSTEM AND METHOD FOR OPTIMIZING FRAME ROUTING IN A NET
MP12046	MP12046	US	10/213,237	8/6/2002	7263108	8/28/2007	In Force	DUAL-MODE NETWORK STORAGE SYSTEMS AND METHODS
MP12046	MP12046C1	US	11/845,688	8/27/2007	7688867	3/30/2010	In Force	DUAL-MODE NETWORK STORAGE SYSTEMS AND METHODS
MP12048	MP12048	US	10/241,153	11/9/2002	7397768	8/7/2008	In Force	ZONE MANAGEMENT IN A MULTI-MODULE FIBRE CHANNEL SWITCH
MP12048	MP12048C1	US	11/682,199	3/5/2007	7729288	6/1/2010	In Force	ZONE MANAGEMENT IN A MULTI-MODULE FIBRE CHANNEL SWITCH
MP12049	MP12049	US	10/263,858	3/10/2002	7362717	4/22/2008	In Force	METHOD AND SYSTEM FOR USING DISTRIBUTED NAME SERVERS IN
MP12050	MP12050	US	10/295,976	11/15/2002	6769596	8/3/2004	In Force	METHOD AND SYSTEM FOR REWORKING BALL GRID ARRAYS
MP12053	MP12053	US	10/375,643	2/27/2003	6810440	10/26/2004	In Force	METHOD AND APPARATUS FOR AUTOMATICALLY TRANSFERRING I/O
MP12054	MP12054	US	10/387,151	3/11/2003	6861865	3/1/2005	In Force	APPARATUS AND METHOD FOR REPAIRING LOGIC BLOCKS
MP12054	MP12054D1	US	11/040,266	1/20/2005	7205785	4/17/2007	In Force	APPARATUS AND METHOD FOR REPAIRING LOGIC BLOCKS
MP12055	MP12055	US	10/397,083	3/26/2003	7209531	4/24/2007	In Force	APPARATUS AND METHOD FOR DATA DESKEW
MP12056	MP12056	US	10/410,773	4/10/2003	6959345	10/25/2005	In Force	METHOD AND SYSTEM FOR CALIBRATING SCSI EXPANDERS
MP12057	MP12057	US	10/411,909	4/12/2003	7398386	7/8/2008	In Force	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND
MP12057	MP12057JPD1	JP	2010-131757	6/9/2010	5074558	11/14/2012	In Force	Network processing using IPSec
MP12057	MP12057WO	WO	PCT/US2004/00	3/30/2004			Pending	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND
MP12057	MP12057WOEP	EP	04749529.6	3/30/2004	1614250	8/26/2015	In Force	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND
MP12057	MP12057WOEPDE	DE	04749529.6	3/30/2004	602004047762	8/26/2015	In Force	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND
MP12057	MP12057WOEPFR	FR	04749529.6	3/30/2004	1614250	8/26/2015	In Force	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND
MP12057	MP12057WOJP	JP	2006-509485	3/30/2004			Pending	TRANSPARENT IPSEC PROCESSING INLINE BETWEEN A FRAMER AND

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MP12058	MP12058	US	10/411,944	4/12/2003	7661130	2/9/2010	In Force	APPARATUS AND METHOD FOR ALLOCATING RESOURCES WITHIN A S
MP12059	MP12059	US	10/411,967	4/12/2003	7814310	10/12/2010	In Force	IPSEC PERFORMANCE OPTIMIZATION
MP12059	MP12059WO	WO	PCT/US2004/00	3/31/2004	DNA		Pending	IPSEC PERFORMANCE OPTIMIZATION
MP12060	MP12060	US	10/411,943	4/12/2003	7657933	2/2/2010	In Force	APPARATUS AND METHOD FOR ALLOCATING RESOURCES WITHIN A
MP12060	MP12060WO	WO	PCT/US2004/01	4/8/2004	DNA		Pending	AN APPARATUS AND METHOD FOR ALLOCATING RESOURCES WITHI
MP12061	MP12061	US	10/411,945	4/12/2003	7337314	2/26/2008	In Force	APPARATUS AND METHOD FOR ALLOCATING RESOURCES WITHIN A
MP12061	MP12061WO	WO	PCT/US2004/01	4/7/2004	DNA		Pending	AN APPARATUS AND METHOD FOR ALLOCATING RESOURCES WITHI
MP12062	MP12062	US	10/417,523	4/17/2003	6961784	11/1/2005	In Force	METHOD AND SYSTEM FOR PROCESSING NON-DATA FRAMES IN HC
MP12063	MP12063	US	10/423,499	4/25/2003	6996785	2/7/2006	In Force	ON-CHIP PACKET-BASED INTERCONNECTIONS USING REPEATERS/R(
MP12063	MP12063C1	US	11/283,210	11/18/2005	7543250	6/2/2009	In Force	ON-CHIP PACKET-BASED INTERCONNECTIONS USING REPEATERS/R(
MP12065	MP12065	US	10/445,695	5/27/2003	7424419	9/9/2008	In Force	METHOD AND SYSTEM FOR PROVIDING A VIRTUAL UNIVERSAL SERI
MP12066	MP12066	US	10/601,534	6/24/2003	7352778	4/1/2008	In Force	SYNCHRONIZING METHOD AND APPARATUS
MP12066	MP12066CA	CA	2433139	6/23/2003	2433139	3/27/2012	In Force	SYNCHRONIZING METHOD AND APPARATUS
MP12067	MP12067	US	10/619,719	7/15/2003	7515612	4/7/2009	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK DATA PACKETS
MP12067	MP12067C1	US	12/389,308	2/19/2009	7830919	11/9/2010	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK DATA PACKETS
MP12068	MP12068	US	10/620,040	7/15/2003	7403542	7/22/2008	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK DATA PACKETS
MP12070	MP12070	US	10/889,551	7/12/2004	7388843	6/17/2008	In Force	METHOD AND APPARATUS FOR TESTING LOOP PATHWAY INTEGRITY
MP12071	MP12071	US	10/889,256	7/12/2004	7620059	11/17/2009	In Force	METHOD AND APPARATUS FOR ACCELERATING RECEIVE-MODIFY-SE
MP12072	MP12072	US	10/889,259	7/12/2004	7453802	11/18/2008	In Force	METHOD AND APPARATUS FOR DETECTING AND REMOVING ORPHA
MP12072	MP12072C1	US	12/198,644	8/26/2008	7542418	6/2/2009	In Force	METHOD AND APPARATUS FOR DETECTING AND REMOVING ORPHA
MP12073	MP12073	US	10/889,337	7/12/2004	7355966	4/8/2008	In Force	METHOD AND SYSTEM FOR MINIMIZING DISRUPTION IN COMMON-
MP12074	MP12074	US	10/889,635	7/12/2004	7525910	4/28/2009	In Force	METHOD AND SYSTEM FOR NON-DISRUPTIVE DATA CAPTURE IN NET
MP12075	MP12075	US	10/889,588	7/12/2004	7152132	12/19/2006	In Force	METHOD AND APPARATUS FOR IMPROVING BUFFER UTILIZATION IN
MP12076	MP12076	US	10/894,827	7/20/2004	7525983	4/28/2009	In Force	METHOD AND SYSTEM FOR SELECTING VIRTUAL LANES IN FIBRE CHA
MP12076	MP12076D1	US	12/427,966	4/22/2009	8081650	12/20/2011	In Force	METHOD AND SYSTEM FOR SELECTING VIRTUAL LANES IN FIBRE CHA
MP12077	MP12077	US	10/889,267	7/12/2004	7463646	12/9/2008	In Force	METHOD AND SYSTEM FOR FIBRE CHANNEL ARBITRATED LOOP ACCE
MP12078	MP12078	US	10/889,255	7/12/2004	7471635	12/30/2008	In Force	METHOD AND APPARATUS FOR TEST PATTERN GENERATION
MP1208	MP1208C1	US	14/610,822	1/30/2015	9361556	6/7/2016	In Force	PULSE WIDTH MODULATOR FOR A PRINTING DEVICE
MP12080	MP12080	US	10/894,492	7/20/2004	7466700	12/16/2008	In Force	LUN BASED HARD ZONING IN FIBRE CHANNEL SWITCHES
MP12081	MP12081	US	10/895,175	7/20/2004	7522529	4/21/2009	In Force	METHOD AND SYSTEM FOR DETECTING CONGESTION AND OVER SUB
MP12082	MP12082	US	10/674,943	9/30/2003	7234101	6/19/2007	In Force	METHOD AND SYSTEM FOR PROVIDING DATA INTEGRITY IN STORAG
MP12083	MP12083	US	10/894,689	7/20/2004	7477655	1/13/2009	In Force	METHOD AND SYSTEM FOR POWER CONTROL OF FIBRE CHANNEL S
MP12083	MP12083C1	US	12/267,188	11/7/2008	7822061	10/26/2010	In Force	METHOD AND SYSTEM FOR POWER CONTROL OF FIBRE CHANNEL S
MP12084	MP12084-1	US	10/894,491	7/20/2004	7420982	9/2/2008	In Force	METHOD AND SYSTEM FOR KEEPING A FIBRE CHANNEL ARBITRATED

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MP12084	MP12084-1-C1	US	12/191,890	8/14/2008	7822057	10/26/2010	In Force	METHOD AND SYSTEM FOR KEEPING A FIBRE CHANNEL ARBITRATED
MP12084	MP12084-2	US	10/894,536	7/20/2004	7512067	3/31/2009	In Force	METHOD AND SYSTEM FOR CONGESTION CONTROL BASED ON OPT
MP12084	MP12084-3	US	10/894,587	7/20/2004	7580354	8/25/2009	In Force	MULTI-SPEED CUT THROUGH OPERATION IN FIBRE CHANNEL SWITC
MP12084	MP12084-3-C1	US	12/502,973	7/14/2009	9118586	8/25/2015	In Force	MULTI-SPEED CUT THROUGH OPERATION IN FIBRE CHANNEL SWITC
MP12084	MP12084-4	US	10/894,629	7/20/2004	7447224	11/4/2008	In Force	METHOD AND SYSTEM FOR ROUTING FIBRE CHANNEL FRAMES
MP12084	MP12084-4-C1	US	12/189,502	8/11/2008	7936771	5/3/2011	In Force	METHOD AND SYSTEM FOR ROUTING FIBRE CHANNEL FRAMES
MP12084	MP12084-5	US	10/894,726	7/20/2004	7583597	9/1/2009	In Force	METHOD AND SYSTEM FOR IMPROVING BANDWIDTH AND REDUCI
MP12085	MP12085	US	10/664,548	9/19/2003	7352701	4/1/2008	In Force	BUFFER TO BUFFER CREDIT RECOVERY FOR IN-LINE FIBRE CHANNEL
MP12086	MP12086	US	10/894,595	7/20/2004	7522522	4/21/2009	In Force	METHOD AND SYSTEM FOR REDUCING LATENCY AND CONGESTION
MP12087	MP12087	US	10/894,627	7/20/2004	7573909	8/11/2009	In Force	METHOD AND SYSTEM FOR PROGRAMMABLE DATA DEPENDANT NI
MP12088	MP12088	US	10/683,622	10/10/2003	7009413	3/7/2006	In Force	SYSTEM AND METHOD FOR TESTING BALL GRID ARRAYS
MP12089	MP12089	US	10/696,471	10/29/2003	7219263	5/15/2007	In Force	METHOD AND SYSTEM FOR MINIMIZING MEMORY CORRUPTION
MP1209	MP1209C1	US	13/205,406	8/8/2011	8233196	7/31/2012	In Force	HALFTONER BLOCK EMPLOYING AVERAGE VALUES AND SHIFTS
MP1209	MP1209C1C1	US	13/561,877	7/30/2012	8467100	6/18/2013	In Force	HALFTONER BLOCK EMPLOYING AVERAGE VALUES AND SHIFTS
MP12090	MP12090	US	10/719,077	11/21/2003	7103504	9/5/2006	In Force	METHOD AND SYSTEM FOR MONITORING EVENTS IN STORAGE AREA
MP12091	MP12091	US	10/723,004	11/26/2003	6949423	9/27/2005	In Force	MOSFET-FUSED NONVOLATILE READ-ONLY MEMORY CELL (MOFRO
MP12092	MP12092	US	10/894,586	7/20/2004	7630384	12/8/2009	In Force	METHOD AND SYSTEM FOR DISTRIBUTING CREDIT IN FIBRE CHANNEL
MP12092	MP12092C1	US	12/580,169	10/15/2009	8072988	12/6/2011	In Force	METHOD AND SYSTEM FOR BUFFER-TO-BUFFER CREDIT RECOVERY I
MP12093	MP12093	US	10/894,547	7/20/2004	7684401	3/23/2010	In Force	METHOD AND SYSTEM FOR USING EXTENDED FABRIC FEATURES WIT
MP12093	MP12093C1	US	12/697,994	2/1/2010	7990975	8/2/2011	In Force	METHOD AND SYSTEM FOR USING EXTENDED FABRIC FEATURES WIT
MP12093	MP12093C1D1	US	13/177,314	7/6/2011	8644317	2/4/2014	In Force	METHOD AND SYSTEM FOR USING EXTENDED FABRIC FEATURES WIT
MP12094	MP12094	US	10/894,597	7/20/2004	7406092	7/29/2008	In Force	PROGRAMMABLE PSEUDO VIRTUAL LANES FOR FIBRE CHANNEL SYST
MP12094	MP12094C1	US	12/141,519	6/18/2008	7760752	7/20/2010	In Force	PROGRAMMABLE PSEUDO VIRTUAL LANES FOR FIBRE CHANNEL SYST
MP12095	MP12095	US	10/894,579	7/20/2004	7430175	9/30/2008	In Force	METHOD AND SYSTEM FOR MANAGING TRAFFIC IN FIBRE CHANNEL
MP12095	MP12095C1	US	12/189,497	8/11/2008	7649903	1/19/2010	In Force	METHOD AND SYSTEM FOR MANAGING TRAFFIC IN FIBRE CHANNEL
MP12096	MP12096	US	10/894,546	7/20/2004	7792115	9/7/2010	In Force	METHOD AND SYSTEM FOR ROUTING AND FILTERING NETWORK DAT
MP12097	MP12097	US	10/798,527	3/11/2004	7480293	1/20/2009	In Force	METHOD AND SYSTEM FOR PREVENTING DEADLOCK IN FIBRE CHAN
MP12098	MP12098	US	10/798,468	3/11/2004	7564789	7/21/2009	In Force	METHOD AND SYSTEM FOR REDUCING DEADLOCK IN FIBRE CHANNE
MP12099	MP12099D1	US	11/617,716	12/28/2006	7774374	8/10/2010	In Force	SWITCHING SYSTEMS AND METHODS USING WILDCARD SEARCHING
MP12100	MP12100	US	10/894,978	7/20/2004	7646767	1/12/2010	In Force	METHOD AND SYSTEM FOR PROGRAMMABLE DATA DEPENDANT NE
MP12101	MP12101	US	10/797,977	3/10/2004	7231410	6/12/2007	In Force	REVISION CONTROL SYSTEM FOR LARGE-SCALE SYSTEMS MANAGEM
MP12101	MP12101C1	US	11/748,160	5/14/2007	7647363	1/12/2010	In Force	REVISION CONTROL SYSTEM FOR LARGE-SCALE SYSTEMS MANAGEM
MP12102	MP12102	US	10/806,691	3/23/2004	7424564	9/9/2008	In Force	PCI - EXPRESS SLOT FOR COUPLING PLURAL DEVICES TO A HOST SYST
MP12107	MP12107	US	10/894,732	7/20/2004	7894348	2/22/2011	In Force	METHOD AND SYSTEM FOR CONGESTION CONTROL IN A FIBRE CHAN

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MP12108	MP12108	US	10/848,485	5/17/2004	7493481	2/17/2009	In Force	DIRECT HARDWARE PROCESSING OF INTERNAL DATA STRUCTURE FIE
MP12109	MP12109	US	10/894,917	7/20/2004	7558281	7/7/2009	In Force	METHOD AND SYSTEM FOR CONFIGURING FIBRE CHANNEL PORTS
MP12109	MP12109C1	US	12/473,150	5/27/2009	8005105	8/23/2011	In Force	METHOD AND SYSTEM FOR CONFIGURING FIBRE CHANNEL PORTS
MP12110	MP12110	US	10/847,756	5/18/2004	7669190	2/23/2010	In Force	METHOD AND SYSTEM FOR EFFICIENTLY RECORDING PROCESSOR E'
MP12112	MP12112	US	10/878,595	6/28/2004	7469267	12/23/2008	In Force	METHOD AND SYSTEM FOR HOST DEVICE EVENT SYNCHRONIZATIOI
MP12113	MP12113	US	29/209,313	7/14/2004	D520,863	5/16/2006	In Force	FIBER OPTIC CABLE RETENTION AND GUIDE CLIP
MP12114	MP12114	US	10/894,529	7/20/2004	7404020	7/22/2008	In Force	INTEGRATED FIBRE CHANNEL FABRIC CONTROLLER
MP12117	MP12117	US	10/927,170	8/25/2004	7308535	12/11/2007	In Force	SELF-TRIGGERING OUTGOING BUFFERS
MP12117	MP12117C1	US	11/933,860	11/1/2007	7500057	3/3/2009	In Force	SELF-TRIGERRING OUTGOING BUFFERS
MP12118	MP12118	US	10/931,851	1/9/2004	7522623	4/21/2009	In Force	METHOD AND SYSTEM FOR EFFICIENTLY USING BUFFER SPACE
MP12118	MP12118C1	US	12/407,153	3/19/2009	7924859	4/12/2011	In Force	METHOD AND SYSTEM FOR EFFICIENTLY USING BUFFER SPACE
MP12119	MP12119	US	10/931,850	1/9/2004	7761608	7/20/2010	In Force	METHOD AND SYSTEM FOR PROCESSING MARKERS, DATA INTEGRIT
MP12120	MP12120	US	10/935,919	9/8/2004	7577772	8/18/2009	In Force	METHOD AND SYSTEM FOR OPTIMIZING DMA CHANNEL SELECTION
MP12122	MP12122	US	10/940,355	9/13/2004	7895390	2/22/2011	In Force	ENSURING BUFFER AVAILABILITY
MP12125	MP12125	US	10/956,955	10/1/2004	7676611	3/9/2010	In Force	METHOD AND SYSTEM FOR PROCESSING OUT OF ORDER FRAMES
MP12126	MP12126	US	10/956,501	10/1/2004	8295299	10/23/2012	In Force	HIGH SPEED FIBRE CHANNEL SWITCH ELEMENT
MP12126	MP12126C1	US	13/604,261	9/5/2012	8774206	7/8/2014	In Force	HIGH SPEED FIBRE CHANNEL SWITCH ELEMENT
MP12126	MP12126EP	EP	05798761.2	9/27/2005	1794952	7/14/2010	In Force	HIGH SPEED FIBER CHANNEL SWITCH ELEMENT
MP12127	MP12127	US	10/956,502	10/1/2004	7593997	9/22/2009	In Force	METHOD AND SYSTEM FOR LUN REMAPPING IN FIBRE CHANNEL NET
MP12128	MP12128	US	10/957,465	10/1/2004	7930377	4/19/2011	In Force	METHOD AND SYSTEM FOR USING BOOT SERVERS IN NETWORKS
MP12129	MP12129	US	10/956,718	10/1/2004	7380030	5/27/2008	In Force	METHD AND SYSTEM FOR USING AN IN-LINE CREDIT EXTENDER WIT
MP12130	MP12130	US	10/956,717	1/10/2004	7411958	12/8/2008	In Force	METHOD AND SYSTEM FOR TRANSFERRING DATA DIRECTLY BETWEE
MP12131	MP12131	US	10/961,463	8/10/2004	7340167	4/3/2008	In Force	FIBRE CHANNEL TRANSPARENT SWITCH FOR MIXED SWITCH FABRICS
MP12131	MP12131C1	US	12/031,585	2/14/2008	7542676	6/2/2009	In Force	FIBRE CHANNEL TRANSPARENT SWITCH FOR MIXED SWITCH FABRICS
MP12132	MP12132	US	10/963,287	10/11/2004	7512721	3/31/2009	In Force	METHOD AND APPARATUS FOR EFFICIENT DETERMINATION OF STAT
MP12133	MP12133	US	10/994,829	11/22/2004	7398335	7/8/2008	In Force	METHOD AND SYSTEM FOR DMA OPTIMIZATION IN HOST BUS ADAP
MP12134	MP12134	US	11/002,728	11/30/2004	7606998	10/20/2009	In Force	Store Instruction Ordering For Multi-Core Processor
MP12134	MP12134WO	WO	PCT/US2005/03	9/1/2005	DNA		Pending	Store Instruction Ordering For Multi-Core Processor
MP12134	MP12134WOCN	CN	2005800346066	9/1/2005	ZL2005800346	8/26/2009	In Force	Store Instruction Ordering For Multi-Core Processor
MP12134	MP12134WODE	DE	05795368.9	9/1/2005	602005052948	10/25/2017	In Force	Store Instruction Ordering For Multi-Core Processor
MP12134	MP12134WOEP	EP	05795368.9	9/1/2005	1787194	10/25/2017	In Force	Store Instruction Ordering For Multi-Core Processor
MP12134	MP12134WOJP	JP	2007-531266	9/1/2005	JP5197010	2/15/2013	In Force	Store Instruction Ordering For Multi-Core Processor
MP12135	MP12135	US	11/005,490	12/6/2004	7895431	2/22/2011	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WO	WO	PCT/US2005/03	9/1/2005	DNA		Pending	Packet Queuing, Scheduling And Ordering

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MP12135	MP12135WOCN	CN	2005800346009	9/1/2005	ZL2005800346	2/8/2012	In Force	Data packet queuing, scheduling and ordering
MP12135	MP12135WOEP	EP	05796391.0	9/1/2005	1787212	2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOEPDE	DE	05796391.0	9/1/2005	602005038127	2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOEPFI	FI	05796391.0	9/1/2005	1787212	2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOEPFR	FR	05796391.0	9/1/2005	1787212	2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOEPGB	GB	05796391.0	9/1/2005	1787212	2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOEPSE	SE	05796391.0	9/1/2005		2/13/2013	In Force	Packet Queuing, Scheduling And Ordering
MP12135	MP12135WOJP	JP	2007-531240	9/1/2005	JP4723586	4/15/2011	In Force	Packet Queuing, Scheduling And Ordering
MP12136	MP12136	US	11/015,343	12/17/2004	7941585	5/10/2011	In Force	Local Scratchpad And Data Caching System
MP12137	MP12137	US	11/018,055	12/21/2004	7164425	1/16/2007	In Force	METHOD AND SYSTEM FOR HIGH SPEED NETWORK APPLICATION
MP12138	MP12138	US	11/024,002	12/28/2004	7594081	9/22/2009	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WO	WO	PCT/US2005/03	9/1/2005	DNA		Pending	Direct Access To Low-Latency Memory
MP12138	MP12138WOCN	CN	2005800334834	9/1/2005	ZL2005800334	12/8/2010	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEP	EP	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPCH	CH	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPDE	DE	05810485.2	9/1/2005	602005045424	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPFR	FR	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPGB	GB	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPIE	IE	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12138	MP12138WOEPSE	SE	05810485.2	9/1/2005	1787193	12/10/2014	In Force	Direct Access To Low-Latency Memory
MP12139	MP12139C1	US	14/159,210	1/20/2014	9141548	9/22/2015	In Force	Method And Apparatus For Managing Write Back Cache
MP12140	MP12140	US	11/037,922	1/18/2005	7519058	4/14/2009	In Force	ADDRESS TRANSLATION IN FIBRE CHANNEL SWITCHES
MP12141	MP12141	US	11/039,189	1/20/2005	7392437	6/24/2008	In Force	METHOD AND SYSTEM FOR TESTING HOST BUS ADAPTERS
MP12143	MP12143WO	WO	PCT/US2005/03	9/1/2005	DNA		Pending	Multiply Instructions For Modular Exponentiation
MP12147	MP12147	US	11/346,106	2/2/2006	7496745	2/24/2009	In Force	METHOD AND SYSTEM FOR MANAGING STORAGE AREA NETWORKS
MP12149	MP12149	US	11/355,622	2/16/2006	7694047	4/6/2010	In Force	METHOD AND SYSTEM FOR SHARING INPUT/OUTPUT DEVICES
MP1215	MP1215	US	12/050,604	3/18/2008	7855909	12/21/2010	In Force	Calibrating Page Borders In A Phase-Change Memory
MP12150	MP12150I1	US	11/097,024	4/1/2005	7420991	9/2/2008	In Force	TCP TIME STAMP PROCESSING IN HARDWARE BASED TCP OFFLOAD
MP12153	MP12153	US	11/100,063	6/4/2005	7231480	12/6/2007	In Force	METHOD AND SYSTEM FOR RECEIVER DETECTION IN PCI-EXPRESS DE
MP12154	MP12154	US	11/220,899	9/7/2005	8301788	10/30/2012	In Force	Deterministic Finite Automata (DFA) Instruction
MP12155	MP12155	US	11/221,408	9/7/2005	8392590	3/5/2013	In Force	Deterministic Finite Automata (DFA) Processing
MP12155	MP12155WO	WO	PCT/US2005/03	9/8/2005	DNA		Pending	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOCN	CN	200580034214X	9/8/2005	ZL2005800342	2/29/2012	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEP	EP	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing

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MP12155	MP12155WOEPCH	CH	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPDE	DE	05810485.2	9/8/2005	602005038814	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPFI	FI	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPFR	FR	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPGB	GB	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPIE	IE	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12155	MP12155WOEPSE	SE	05812863.8	9/8/2005	1790148	3/27/2013	In Force	Deterministic finite Automata (DFA) Processing
MP12156	MP12156	US	11/224,728	9/12/2005	8560475	10/15/2013	In Force	Content Search Mechanism That Uses A Deterministic Finite Autom
MP12156	MP12156-2	US	14/040,323	9/27/2013	8818921	8/26/2014	In Force	Content Search Mechanism That Uses A Deterministic Finite Autom
MP12156	MP12156C1	US	14/337,759	7/22/2014	9336328	5/10/2016	In Force	Content search mechanism that uses a deterministic finite automat
MP12156	MP12156C1C1	US	15/134,919	4/21/2016	9652505	5/16/2017	In Force	Content Search Pattern Matching Using Deterministic Finite Autom
MP12157	MP12157I1	US	11/335,189	1/18/2006	7558925	7/7/2009	In Force	Selective Replication Of Data Structures
MP12157	MP12157WO	WO	PCT/US2005/03	9/9/2005	DNA		Pending	Selective Replication Of Data Structures
MP12157	MP12157WO2	WO	PCT/US2005/03	9/9/2005	DNA		Pending	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2CH	CH	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2CN	CN	2005800304519	9/9/2005	ZL2005800304	2/1/2012	In Force	Selective replication of data structure
MP12157	MP12157WO2DE	DE	05795143.6	9/9/2005	602005051737	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2DK	DK	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2EP	EP	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2FI	FI	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2FR	FR	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2GB	GB	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2NL	NL	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12157	MP12157WO2SE	SE	05795143.6	9/9/2005	1794979	4/12/2017	In Force	SELECTIVE REPLICATION OF DATA STRUCTURE
MP12158	MP12158	US	11/225,373	9/12/2005	7613813	11/3/2009	In Force	METHOD AND APPARATUS FOR REDUCING HOST OVERHEAD IN A SO
MP12158	MP12158C1	US	12/574,263	10/6/2009	7930349	4/19/2011	In Force	Method And Apparatus For Reducing Host Overhead In A Socket Serv
MP12159	MP12159	US	11/218,727	9/2/2005	7535907	5/19/2009	In Force	TCP Engine
MP1216	MP1216	US	11/820,912	6/21/2007	7958301	6/7/2011	In Force	Memory Controller And Method For Memory Pages With Dynamicall
MP1216	MP1216C1	US	13/154,320	6/6/2011	8166271	4/24/2012	In Force	Memory Controller For Setting Page Length And Memory Cell Density
MP1216	MP1216CN	CN	200880019550.(	4/10/2008	ZL2008800195	1/2/2013	In Force	MEMORY CONTROLLER
MP12162	MP12162	US	11/126,988	5/11/2005	7447618	11/4/2008	In Force	METHOD AND SYSTEM FOR ASIC SIMULATION
MP12163	MP12163	US	11/132,020	5/18/2005	7802031	9/21/2010	In Force	METHOD AND SYSTEM FOR HIGH SPEED NETWORK APPLICATION
MP12165	MP12165	US	11/140,891	5/31/2005	7168971	1/30/2007	In Force	GASKET RETAINER
MP12166	MP12166	US	11/201,036	8/10/2005	7565580	7/21/2009	In Force	METHOD AND SYSTEM FOR TESTING NETWORK DEVICE LOGIC

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MP12167	MP12167	US	11/222,592	9/9/2005	7230549	12/6/2007	In Force	METHOD AND SYSTEM FOR SYNCHRONIZING BIT STREAMS FOR PCI E
MP12169	MP12169	US	11/223,693	9/9/2005	7639715	12/29/2009	In Force	DEDICATED APPLICATION INTERFACE FOR NETWORK SYSTEMS
MP1216W	MP1216WcD1	CN	201210473722.)	4/10/2008	ZL2012104737	5/18/2016	In Force	MEMORY CONTROLLER
MP1216W	MP1216WO	WO	PCT/US2008/00	4/10/2008	DNA		Pending	Memory Controller
MP12170	MP12170	US	11/222,936	9/9/2005	7577773	8/18/2009	In Force	METHOD AND SYSTEM FOR DMA OPTIMIZATION
MP12172	MP12172	US	11/252,524	10/18/2005	7447874	4/11/2008	In Force	METHOD AND SYSTEM FOR DESIGNING A FLEXIBLE HARDWARE STA
MP12173	MP12173	US	11/257,693	10/25/2005	7630876	12/8/2009	In Force	METHOD AND SYSTEM FOR FILTERING UNKNOWN VALUES IN ASIC I
MP12174	MP12174	US	11/291,430	12/1/2005	7920473	4/5/2011	In Force	METHOD AND SYSTEM FOR MANAGING TRANSMIT DESCRIPTORS IN
MP12175	MP12175	US	11/317,315	12/23/2005	7735099	6/8/2010	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK DATA
MP12176	MP12176	US	11/318,116	12/23/2005	8682799	3/25/2014	In Force	Method and system for port licensing in switches
MP12178	MP12178	US	11/328,358	9/1/2006	7594057	9/22/2009	In Force	METHOD AND SYSTEM FOR PROCESSING DMA REQUESTS
MP1218	MP1218	US	11/986,520	11/21/2007	7937526	5/3/2011	In Force	Storage Device Cache With Phase Change Memory
MP12180	MP12180	US	11/343,596	1/31/2006	8464238	6/11/2013	In Force	METHOD AND SYSTEM FOR MANAGING STORAGE AREA NETWORKS
MP12181	MP12181	US	11/357,515	2/17/2006	7668978	2/23/2010	In Force	METHOD AND SYSTEM FOR STORING FRAMES IN NETWORKS
MP12182	MP12182	US	11/362,690	2/27/2006	7518995	4/14/2009	In Force	METHOD AND SYSTEM FOR MANAGING FRAME FLOW IN FIBRE CHA
MP12183	MP12183	US	11/363,365	2/27/2006	7548560	6/16/2009	In Force	METHOD AND SYSTEM FOR CHECKING FRAME-LENGTH IN FIBRE CHA
MP12184	MP12184	US	11/376,703	3/14/2006	8009923	8/30/2011	In Force	METHOD AND SYSTEM FOR MOTION ESTIMATION WITH MULTIPLE V
MP12185	MP12185	US	11/376,955	3/16/2006	7525968	4/28/2009	In Force	METHOD AND SYSTEM FOR AUTO ROUTING FIBRE CHANNEL CLASS F
MP12187	MP12187	US	11/378,838	3/17/2006	7461195	12/2/2008	In Force	METHOD AND SYSTEM FOR DYNAMICALLY ADJUSTING DATA TRANSF
MP12188	MP12188	US	11/396,748	4/3/2006	7681102	3/16/2010	In Force	BYTE LEVEL PROTECTION IN PCI-EXPRESS DEVICES
MP12189	MP12189	US	11/430,704	5/9/2006	7447817	11/4/2008	In Force	METHOD AND SYSTEM FOR PROCESSING ARBITRATION REQUESTS
MP1219	MP1219	US	11/789,971	4/26/2007	8856423	10/7/2014	In Force	Dual-Purpose Nonvolatile Memory For Code And Data Storage
MP12190	MP12190	US	11/453,500	6/15/2006	7660302	2/9/2010	In Force	METHOD AND SYSTEM FOR INTER-FABRIC ROUTING
MP12192	MP12192	US	11/477,161	6/28/2006	7610482	10/27/2009	In Force	METHOD AND SYSTEM FOR MANAGING BOOT TRACE INFORMATION
MP12193	MP12193	US	11/491,357	7/21/2006	7379837	5/27/2008	In Force	METHOD AND SYSTEM FOR TESTING INTEGRATED CIRCUITS
MP12194	MP12194	US	11/493,978	7/27/2006	8185960	5/22/2012	In Force	SYSTEM AND METHOD FOR MANAGING ACCESS TO ADAPTER FEATU
MP12195	MP12195	US	11/461,387	7/31/2006	7764676	7/27/2010	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK INFORMATION
MP12196	MP12196	US	11/502,282	8/10/2006	7457887	11/25/2008	In Force	METHOD AND SYSTEM FOR PROCESSING ASYNCHRONOUS EVENT NO
MP12198	MP12198	US	11/469,377	8/31/2006	7669001	2/23/2010	In Force	METHOD AND SYSTEM FOR USING APPLICATION SPECIFIC INTEGRAT
MP12199	MP12199	US	11/522,672	9/18/2006	7843846	11/30/2010	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK INFORMATION
MP1220	MP1220	US	11/789,605	4/25/2007	8176386	5/8/2012	In Force	Systems And Methods For Processing Streaming Data
MP1220	MP1220C1	US	14/190,836	2/26/2014	8935591	1/13/2015	In Force	SYSTEM AND METHOD TO CORRECT ERRORS IN DATA READ FROM A
MP1220	MP1220D1	US	13/465,964	5/7/2012	8392799	3/5/2013	In Force	Systems And Methods For Arbitrating Use Of Processor Memory
MP1220	MP1220D1C1	US	13/785,803	3/5/2013	8667370	3/4/2014	In Force	Systems And Methods For Arbitrating Use Of Processor Memory

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MP12201	MP12201	US	11/533,873	9/21/2006	7773592	8/10/2010	In Force	METHOD AND SYSTEM FOR ROUTING NETWORK INFORMATION
MP12205	MP12205	US	11/555,180	10/31/2006	8055726	11/8/2011	In Force	METHOD AND SYSTEM FOR WRITING NETWORK DATA
MP12205	MP12205C1	US	13/241,012	9/22/2011	8676928	3/18/2014	In Force	METHOD AND SYSTEM FOR WRITING NETWORK DATA
MP12206	MP12206	US	11/555,123	10/31/2006	8601179	12/3/2013	In Force	METHOD AND SYSTEM FOR DATA TRANSFER
MP12208	MP12208	US	11/560,317	11/15/2006	7613816	11/3/2009	In Force	METHOD AND SYSTEM FOR ROUTING NETWORK INFORMATION
MP1221	MP1221	US	11/728,449	3/26/2007	7619919	11/17/2009	In Force	Multi-Level Memory
MP12210	MP12210	US	11/565,444	11/30/2006	7643505	1/5/2010	In Force	METHOD AND SYSTEM FOR REAL TIME COMPRESSION AND DECOM
MP12212	MP12212	US	11/615,329	12/22/2006	7549005	6/16/2009	In Force	SYSTEM AND METHOD FOR MANAGING INTERRUPTS
MP12214	MP12214	US	11/669,573	1/31/2007	7668177	2/23/2010	In Force	METHOD AND SYSTEM FOR QUALITY OF SERVICE IN HOST BUS ADAI
MP12217	MP12217	US	11/669,775	1/31/2007	7668162	2/23/2010	In Force	COMPRESSION AND DECOMPRESSION FOR NETWORK SYSTEMS
MP12217	MP12217C1	US	12/684,054	1/7/2010	7889730	2/15/2011	In Force	COMPRESSION AND DECOMPRESSION FOR NETWORK SYSTEMS
MP12219	MP12219WOBR	BR	P10721380-8	2/28/2007			Pending	METHOD OF ALLOCATING COMMUNICATIONS RESOURCES AND SCI
MP12219	MP12219WOID	ID	WO0200902395	2/28/2007	P0032751	1/21/2013	In Force	METHOD OF ALLOCATING COMMUNICATIONS RESOURCES AND SCI
MP12219	MP12219WOIN	IN	1660/MUMNP/2	2/28/2007			Pending	METHOD OF ALLOCATING COMMUNICATIONS RESOURCES AND SCH
MP1221T	MP1221TW	TW	97101369	1/14/2008	I470630	1/21/2015	In Force	IMPROVED MULTI-LEVEL MEMORY
MP1221W	MP1221WO	WO	PCT/US2008/00	1/10/2008	DNA		Pending	Improved Multi-Level Memory
MP1221W	MP1221WOCN	CN	200880002210.7	1/10/2008	ZL2008800022	8/8/2012	In Force	IMPROVED MULTI-LEVEL MEMORY
MP1221W	MP1221WOJP	JP	2009-545594	1/10/2008	JP5217048	3/15/2013	In Force	AN IMPROVED MULTI-LEVEL MEMORY AND A METHOD FOR PERFOR
MP1222	MP1222	US	11/820,934	6/21/2007	8046660	10/25/2011	In Force	System And Method For Correcting Errors In Non-Volatile Memory U
MP1222	MP1222D1	US	13/280,168	10/24/2011	8566664	10/22/2013	In Force	System And Method For Correcting Errors In Non-Volatile Memory U
MP12220	MP12220WOIN	IN	1718/MUMNP/2	3/5/2007			Pending	CHANNEL PROFILER AND METHOD OF PROFILING AN INCOMING SIG
MP12220	MP12220WOJP	JP	2009-552286	3/5/2007			Pending	CHANNEL PROFILER AND METHOD OF PROFILING AN INCOMING SIG
MP12221	MP12221CN	CN	200810081776.5	3/13/2008	ZL2008100817	9/14/2011	In Force	Reliable Network Packet Dispatcher With Interleaving Multi-Port Circ
MP12222	MP12222	US	11/686,417	3/15/2007	7693070	4/6/2010	In Force	CONGESTION REDUCING RELIABLE TRANSPORT PACKET RETRY ENGI
MP12223	MP12223	US	11/742,282	4/30/2007	8387073	2/26/2013	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK PACKETS
MP12224	MP12224	US	11/694,698	3/30/2007	8185715	5/22/2012	In Force	METHOD AND SYSTEM FOR MANAGING METADATA IN STORAGE VIR
MP12225	MP12225WOIN	IN	2098/MUMNP/2	5/2/2007			Pending	METHOD AND APPARATUS FOR CORRECTING LINEAR ERROR PHASE
MP12225	MP12225WOJP	JP	2010-504875	5/2/2007	JP5460579	4/2/2014	In Force	METHOD AND APPARATUS FOR CORRECTING LINEAR ERROR PHASE
MP12225	MP12225WOUS	US	12/598,544	5/2/2007	8340205	12/25/2012	In Force	METHOD AND APPARATUS FOR CORRECTING LINEAR ERROR PHASE
MP12226	MP12226	US	11/752,212	5/22/2007	8250203	8/21/2012	In Force	METHOD AND SYSTEM FOR NETWORK TRAFFIC MONITORING
MP12227	MP12227	US	11/761,279	6/11/2007	8095978	1/10/2012	In Force	METHOD AND SYSTEM FOR MANAGING PORTS IN A HOST BUS ADAP
MP12228	MP12228	US	11/762,578	6/13/2007	8102769	1/24/2012	In Force	METHOD AND SYSTEM FOR NETWORK COMMUNICATION
MP12228	MP12228C1	US	13/330,085	12/19/2011	8792351	7/29/2014	In Force	METHOD AND SYSTEM FOR NETWORK COMMUNICATION
MP12229	MP12229	US	11/965,037	12/27/2007	8010809	8/30/2011	In Force	METHOD AND SYSTEM FOR SECURING NETWORK DATA

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MP12229	MP12229C1	US	13/190,165	7/25/2011	8261099	9/4/2012	In Force	METHOD AND SYSTEM FOR SECURING NETWORK DATA
MP1222T	MP1222TW	TW	96129103	8/7/2007	I451430	9/1/2014	In Force	System And Method For Correcting Errors In Non-Volatile Memory (
MP1222W	MP1222WO	WO	PCT/US2007/01	8/6/2007	DNA		Pending	System And Method For Correcting Errors In Non-Volatile Memory (
MP1222W	MP1222WOCN	CN	200780029503.2	8/6/2007	ZL2007800295	10/23/2013	In Force	SYSTEM AND METHOD FOR CORRECTING ERRORS IN NON-VOLATILE
MP12230	MP12230	US	12/111,057	4/28/2008	8904077	12/2/2014	In Force	POWER MANAGEMENT FOR HOST BUS ADAPTERS
MP12232	MP12232	US	11/842,830	8/21/2007	8411677	4/2/2013	In Force	METHOD AND SYSTEM FOR PROCESSING LAYERED NETWORKING PI
MP12234	MP12234	US	11/849,122	8/31/2007	7605658	10/20/2009	In Force	Resistively Loaded Single Stage Differential Amplifier Having Zero V
MP12236	MP12236	US	11/864,651	9/28/2007	7903558	3/8/2011	In Force	METHOD AND SYSTEM FOR MONITORING A NETWORK LINK IN NET
MP12236	MP12236C1	US	13/026,007	2/11/2011	8441929	5/14/2013	In Force	METHOD AND SYSTEM FOR MONITORING A NETWORK LINK IN NET
MP12238	MP12238	US	11/982,391	11/1/2007	8086609	12/27/2011	In Force	Graph Caching
MP12238	MP12238C1	US	13/311,244	12/5/2011	9787693	10/10/2017	In Force	Graph Caching
MP12239	MP12239	US	11/982,433	11/1/2007	8819217	8/26/2014	In Force	Intelligent Graph Walking
MP1224	MP1224	US	11/890,732	8/7/2007	7564394	7/21/2009	In Force	BUFFER FOR A/D CONVERSION
MP12240	MP12240WOIN	IN	3312/CHENP/20	11/6/2007			Pending	METHOD AND SYSTEM FOR DIGITALLY CORRECTING SAMPLING EFFE
MP12241	MP12241	US	12/052,130	3/20/2008	7908404	3/15/2011	In Force	METHOD AND SYSTEM FOR MANAGING NETWORK AND STORAGE D
MP12242	MP12242	US	11/986,975	11/27/2007	7949683	5/24/2011	In Force	Method And Apparatus For Traversing A Compressed Deterministic Fi
MP12242	MP12242WO	WO	PCT/US2008/01	10/7/2008	DNA		Pending	Method And Apparatus For Traversing A Deterministic Finite Automa
MP12242	MP12242WOEP	EP	08853776.6	10/7/2008	2215563	5/4/2016	In Force	Method And Apparatus For Traversing A Deterministic Finite Automa
MP12242	MP12242WOEPDE	DE	08853776.6	10/7/2008	602008044099	5/4/2016	In Force	Method And Apparatus For Traversing A Deterministic Finite Automa
MP12242	MP12242WOEPFI	FI	08853776.6	10/7/2008	2215563	5/4/2016	In Force	Method And Apparatus For Traversing A Deterministic Finite Automa
MP12242	MP12242WOEPFR	FR	08853776.6	10/7/2008	2215563	5/4/2016	In Force	Method And Apparatus For Traversing A Deterministic Finite Automa
MP12242	MP12242WOEPSE	SE	08853776.6	10/7/2008	2215563	5/4/2016	In Force	Method And Apparatus For Traversing A Deterministic Finite Automa
MP12243	MP12243	US	11/986,970	11/27/2007	8180803	5/15/2012	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WO	WO	PCT/US2008/01	10/7/2008	DNA		Pending	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WOEP	EP	08854609.8	10/7/2008	2215565	5/4/2016	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WOEPDE	DE	08854609.8	10/7/2008	602008044100	5/4/2016	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WOEPFI	FI	08854609.8	10/7/2008	2215565	5/4/2016	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WOEPFR	FR	08854609.8	10/7/2008	2215565	5/4/2016	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12243	MP12243WOEPSE	SE	08854609.8	10/7/2008	2215565	5/4/2016	In Force	Deterministic Finite Automata (DFA) Graph Compression
MP12245	MP12245	US	11/963,566	12/21/2007	7720064	5/18/2010	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK AND STORAGE D
MP12245	MP12245C1	US	12/751,955	3/31/2010	8225004	7/17/2012	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK AND STORAGE D
MP12246	MP12246	US	11/966,857	12/28/2007	7719969	5/18/2010	In Force	SYSTEM AND METHOD FOR ASSIGNING NETWORK DEVICE PORT ADD
MP12248	MP12248	US	12/019,094	1/24/2008	7609710	10/27/2009	In Force	METHOD AND SYSTEM FOR CREDIT MANAGEMENT IN A NETWORKIN
MP12251	MP12251	US	12/034,913	2/21/2008	7962675	6/14/2011	In Force	METHOD AND SYSTEM FOR COMMUNICATING WITH A HOST BUS AD

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MP12252	MP12252	US	12/392,004	2/24/2009	8677144	3/18/2014	In Force	Secure Software And Hardware Association Technique
MP12253	MP12253	US	12/037,847	2/26/2008	8116206	2/14/2012	In Force	METHOD AND SYSTEM FOR ROUTING FRAMES IN A NETWORK
MP12254	MP12254	US	12/039,604	2/28/2008	7895300	2/22/2011	In Force	SYSTEMS AND METHODS FOR TESTING DEVICE PORTS IN A STORAG
MP12255	MP12255	US	12/082,246	4/9/2008	8358699	1/22/2013	In Force	METHOD AND SYSTEM FOR SELECTION OF REFERENCE PICTURE ANI
MP12256	MP12256C1	US	14/144,252	12/30/2013	8891706	11/18/2014	In Force	SYSTEM AND METHOD FOR OPTIMIZING USE OF CHANNEL STATE IN
MP12256	MP12256WOUS	US	12/988,348	4/17/2008	8619923	12/31/2013	In Force	SYSTEM AND METHOD FOR OPTIMIZING USE OF CHANNEL STATE IN
MP12258	MP12258	US	12/135,973	6/9/2008	7773629	8/10/2010	In Force	METHOD AND SYSTEM FOR NETWORK PORT ADDRESSING
MP12259	MP12259	US	12/214,789	6/20/2008	8471601	6/25/2013	In Force	Single-Ended to Differential Converter
MP12260	MP12260	US	12/489,241	6/22/2009	8296386	10/23/2012	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK PACKETS
MP12260	MP12260C1	US	13/610,095	9/11/2012	8867537	10/21/2014	In Force	METHOD AND SYSTEM FOR PROCESSING NETWORK PACKETS
MP12261	MP12261	US	12/165,464	6/30/2008	7765317	7/27/2010	In Force	SYSTEM AND METHODS FOR LOCATING FPDU HEADERS WHEN MAF
MP12262	MP12262	US	12/505,430	7/17/2009	8351442	1/8/2013	In Force	METHOD AND SYSTEM FOR NETWORK COMMUNICATION
MP12264	MP12264	US	12/536,336	8/5/2009	8793399	7/29/2014	In Force	METHOD AND SYSTEM FOR ACCELERATING NETWORK PACKET PRO
MP12265	MP12265	US	12/539,156	8/11/2009	8391300	3/5/2013	In Force	CONFIGURABLE SWITCH ELEMENT AND METHODS THEREOF
MP12265	MP12265C1	US	13/758,543	2/4/2013	8976800	3/10/2015	In Force	CONFIGURABLE SWITCH ELEMENT AND METHODS THEREOF
MP12266	MP12266	US	12/229,617	8/25/2008	8176300	5/8/2012	In Force	Method And Apparatus For Content Based Searching
MP12267	MP12267	US	12/547,414	8/25/2009	8200473	6/12/2012	In Force	EMULATION OF MULTIPLE MDIO MANAGEABLE DEVICES
MP12268	MP12268	US	12/200,649	8/28/2008	7667985	2/23/2010	In Force	COMPUTER NETWORK DEVICE AND METHOD OF ASSEMBLY
MP12269	MP12269	US	12/201,304	8/29/2008	7646602	1/12/2010	In Force	APPARATUS AND METHODS FOR COOLING NETWORK SWITCHES
MP1227	MP1227	US	11/828,975	7/26/2007	8190976	5/29/2012	In Force	HIGH-SPEED INTERFACE FOR HOLOGRAPHIC STORAGE READ CHANNE
MP1227	MP1227C1	US	13/480,288	5/24/2012	8458573	6/4/2013	In Force	HIGH-SPEED INTERFACE FOR READ CHANNEL
MP12270	MP12270	US	12/201,359	8/29/2008	7675750	3/9/2010	In Force	APPARATUS AND METHODS FOR COOLING NETWORK SWITCHES
MP12271	MP12271	US	12/586,169	9/17/2009	7961033	6/14/2011	In Force	DLL-Based Temperature Sensor
MP12273	MP12273	US	12/277,129	11/24/2008	8473523	6/25/2013	In Force	Deterministic Finite Automata Graph Traversal With Nodal Bit Mappi
MP12273	MP12273C1	US	13/905,711	5/30/2013	8886680	11/11/2014	In Force	Deterministic Finite Automata Graph Traversal With Nodal Bit Mappi
MP12273	MP12273C1C1	US	14/516,807	10/17/2014	9495479	11/15/2016	In Force	Traversal With Arc Configuration Information
MP12276	MP12276	US	12/410,294	3/24/2009	7907414	3/15/2011	In Force	NETWORK DEVICE HAVING LEVERS AND ASSOCIATED METHODS
MP12278	MP12278	US	12/556,064	9/9/2009	8068482	11/29/2011	In Force	METHOD AND SYSTEM FOR NETWORK SWITCH ELEMENT
MP12278	MP12278C1	US	13/273,008	10/13/2011	8873546	10/28/2014	In Force	METHOD AND SYSTEM FOR NETWORK SWITCH ELEMENT
MP12278	MP12278WOCN	CN	200980145538.9	11/3/2009	ZL2009801455	5/13/2015	In Force	METHOD AND SYSTEM FOR NETWORK SWITCH ELEMENT
MP12279	MP12279	US	12/412,050	3/26/2009	9094343	7/28/2015	In Force	METHOD AND SYSTEM FOR TAKING A NETWORK PORT OFFLINE
MP1228	MP1228	US	11/890,407	8/6/2007	8095745	1/10/2012	In Force	NON-SEQUENTIAL TRANSFER OF DATA FROM A MEMORY
MP1228	MP1228D1	US	13/331,901	12/20/2011	9053052	6/9/2015	In Force	NON-SEQUENTIAL TRANSFER OF DATA FROM A MEMORY
MP12280	MP12280	US	12/411,176	3/25/2009	7907546	3/15/2011	In Force	METHOD AND SYSTEM FOR PORT NEGOTIATION

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MP12281	MP12281	US	12/615,152	11/9/2009	8116311	2/14/2012	In Force	METHOD AND SYSTEM FOR TAG ARBITRATION IN SWITCHES
MP12282	MP12282	US	12/423,626	4/14/2009	8089971	1/3/2012	In Force	METHOD AND SYSTEM FOR TRANSMITTING FLOW CONTROL INFOR
MP12284	MP12284	US	12/415,873	3/31/2009	8060682	11/15/2011	In Force	METHOD AND SYSTEM FOR MULTI-LEVEL SWITCH CONFIGURATION
MP12288	MP12288	US	12/315,080	11/26/2008	8601139	12/3/2013	In Force	Multiple Core Session Initiation Protocol (SIP)
MP12290	MP12290	US	12/552,222	9/1/2009	8155022	4/10/2012	In Force	METHOD AND SYSTEM FOR CONTROLLING POWER CONSUMPTION
MP12291	MP12291	US	12/731,065	3/24/2010	8351448	1/8/2013	In Force	METHOD AND SYSTEM FOR EXTENDED PORT ADDRESSING
MP12291	MP12291C1	US	13/711,380	12/11/2012	9071558	6/30/2015	In Force	METHOD AND SYSTEM FOR EXTENDED PORT ADDRESSING
MP12292	MP12292	US	12/410,277	3/24/2009	8068338	11/29/2011	In Force	NETWORK DEVICE WITH BAFFLE FOR REDIRECTING COOLING AIR AF
MP12293	MP12293	US	12/551,302	8/31/2009	8978052	3/10/2015	In Force	SYSTEM AND METHODS FOR INTER-DRIVER COMMUNICATION
MP12295	MP12295	US	12/774,574	5/5/2010	8705351	4/22/2014	In Force	METHOD AND SYSTEM FOR LOAD BALANCING IN NETWORKS
MP12296	MP12296	US	12/471,331	5/22/2009	8069293	11/29/2011	In Force	FLEXIBLE SERVER NETWORK CONNECTION UPGRADE SYSTEMS AND
MP12298	MP12298C1	US	13/447,652	4/16/2012	8996730	3/31/2015	In Force	SYSTEM AND METHOD TO RESTORE MAXIMUM PAYLOAD SIZE IN A
MP12299	MP12299	US	12/572,835	10/2/2009	8504750	8/6/2013	In Force	SYSTEM AND METHOD TO PROCESS EVENT REPORTING IN AN ADAP
MP12299	MP12299C1	US	13/856,848	4/4/2013	9043519	5/26/2015	In Force	SYSTEM AND METHOD TO PROCESS EVENT REPORTING IN AN ADAPT
MP12304	MP12304	US	12/544,658	8/20/2009	8065454	11/22/2011	In Force	SYSTEM AND METHOD FOR MAPPING FUNCTIONS TO AND FROM NE
MP12304	MP12304D1	US	13/221,737	8/30/2011	8225018	7/17/2012	In Force	SYSTEM AND METHOD FOR MAPPING FUNCTIONS TO AND FROM NE
MP12305	MP12305	US	12/859,690	8/19/2010	8706905	4/22/2014	In Force	METHOD AND SYSTEM FOR ROUTING INFORMATION IN A NETWORK
MP12306	MP12306	US	12/572,810	10/2/2009	8671228	3/11/2014	In Force	SYSTEM AND METHODS FOR MANAGING VIRTUAL ADAPTER INSTAN
MP12307	MP12307	US	12/651,270	12/31/2009	7970927	6/28/2011	In Force	CONCURRENT TRANSMIT PROCESSING
MP12307	MP12307C1	US	13/111,475	5/19/2011	8275903	9/25/2012	In Force	CONCURRENT TRANSMIT PROCESSING
MP12309	MP12309	US	12/691,629	1/21/2010	9329794	5/3/2016	In Force	SYSTEM AND METHODS FOR DATA MIGRATION
MP1231	MP1231	US	11/833,827	8/3/2007	8699601	4/15/2014	In Force	DISTANCE-LEVEL COMBINING FOR MIMO SYSTEMS WITH HARQ AND
MP1231	MP1231C1	US	14/167,661	1/29/2014	8787486	7/22/2014	In Force	DISTANCE-LEVEL COMBINING FOR MIMO SYSTEMS WITH HARQ AND
MP12310	MP12310	US	12/695,648	1/28/2010	8356194	1/15/2013	In Force	METHOD AND APPARATUS FOR ESTIMATING OVERSHOOT POWER AF
MP12310	MP12310D1	US	13/713,650	12/13/2012	8868949	10/21/2014	In Force	Method And Apparatus For Power Control
MP12310	MP12310D1C1	US	14/488,724	9/17/2014	9703351	7/11/2017	In Force	Method And Apparatus For Power Control
MP12310	MP12310WO	WO	PCT/US2011/02	1/24/2011	DNA		Pending	Method And Apparatus For Power Control
MP12310	MP12310WOEP	EP	11703077.5	1/24/2011	2529282	11/4/2015	In Force	Method And Apparatus For Power Control
MP12310	MP12310WOEPDE	DE	11703077.5	1/24/2011	602011021124	11/4/2015	In Force	Method And Apparatus For Power Control
MP12310	MP12310WOEPSE	SE	11703077.5	1/24/2011	2529282	11/4/2015	In Force	Method And Apparatus For Power Control
MP12310	MP12310WOJP	JP	2012-551212	1/24/2011	JP5827242	12/2/2015	In Force	METHOD AND APPARATUS FOR POWER CONTROL
MP12310	MP12310WOJPD1	JP	2015-074985	4/1/2015	JP6154843	6/9/2017	In Force	POWER CONTROL METHOD AND POWER CONTROL APPARATUS
MP12313	MP12313	US	12/730,139	3/23/2010	8406152	3/26/2013	In Force	SYSTEM AND METHODS FOR PRESENTING STORAGE
MP12313	MP12313C1	US	13/770,828	2/19/2013	8873430	10/28/2014	In Force	SYSTEM AND METHODS FOR PRESENTING STORAGE

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MP12314	MP12314	US	12/759,316	4/13/2010	8307111	11/6/2012	In Force	SYSTEMS AND METHODS FOR BANDWIDTH SCAVENGING AMONG A
MP12314	MP12314C1	US	13/644,203	10/3/2012	9003038	4/7/2015	In Force	SYSTEMS AND METHODS FOR BANDWIDTH SCAVENGING AMONG A
MP12315	MP12315	US	13/090,882	4/20/2011	8661266	2/25/2014	In Force	SYSTEM AND METHOD FOR SECURE DEVICE KEY STORAGE
MP12316	MP12316	US	12/769,463	4/28/2010	8826271	9/2/2014	In Force	Method And Apparatus For A Virtual System On Chip
MP12316	MP12316C1	US	14/452,873	8/6/2014	9378033	6/28/2016	In Force	Method And Apparatus For A Virtual System On Chip
MP12316	MP12316C1C1	US	15/192,214	6/24/2016	9665300	5/30/2017	In Force	Method And Apparatus For Virtualization
MP12316	MP12316C1C1C1	US	15/499,177	4/27/2017	9823868	11/21/2017	In Force	Method And Apparatus For Virtualization
MP12316	MP12316C2	US	14/452,878	8/6/2014	10146463	12/4/2018	In Force	Method And Apparatus For A Virtual System On Chip
MP1231T	MP1231TW	TW	96129266	8/8/2007			Pending	DISTANCE-LEVEL COMBINING FOR MIMO SYSTEMS WITH HARQ ANI
MP1231W	MP1231WO	WO	PCT/US2007/01	8/8/2007	DNA		Pending	DISTANCE-LEVEL COMBINING FOR MIMO SYSTEMS WITH HARQ ANI
MP12320	MP12320	US	12/774,608	5/5/2010	9781477	10/3/2017	In Force	SYSTEM AND METHOD FOR LOW-LATENCY MULTIMEDIA STREAMIN
MP12321	MP12321	US	12/795,563	6/7/2010	8315268	11/20/2012	In Force	METHOD AND SYSTEM FOR COMMUNICATION WITH AN ADAPTER
MP12325	MP12325	US	12/826,105	6/29/2010	8250252	8/21/2012	In Force	SYSTEM AND METHODS FOR USING A DMA MODULE FOR A PLURAL
MP12325	MP12325C1	US	13/489,016	6/5/2012	8392629	3/5/2013	In Force	SYSTEM AND METHODS FOR USING A DMA MODULE FOR A PLURALI
MP12326	MP12326	US	12/914,852	10/28/2010	8478909	7/2/2013	In Force	METHOD AND SYSTEM FOR COMMUNICATION ACROSS MULTIPLE CH
MP12327	MP12327	US	12/848,184	8/1/2010	8544106	9/24/2013	In Force	SYSTEM AND METHOD FOR ENABLING ACCESS TO A PROTECTED HAR
MP12330	MP12330	US	13/216,598	8/24/2011	8441922	5/14/2013	In Force	METHOD AND SYSTEM FOR CONGESTION MANAGEMENT IN NETWO
MP12331	MP12331	US	12/901,409	10/8/2010	8510527	8/13/2013	In Force	SYSTEM AND METHODS FOR MIGRATING INFORMATION FROM SOU
MP12332	MP12332	US	12/902,020	10/11/2010	8301817	10/30/2012	In Force	RING BUS FOR SHARING RESOURCES AMONG MULTIPLE ENGINES
MP12333	MP12333	US	12/901,994	10/11/2010	8386644	2/26/2013	In Force	SYSTEMS AND METHODS FOR EFFICIENTLY PROCESSING LARGE DATA
MP12335	MP12335	US	12/941,621	11/8/2010	8566573	10/22/2013	In Force	SELECTABLE INITIALIZATION FOR ADAPTERS
MP12337	MP12337	US	12/964,295	12/9/2010	9426097	8/23/2016	In Force	FACILITATING COMMUNICATION BETWEEN DEVICES IN A NETWORK
MP12338	MP12338	US	13/325,943	12/14/2011	8977944	3/10/2015	In Force	DRAM Address Protection
MP12338	MP12338C1	US	14/629,770	2/24/2015	9239753	1/19/2016	In Force	DRAM Address Protection
MP12339	MP12339	US	13/326,091	12/14/2011	9596193	3/14/2017	In Force	Messaging With Flexible Transmit Ordering
MP12339	MP12339D1	US	14/026,293	9/13/2013	9065781	6/23/2015	In Force	Messaging With Flexible Transmit Ordering
MP12339	MP12339D1C1	US	14/746,674	6/22/2015	9264385	2/16/2016	In Force	Messaging With Flexible Transmit Ordering
MP12343	MP12343	US	13/369,652	2/9/2012	8525572	9/3/2013	In Force	Level-Up Shifter Circuit
MP12343	MP12343I1	US	13/968,523	8/16/2013	8896360	11/25/2014	In Force	Level-Up Shifter Circuit For High Speed And Low Power Applications
MP12344	MP12344	US	13/369,562	2/9/2012	8519785	8/27/2013	In Force	Differential Amplifier With Duty Cycle Compensation
MP12345	MP12345	US	13/397,550	2/15/2012	8536944	9/17/2013	In Force	Differential Amplifier With De-Emphasis
MP12346	MP12346	US	13/397,303	2/15/2012	8634509	1/21/2014	In Force	Synchronized Clock Phase Interpolator
MP12347	MP12347	US	13/396,824	2/15/2012	9263151	2/16/2016	In Force	Memory Interface With Selectable Evaluation Modes
MP12348	MP12348	US	13/369,579	2/9/2012	8513994	8/20/2013	In Force	State Machine For Deskew Delay Locked Loop

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MP12349	MP12349	US	13/370,784	2/10/2012	9143140	9/22/2015	In Force	Multi-Function Delay Locked Loop
MP12349	MP12349C1	US	14/639,476	3/5/2015	9306584	4/5/2016	In Force	Multi-Function Delay Locked Loop
MP1235	MP1235	US	11/836,619	8/9/2007	7595745	9/29/2009	In Force	CIRCUIT FOR CONVERTING A VOLTAGE RANGE OF A LOGIC SIGNAL
MP12350	MP12350	US	13/029,018	2/16/2011	8682632	3/25/2014	In Force	SIMULATION SYSTEM AND METHOD THEREOF
MP12352	MP12352	US	13/035,114	2/25/2011	8880732	11/4/2014	In Force	METHOD AND SYSTEM FOR APPLICATION ISOLATION
MP12353	MP12353	US	13/168,395	6/24/2011	9398033	7/19/2016	In Force	Regular Expression Processing Automaton
MP12354	MP12354	US	13/087,817	4/15/2011	8456331	6/4/2013	In Force	System And Method Of Compression And Decompression
MP12354	MP12354C1	US	13/888,851	5/7/2013	9054729	6/9/2015	In Force	System And Method Of Compression And Decompression
MP12355	MP12355	US	13/067,051	5/4/2011	8831108	9/9/2014	In Force	Low latency rate control system and method
MP12355	MP12355D1	US	14/480,469	9/8/2014	9445107	9/13/2016	In Force	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355HK	HK	14104540.1	5/4/2012	11914858	11/24/2017	In Force	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355JPD1	JP	2016-029840	2/19/2016	JP6226490	11/8/2017	In Force	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355JPD2	JP	2017-143464	7/25/2017			Pending	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355WO	WO	PCT/US2012/03	5/4/2012	DNA		Pending	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355WOCN	CN	2012800316776	5/4/2012	ZL2012800316	2/1/2017	In Force	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355WOJP	JP	2014-509498	5/4/2012			Pending	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12355	MP12355WOKR	KR	10-2013-703224	5/4/2012	101809306	12/14/2017	In Force	LOW LATENCY RATE CONTROL SYSTEM AND METHOD
MP12356	MP12356	US	13/067,050	5/4/2011	9025672	5/5/2015	In Force	ON-DEMAND INTRA-REFRESH FOR END-TO END CODED VIDEO TRAN
MP12356	MP12356HK	HK	14106723.5	5/4/2012	1193292B	12/8/2017	In Force	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12356	MP12356WO	WO	PCT/US2012/03	5/4/2012	DNA		Pending	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12356	MP12356WOCN	CN	201280031662.4	5/4/2012	ZL2012800316	2/15/2017	In Force	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12356	MP12356WOJP	JP	2014-509499	5/4/2012	JP5784823	9/24/2015	In Force	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12356	MP12356WOKR	KR	10-2013-703224	5/4/2012		3/6/2019	In Force	ON-DEMAND INTRA-REFRESH FOR END-TO-END CODED VIDEO TRAN
MP12359	MP12359	US	13/105,401	5/11/2011	8350732	1/8/2013	In Force	Compression With Adjustable Quality/Bandwidth Capability
MP12360	MP12360	US	13/168,450	6/24/2011	9858051	1/2/2018	In Force	Regex Compiler
MP12360	MP12360HK	HK	14106732.4	6/20/2012	1193278	3/9/2018	In Force	Compiler For Regular Expressions
MP12360	MP12360WO	WO	PCT/US2012/04	6/20/2012	DNA		Pending	Compiler For Regular Expressions
MP12360	MP12360WOCN	CN	2012800387992	6/20/2012	ZL2012800387	5/3/2017	In Force	Compiler For Regular Expressions
MP12360	MP12360WOCND1	CN	201710258370.9	6/20/2012	ZL2012800387	5/3/2017	In Force	Compiler For Regular Expressions
MP12360	MP12360WODE	DE	112012002624.3	6/20/2012			Pending	Compiler For Regular Expressions
MP12360	MP12360WOKRD1	KR	1020167020582	6/20/2012		7/17/2018	In Force	Compiler For Regular Expressions
MP12361	MP12361	US	13/168,323	6/24/2011	8990259	3/24/2015	In Force	Anchored Patterns
MP12361	MP12361C1	US	14/632,448	2/26/2015	9514246	12/6/2016	In Force	Anchored Patterns
MP12361	MP12361WO	WO	PCT/US2012/04	6/20/2012	DNA		Pending	Anchored Patterns

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MP12361	MP12361WOKR	KR	1020147001978	6/20/2012		7/14/2015	In Force	Anchored Patterns
MP12362	MP12362	US	13/067,880	7/1/2011	9025665	5/5/2015	In Force	Video encoder bit estimator for macroblock encoding
MP12362	MP12362D1	US	14/329,843	7/11/2014	9094669	7/28/2015	In Force	VIDEO ENCODER BIT ESTIMATOR FOR MACROBLOCK ENCODING
MP12363	MP12363	US	13/181,270	7/12/2011	8488601	7/16/2013	In Force	METHOD AND SYSTEM FOR LINK AGGREGATION
MP12364	MP12364	US	13/181,251	7/12/2011	8467395	6/18/2013	In Force	METHOD AND SYSTEM FOR LINK AGGREGATION
MP12365	MP12365	US	13/193,462	7/28/2011	9071544	6/30/2015	In Force	METHOD AND SYSTEM FOR MANAGING NETWORK ELEMENTS
MP12366	MP12366	US	13/565,271	8/2/2012	10277510	4/30/2019	In Force	System and Method for Storing Lookup Request Rules in Multiple M
MP12367	MP12367	US	13/565,784	8/2/2012	8937952	1/20/2015	In Force	Packet Classification
MP12367	MP12367C1	US	13/664,015	10/30/2012	8934488	1/13/2015	In Force	Identifying Duplication in Decision Trees
MP12367	MP12367C1I1	US	13/831,487	3/14/2013	9208438	12/8/2015	In Force	Duplication in Decision Trees
MP12367	MP12367C2	US	13/686,269	11/27/2012	8937954	1/20/2015	In Force	Decision Tree Level Merging
MP12367	MP12367C3	US	14/570,626	12/15/2014	9191321	11/17/2015	In Force	Packet Classification
MP12367	MP12367HK	HK	14107043.6	8/2/2012	1193686	6/1/2018	In Force	Packet Classification By An Optimised Decision Tree
MP12367	MP12367WO	WO	PCT/US2012/04	8/2/2012	DNA		Pending	Packet Classification By An Optimised Decision Tree
MP12367	MP12367WOCN	CN	201280048417.4	8/2/2012	ZL2012800484	8/25/2017	In Force	Packet Classification By An Optimised Decision Tree
MP12368	MP12368	US	13/565,389	8/2/2012	9596222	3/14/2017	In Force	Method and apparatus encoding a rule for a lookup request in a proc
MP12369	MP12369	US	13/565,406	8/2/2012	9344366	5/17/2016	In Force	System and method for rule matching in a processor
MP12369	MP12369C1	US	15/145,052	5/3/2016	9866540	1/9/2018	In Force	System And Method For Rule Matching In A Processor
MP12370	MP12370	US	13/565,727	8/2/2012	8711861	4/29/2014	In Force	Lookup Front End Packet Input Processor
MP12370	MP12370C1	US	13/599,276	8/30/2012	9729527	8/8/2017	In Force	Lookup Front End Packet Input Processor
MP12370	MP12370C2	US	14/138,931	12/23/2013	9031075	5/12/2015	In Force	Lookup Front End Packet Input Processor
MP12370	MP12370WO	WO	PCT/US2012/04	8/2/2012	DNA		Pending	LOOKUP FRONT END INPUT PROCESSOR
MP12370	MP12370WOKR	KR	1020147005800	8/2/2012		12/17/2014	In Force	LOOKUP FRONT END INPUT PROCESSOR
MP12371	MP12371	US	13/565,767	8/2/2012	8606959	12/10/2013	In Force	Lookup Front End Packet Output Processor
MP12371	MP12371C1	US	14/082,365	11/18/2013	9497117	11/15/2016	In Force	Lookup Front End Packet Output Processor
MP12371	MP12371WO	WO	PCT/US2012/04	8/2/2012	DNA		Pending	Lookup Front End Output Processor
MP12371	MP12371WOJP	JP	2014-524092	8/2/2012	JP5657840	1/21/2015	In Force	LOOKUP FRONT END PACKET INPUT PROCESSOR
MP12372	MP12372	US	13/565,741	8/2/2012	9391892	7/12/2016	In Force	Method and apparatus for managing transport operations to a cluste
MP12372	MP12372-2	US	13/565,743	8/2/2012	9319316	4/19/2016	In Force	Method and apparatus for managing transfer of transport operations
MP12372	MP12372-3	US	13/565,746	8/2/2012	9525630	12/20/2016	In Force	Method and Apparatus for Assigning Resources Used To Manage Tra
MP12372	MP12372-4	US	13/565,749	8/2/2012	8954700	2/10/2015	In Force	Method and Apparatus for Managing Processing Thread Migration Be
MP12372	MP12372-4C1	US	14/592,384	1/8/2015	9531690	12/27/2016	In Force	Method and Apparatus for Managing Processing Thread Migration Be
MP12373	MP12373	US	13/565,775	8/2/2012	9137340	9/15/2015	In Force	Incremental Update
MP12373	MP12373CN	CN	201280048291.0	8/2/2012	ZL2012800482	12/14/2018	In Force	Incremental Update of Rules for Packet Classification

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MP12373	MP12373DE	DE	102014 001 498	2/5/2014			Pending	Incremental Update Heuristics
MP12373	MP12373HK	HK	14111478.2	2/8/2012			Pending	Incremental Update of Rules for Packet Classification
MP12373	MP12373I1	US	13/831,232	3/14/2013	9183244	11/10/2015	In Force	Rule Modification in Decision Trees
MP12373	MP12373I2	US	13/831,191	3/14/2013	10229139	3/12/2019	In Force	Incremental Update Heuristics
MP12373	MP12373WO	WO	PCT/US2012/04	8/2/2012	DNA		Pending	Incremental Update of Rules for Packet Classification
MP12374	MP12374	US	13/565,735	8/2/2012	9065860	6/23/2015	In Force	Method And Apparatus For Multiple Access Of Plural Memory Bank
MP12374	MP12374-2	US	13/565,736	8/2/2012	8966152	2/24/2015	In Force	On-Chip Memory (OCM) Physical Bank Parallelism
MP12375	MP12375	US	13/565,459	8/2/2012	8719331	5/6/2014	In Force	Work Migration in A Processor
MP12375	MP12375C1	US	14/230,698	3/31/2014	9614762	4/4/2017	In Force	Work Migration in A Processor
MP12376	MP12376	US	13/565,422	8/2/2012	8472452	6/25/2013	In Force	Lookup Cluster Complex
MP12376	MP12376C1	US	13/901,220	5/23/2013	8995449	3/31/2015	In Force	Lookup Cluster Complex
MP12376	MP12376C1C1	US	14/660,132	3/17/2015	9225643	12/29/2015	In Force	Lookup Cluster Complex
MP12376	MP12376I1	US	13/843,353	3/15/2013	8923306	12/30/2014	In Force	Phased Bucket Pre-Fetch in A Network Processor
MP12376	MP12376I1C1	US	14/583,968	12/29/2014	9531723	12/27/2016	In Force	Phased Bucket Pre-Fetch in A Network Processor
MP12376	MP12376WO	WO	PCT/US2012/04	8/2/2012	DNA		Pending	Lookup Cluster Complex
MP12376	MP12376WOKR	KR	1020147005633	8/2/2012		12/17/2014	In Force	Lookup Cluster Complex
MP12377	MP12377	US	13/220,249	8/29/2011	8798047	8/5/2014	In Force	METHODS AND SYSTEMS FOR PROCESSING NETWORK INFORMATIO
MP12378	MP12378	US	13/238,485	9/21/2011	8572328	10/29/2013	In Force	METHOD AND SYSTEM FOR MEMORY MANAGEMENT
MP12378	MP12378C1	US	14/036,920	9/25/2013	8918593	12/23/2014	In Force	METHOD AND SYSTEM FOR MEMORY MANAGEMENT
MP12379	MP12379	US	13/433,146	3/28/2012	8953606	2/10/2015	In Force	FLEXIBLE EDGE ACCESS SWITCH AND ASSOCIATED METHODS THEREO
MP1238	MP1238	US	11/524,789	9/20/2006	8040953	10/18/2011	In Force	RELIABILITY METRIC GENERATION FOR TRELLIS-BASED DETECTION A
MP1238	MP1238CN	CN	200780043023.5	9/20/2007	ZL2007800430	3/27/2013	In Force	RELIABILITY METRIC GENERATION FOR TRELLIS-BASED DETECTION A
MP12380	MP12380	US	13/397,002	2/15/2012	8873236	10/28/2014	In Force	ELECTRONIC DEVICES HAVING COOLING MODULE WITH DIRECTION-
MP12381	MP12381	US	13/272,975	10/13/2011	9129060	9/8/2015	In Force	QoS Based Dynamic Execution Engine Selection
MP12381	MP12381C1	US	14/828,884	8/18/2015	9495161	11/15/2016	In Force	QoS Based Dynamic Execution Engine Selection
MP12382	MP12382	US	13/272,937	10/13/2011	9128769	9/8/2015	In Force	Processor With Dedicated Virtual Functions And Dynamic Assignment
MP12383	MP12383	US	13/274,767	10/17/2011	9465662	10/11/2016	In Force	Processor With Efficient Work Queuing
MP12383	MP12383HK	HK	14109347.5	9/19/2012	1195959	6/15/2018	In Force	Processor With Efficient Work Queuing
MP12383	MP12383WO	WO	PCT/US2012/05	9/19/2012	DNA		Pending	Processor With Efficient Work Queuing
MP12383	MP12383WOCN	CN	2012800564728	9/19/2012	ZL2012800564	9/29/2017	In Force	Processor With Efficient Work Queuing
MP12383	MP12383WOKR	KR	1020147013222	9/19/2012		7/17/2019	In Force	Processor With Efficient Work Queuing
MP12384	MP12384	US	13/277,613	10/20/2011	8885480	11/11/2014	In Force	Packet Priority in A Network Processor
MP12385	MP12385	US	13/281,059	10/25/2011	9065626	6/23/2015	In Force	Bit Error Rate Impact Reduction
MP12387	MP12387	US	13/280,756	10/25/2011	8850125	9/30/2014	In Force	System And Method To Provide Non-Coherent Access To A Coherent

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MP12387	MP12387C1	US	14/466,384	8/22/2014	9569366	2/14/2017	In Force	System And Method To Provide Non-Coherent Access To A Coherent
MP12388	MP12388	US	13/280,738	10/25/2011	8560757	10/15/2013	In Force	System And Method To Reduce Memory Access Latencies Using Sel
MP12388	MP12388C1	US	14/023,953	9/11/2013	8850101	9/30/2014	In Force	System And Method To Reduce Memory Access Latencies Using Sel
MP12388	MP12388HK	HK	14109348.4	9/16/2014	1195960	4/28/2017	In Force	System And Method To Reduce Memory Access Latencies Using Sel
MP12388	MP12388WO	WO	PCT/US2012/05	9/26/2012	DNA		Pending	System And Method To Reduce Memory Access Latencies Using Sel
MP12388	MP12388WOCN	CN	201280057867X	9/26/2012	ZL2012800578	6/29/2016	In Force	System And Method To Reduce Memory Access Latencies Using Sel
MP12388	MP12388WOKR	KR	1020147013564	5/20/2014		4/24/2019	In Force	System And Method To Reduce Memory Access Latencies Using Sel
MP12389	MP12389	US	13/281,052	10/25/2011	8855248	10/7/2014	In Force	Word Boundary Lock
MP12389	MP12389C1	US	14/492,557	9/22/2014	9059836	6/16/2015	In Force	Word Boundary Lock
MP1238W	MP1238WO	WO	PCT/US2007/02	9/20/2007	DNA		Pending	RELIABILITY METRIC GENERATION FOR TRELLIS-BASED DETECTION A
MP12390	MP12390	US	13/280,841	10/25/2011	9219560	12/22/2015	In Force	Multi-Protocol Serdes Phy Apparatus
MP12390	MP12390HK	HK	14107924.0	9/26/2012	1194544	12/8/2017	In Force	Multi-Protocol Serdes Phy Apparatus
MP12390	MP12390WO	WO	PCT/US2012/05	9/26/2012	DNA		Pending	Multi-Protocol Serdes Phy Apparatus
MP12390	MP12390WOCN	CN	201280052392.5	9/26/2012	ZL2012800523	11/9/2016	In Force	Multi-protocol serdes phy apparatus
MP12390	MP12390WODE	DE	112012004 451.	9/26/2012			Pending	Multi-Protocol Serdes Phy Apparatus
MP12390	MP12390WOKR	KR	1020147014165	9/26/2012		8/4/2016	In Force	MULTI-PROTOCOL SEREDS PHY APPARATUS
MP12391	MP12391	US	13/280,768	10/25/2011	8473658	6/25/2013	In Force	Input Output Bridging
MP12391	MP12391C1	US	13/905,750	5/30/2013	8595401	11/26/2013	In Force	Input Output Bridging
MP12392	MP12392	US	13/328,919	12/16/2011	9094333	7/28/2015	In Force	SYSTEMS AND METHODS FOR SENDING AND RECEIVING INFORMATI
MP12392	MP12392C1	US	14/747,790	6/23/2015	9401879	7/26/2016	In Force	SYSTEMS AND METHODS FOR SENDING AND RECEIVING INFORMATI
MP12393	MP12393	US	13/283,252	10/27/2011	9906468	2/27/2018	In Force	Packet Traffic Control In A Network Processor
MP12394	MP12394	US	13/284,289	10/28/2011	9612934	4/4/2017	In Force	Network Processor With Distributed Trace Buffers
MP12395	MP12395	US	13/285,773	10/31/2011	9059945	6/16/2015	In Force	Work Request Processor
MP12395	MP12395C1	US	14/670,934	3/27/2015	9529640	12/27/2016	In Force	Work Request Processor
MP12396	MP12396	US	13/403,697	2/23/2012	8893366	11/25/2014	In Force	TOOLS, SYSTEMS, AND METHODS FOR REMOVING CONNECTORS FRO
MP12397	MP12397	US	13/285,629	10/31/2011	9330002	5/3/2016	In Force	Multi-Core Interconnect In A Network Processor
MP12397	MP12397HK	HK	14109343.9	10/29/2012			Pending	Multi-Core Interconnect In A Network Processor
MP12397	MP12397WO	WO	PCT/US2012/06	10/29/2012	DNA		Pending	Multi-Core Interconnect In A Network Processor
MP12397	MP12397WOCN	CN	201280059239.5	10/29/2012	ZL2012800592	6/21/2019	In Force	Multi-Core Interconnect In A Network Processor
MP12397	MP12397WODE	DE	112012004551.3	10/29/2012			Pending	Multi-Core Interconnect In A Network Processor
MP12397	MP12397WOJP	JP	2014-539104	10/29/2012			Pending	Multi-Core Interconnect In A Network Processor
MP12397	MP12397WOJPD1	JP	2017-213851	11/6/2017			Pending	MULTI-CORE INTERCONNECT IN NETWORK PROCESSOR
MP12397	MP12397WOKR	KR	1020147012490	10/29/2012			Pending	Multi-Core Interconnect In A Network Processor
MP12398	MP12398	US	13/303,885	11/23/2011	9203805	12/1/2015	In Force	Reverse NFA Generation And Processing

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MP12398	MP12398C1	US	14/863,484	9/24/2015	9762544	9/12/2017	In Force	Reverse NFA Generation And Processing
MP12401	MP12401WO	WO	PCT/US2013/03	3/20/2013	DNA		Pending	Hardware And Software Association And Authentication
MP12402	MP12402	US	13/447,521	4/16/2012	9164913	10/20/2015	In Force	METHOD AND SYSTEM FOR OFFLOADING COPY ON WRITE OPERATI
MP12403	MP12403	US	13/481,122	5/25/2012	8989220	3/24/2015	In Force	High Speed Variable Bandwidth Ring-Based System
MP12404	MP12404-1	US	13/790,395	3/8/2013	9350807	5/24/2016	In Force	INTELLIGENT ADAPTER FOR PROVIDING STORAGE AREA NETWORK /
MP12404	MP12404-2	US	13/790,471	3/8/2013	9330003	5/3/2016	In Force	INTELLIGENT ADAPTER FOR MAINTAINING CACHE COHERENCY
MP12404	MP12404-3	US	13/790,499	3/8/2013	9507524	11/29/2016	In Force	IN-BAND MANAGEMENT USING AN INTELLIGENT ADAPTER AND ME
MP12404	MP12404-5	US	13/902,427	5/24/2013	9172586	10/27/2015	In Force	METHOD AND SYSTEM FOR WRITING TAG AND DATA
MP12404	MP12404-6	US	13/790,846	3/8/2013	9232005	1/5/2016	In Force	METHODS AND SYSTEMS FOR AN INTELLIGENT STORAGE ADAPTER I
MP12405	MP12405	US	13/594,343	8/24/2012	9229791	1/5/2016	In Force	SYSTEM AND METHOD FOR HIGH SPEED MULTIPLE BUFFER ALLOCA
MP12406	MP12406	US	13/644,535	10/4/2012	9256562	2/9/2016	In Force	METHOD AND SYSTEM FOR COMMUNICATION BETWEEN A COMPU
MP12407	MP12407	US	13/649,391	10/11/2012	9727494	8/8/2017	In Force	METHOD AND SYSTEM FOR COMMUNICATION BETWEEN A COMPU
MP12409	MP12409	US	13/654,308	10/17/2012	9069919	6/30/2015	In Force	METHOD AND SYSTEM FOR ARBITRATION VERIFICATION
MP1241	MP1241	US	11/700,433	1/31/2007	7911326	3/22/2011	In Force	Time Updating And Load Management Systems
MP12410	MP12410	US	14/062,418	10/24/2013	9424226	8/23/2016	In Force	METHOD AND SYSTEM FOR SIGNAL EQUALIZATION IN COMMUNICAT
MP12411	MP12411	US	13/660,928	10/25/2012	8677044	3/18/2014	In Force	METHOD AND SYSTEM FOR COMMUNICATION USING MULTIPLE DM
MP12412	MP12412	US	14/058,887	10/21/2013	9311268	4/12/2016	In Force	METHOD AND SYSTEM FOR COMMUNICATION WITH PERIPHERAL DE
MP12413	MP12413	US	13/924,107	6/21/2013	9338059	5/10/2016	In Force	SYSTEM AND METHODS FOR MANAGING NETWORKS
MP12414	MP12414	US	13/852,885	3/28/2013	9100334	8/4/2015	In Force	METHOD AND SYSTEM FOR VARIABLE LENGTH TAG PROCESSING
MP12415	MP12415	US	13/852,872	3/28/2013	9143415	9/22/2015	In Force	METHOD AND SYSTEM FOR DETECTING LOSS OF SYNCHRONIZATION
MP12416	MP12416	US	13/678,108	11/15/2012	8995455	3/31/2015	In Force	MEMORY DEVICES FOR NETWORK DEVICES AND ASSOCIATED METH
MP12417	MP12417	US	13/678,121	11/15/2012	9225808	12/29/2015	In Force	SYSTEMS AND METHODS FOR PROCESSING INFORMATION BY A NET
MP12418	MP12418	US	13/678,398	11/15/2012	9046941	6/2/2015	In Force	SYSTEM AND METHOD FOR SHARING LOGIC IN A NETWORK DEVICE
MP12419	MP12419	US	13/678,047	11/15/2012	8995425	3/31/2015	In Force	NETWORK DEVICE SCHEDULER AND METHODS THEREOF
MP12419	MP12419D1	US	14/658,544	3/16/2015	9590924	3/7/2017	In Force	NETWORK DEVICE SCHEDULER AND METHODS THEREOF
MP1241T	MP1241TW	TW	97100104	1/2/2008		5/1/2014	In Force	Master Generator For Updating Time Data Of Devices, And Integrate
MP1241W	MP1241WO	WO	PCT/US2007/02	12/28/2007	DNA		Pending	Time Updating And Load Management Systems
MP1242	MP1242	US	11/964,172	12/26/2007	8049599	11/1/2011	In Force	Power Control Device
MP1242	MP1242C1	US	13/286,072	10/31/2011	8164428	4/24/2012	In Force	Power Control Device
MP12420	MP12420	US	13/678,055	11/15/2012	8989191	3/24/2015	In Force	SYSTEMS AND METHODS FOR HARD ZONING IN NETWORKS
MP12421	MP12421	US	13/678,086	11/15/2012	9444713	9/13/2016	In Force	CUT-THROUGH ROUTING FOR NETWORK DEVICES
MP12422	MP12422	US	13/678,161	11/15/2012	8995457	3/31/2015	In Force	SYSTEMS AND METHODS FOR MODIFYING FRAMES IN A NETWORK D
MP12423	MP12423	US	13/678,135	11/15/2012	9172655	10/27/2015	In Force	SYSTEMS AND METHODS FOR QUALITY OF SERVICE IN NETWORKS
MP12424	MP12424	US	13/678,378	11/15/2012	9426063	8/23/2016	In Force	SYSTEMS AND METHODS FOR ROUTING BY NETWORK DEVICES

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MP12425	MP12425	US	13/678,385	11/15/2012	9225672	12/29/2015	In Force	SYSTEMS AND METHODS FOR PACKET GROUPING IN NETWORKS
MP12426	MP12426	US	13/678,377	11/15/2012	8996798	3/31/2015	In Force	SYSTEMS AND METHODS FOR USING TCAMS IN NETWORK DEVICES
MP12427	MP12427	US	13/678,100	11/15/2012	8924764	12/30/2014	In Force	SYSTEMS AND METHODS FOR RATE MATCHING IN NETWORKS UTILI
MP12428	MP12428	US	13/678,367	11/15/2012	9071559	6/30/2015	In Force	NETWORK DEVICES HAVING CONFIGURABLE RECEIVE PACKET QUEL
MP12428	MP12428D1	US	14/749,964	6/25/2015	9282000	3/8/2016	In Force	NETWORK DEVICES HAVING CONFIGURABLE RECEIVE PACKET QUEL
MP1242T	MP1242TW	TW	96149439	12/21/2007		5/1/2014	In Force	Power Control Device
MP1242W	MP1242WO	WO	PCT/US2007/02	12/26/2007	DNA		Pending	Power Control Device
MP1242W	MP1242WOJP	JP	2009-542968	12/26/2007	JP5229505	3/29/2013	In Force	POWER CONTROL DEVICE
MP12431	MP12431	US	13/678,145	11/15/2012	8976667	3/10/2015	In Force	METHOD AND SYSTEM FOR PROGRAMMABLE DELAY BEFORE TRAN
MP12432	MP12432	US	13/830,717	3/14/2013	9282046	3/8/2016	In Force	SMOOTHING FIFO AND METHODS THEREOF
MP12433	MP12433	US	13/678,418	11/15/2012	9172602	10/27/2015	In Force	METHOD AND SYSTEM FOR AUTO-NEGOTIATION
MP12434	MP12434	US	13/678,397	11/15/2012	9049113	6/2/2015	In Force	SYSTEMS AND METHODS FOR PRIMITIVE SEQUENCE CAPTURE AND
MP12435	MP12435	US	13/678,406	11/15/2012	9172661	10/27/2015	In Force	METHOD AND SYSTEM FOR USING LANE ALIGNMENT MARKERS
MP12436	MP12436	US	13/762,715	2/8/2013	9384113	7/5/2016	In Force	SYSTEMS AND METHODS FOR IDENTIFYING PORT PROTOCOLS USING
MP12438	MP12438	US	13/689,607	11/29/2012	9047151	6/2/2015	In Force	METHODS AND SYSTEMS FOR PROCESSING COMMANDS BY A DEVICE
MP12439	MP12439	US	13/782,673	3/1/2013	9164947	10/20/2015	In Force	METHOD AND SYSTEM FOR INSERTING COOKIES IN I/O COMMANDS
MP1244	MP1244	US	11/775,152	7/9/2007	7941643	5/10/2011	In Force	MULTI-THREAD PROCESSOR WITH MULTIPLE PROGRAM COUNTERS
MP12441	MP12441	US	13/834,838	3/15/2013	8953608	2/10/2015	In Force	METHOD AND SYSTEM FOR FRAME AGGREGATION
MP12442	MP12442	US	14/100,856	12/9/2013	9047208	6/2/2015	In Force	METHOD AND SYSTEM OF CONFIGURING VIRTUAL FUNCTION IN PERI
MP12444	MP12444	US	13/755,770	1/31/2013	8948203	2/3/2015	In Force	METHOD AND SYSTEM FOR USING ASYMETRIC TRANSFER RATES IN R
MP12446	MP12446	US	14/194,567	2/28/2014	9331942	5/3/2016	In Force	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12446	MP12446C1	US	15/140,424	4/27/2016	9729447	8/8/2017	In Force	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12446	MP12446CN	CN	201410090589.:	3/12/2014	6373021	12/7/2018	In Force	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12446	MP12446HK	HK	15102553.8	3/12/2014	6190754		Pending	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12446	MP12446JP	JP	2014-47788	3/11/2014	JP6373021	8/15/2018	In Force	Apparatus and method for processing longest prefix match table conf
MP12446	MP12446KR	KR	10-2014-002858	3/11/2014			Pending	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12446	MP12446TW	TW	103108614	3/12/2014		12/21/2018	In Force	APPARATUS AND METHOD FOR PROCESSING ALTERNATELY CONFIGU
MP12447	MP12447	US	14/142,612	12/27/2013	9256380	2/9/2016	In Force	APPARATUS AND METHOD FOR PACKET MEMORY DATAPATH PROCE
MP12448	MP12448	US	13/830,492	3/14/2013	9706564	7/11/2017	In Force	Apparatus and Method for Media Access Control Scheduling with a P
MP12449	MP12449	US	13/831,415	3/14/2013	10083200	9/25/2018	In Force	Batch Incremental Update
MP1244W	MP1244WO	WO	PCT/US2007/01	8/14/2007	DNA		Pending	A Multi-Thread Processor with Multiple Program Counters
MP12450	MP12450	US	13/830,395	3/14/2013	9237581	1/12/2016	In Force	APPARATUS AND METHOD FOR MEDIA ACCESS CONTROL SCHEDULI
MP12450	MP12450CN	CN	201410145120.(	3/11/2014	ZL2014101451	6/11/2019	In Force	APPARATUS AND METHOD FOR MEDIA ACCESS CONTROL SCHEDULI
MP12450	MP12450KR	KR	10-2014-003024	3/14/2014			Pending	APPARATUS AND METHOD FOR MEDIA ACCESS CONTROL SCHEDULI

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MP12451	MP12451-2	US	14/207,928	3/13/2014	9430511	8/30/2016	In Force	Merging Independent Writes, Separating Dependent And Independe
MP12451	MP12451-3	US	14/207,933	3/13/2014	10229144	3/12/2019	In Force	NSP Manager
MP12452	MP12452	US	13/841,743	3/15/2013	9531647	12/27/2016	In Force	Multi-Host Processing
MP12453	MP12453	US	13/896,798	5/17/2013	9595003	3/14/2017	In Force	Compiler With Mask Nodes
MP12455	MP12455	US	13/844,451	3/15/2013	9112767	8/18/2015	In Force	Method And An Accumulator Scoreboard For Out-Of-Order Rule Re
MP12456	MP12456	US	13/840,867	3/15/2013	9195939	11/24/2015	In Force	Scope In Decision Trees
MP12456	MP12456C1	US	14/922,449	10/26/2015	10460250	10/29/2019	In force	Scope In Decision Trees
MP12457	MP12457	US	13/843,992	3/15/2013	9130819	9/8/2015	In Force	Method And Apparatus For Scheduling Rule Matching In A Processo
MP12457	MP12457CN	CN	201410097575X	3/14/2014	ZL2014100975	4/12/2017	In Force	Scheduling Method And Apparatus For Scheduling Rule Matching In
MP12457	MP12457HK	HK	14110989.6	3/14/2014	1197478	3/9/2018	In Force	Scheduling Method And Apparatus For Scheduling Rule Matching In
MP12458	MP12458	US	13/843,308	3/15/2013	9276846	3/1/2016	In Force	Packet Extraction Optimization In A Network Processor
MP12459	MP12459	US	13/843,782	3/15/2013	9152494	10/6/2015	In Force	Method And Apparatus For Data Packet Integrity Checking In A Proc
MP1245W	MP1245WO	WO	PCT/US2007/01	8/14/2007	1204166		Pending	Instruction Dispatching Method and Apparatus
MP1246	MP1246	US	11/833,127	8/2/2007	7886131	2/8/2011	In Force	MULTITHREAD PROCESSOR WITH THREAD BASED THROTTLING
MP12460	MP12460	US	14/201,692	3/7/2014	9264357	2/16/2016	In Force	APPARATUS AND METHOD FOR TABLE SEARCH WITH CENTRALIZED
MP12460	MP12460CN	CN	201410182691	4/30/2014	ZL2014101826	4/10/2018	In Force	APPARATUS AND METHOD FOR TABLE SEARCH WITH CENTRALIZED
MP12460	MP12460HK	HK	15104281.3	5/5/2015	1204166	7/12/2019	In Force	APPARATUS AND METHOD FOR TABLE SEARCH WITH CENTRALIZED
MP12460	MP12460JP	JP	2014-92766	4/28/2014	JP6190754	8/30/2017	In Force	Apparatus and method for searching a table using a centralized mem
MP12460	MP12460TW	TW	103115489	4/30/2014			Pending	APPARATUS AND METHOD FOR TABLE SEARCH WITH CENTRALIZED
MP12462	MP12462	US	13/902,453	5/24/2013	9747227	8/29/2017	In Force	METHOD AND SYSTEM FOR TRANSMITTING INFORMATION FROM A
MP12463	MP12463	US	13/906,207	5/30/2013	10019203	7/10/2018	In Force	METHOD AND SYSTEM FOR PROCESSING WRITE REQUESTS
MP12464	MP12464	US	13/921,090	6/18/2013			Pending	Apparatus and Method for Uniquely Enumerating Paths in a Parse Tr
MP12464	MP12464CN	CN	201410270176.5	6/17/2014	ZL2014102701	5/5/2019	In Force	APPARATUS AND METHOD FOR UNIQUELY ENUMERATING PATHS IN
MP12464	MP12464JP	JP	2014-119339	6/10/2014	JP6383578	8/29/2018	In Force	APPARATUS AND METHOD FOR UNIQUELY ENUMERATING PATHS IN
MP12464	MP12464KR	KR	10-2014-007435	6/18/2014			Pending	APPARATUS AND METHOD FOR UNIQUELY ENUMERATING PATHS IN
MP12464	MP12464TW	TW	103120983	6/18/2014			Pending	APPARATUS AND METHOD FOR UNIQUELY ENUMERATING PATHS IN
MP12465	MP12465	US	13/922,911	6/20/2013	8898346	11/25/2014	In Force	METHOD AND SYSTEM FOR CONFIGURING NETWORK DEVICES
MP12467	MP12467	US	13/949,095	7/23/2013	9411400	8/9/2016	In Force	METHODS AND SYSTEMS FOR ADVERTSING AVAILABLE CREDIT AT M
MP12468	MP12468CN	CN	201410452112.6	7/24/2014			Pending	NETWORK INTERFACE CARD WITH VIRTUAL SWITCH AND TRAFFIC FL
MP12468	MP12468EP	EP	14177898.5	7/21/2014			Pending	NETWORK INTERFACE CARD WITH VIRTUAL SWITCH AND TRAFFIC FL
MP12468	MP12468HK	HK	15107379.9	7/24/2014			Pending	NETWORK INTERFACE CARD WITH VIRTUAL SWITHCH AND TRAFFIC F
MP12468	MP12468JP	JP	2014-151627	7/25/2014			Pending	NETWORK INTERFACE CARD WITH VIRTUAL SWITCH AND TRAFFIC FL
MP12468	MP12468KR	KR	10-2014-009269	7/22/2014			Pending	NETWORK INTERFACE CARD WITH VIRTUAL SWITCH AND TRAFFIC FL
MP12469	MP12469	US	13/954,661	7/30/2013	8977786	3/10/2015	In Force	METHOD AND SYSTEM FOR PROCESSING INFORMATION AT PERIPHE

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MP1247	MP1247	US	11/838,075	8/13/2007	7870372	1/11/2011	In Force	INTERRUPT HANDLING
MP1247	MP1247C1	US	12/987,716	1/10/2011	8190866	5/29/2012	In Force	INTERRUPT HANDLING
MP1247	MP1247C1C1	US	13/479,788	5/24/2012	8473728	6/25/2013	In Force	INTERRUPT HANDLING
MP12472	MP12472	US	14/200,939	3/7/2014	9264023	2/16/2016	In Force	Scannable Flop With A Single Storage Element
MP12473	MP12473	US	14/217,935	3/18/2014	9130549	9/8/2015	In Force	Multiplexer Flop
MP12474	MP12474	US	14/015,929	8/30/2013	9426166	8/23/2016	In Force	Method And Apparatus For Processing Finite Automata
MP12474	MP12474CN	CN	2014104321980	8/28/2014	ZL2014104321	12/14/2018	In Force	Method And Apparatus For Processing Finite Automata
MP12474	MP12474HK	HK	15108607.1	8/28/2014			Pending	Method And Apparatus For Processing Finite Automata
MP12475	MP12475	US	14/014,803	8/30/2013	9154455	10/6/2015	In Force	METHOD AND SYSTEM FOR DETERMINING DROP ELIGIBILITY OF NE
MP12476	MP12476	US	14/015,601	8/30/2013	8963601	2/24/2015	In Force	Clock Gated Delay Line Based On Setting Value
MP12477	MP12477	US	14/186,978	2/21/2014	9563399	2/7/2017	In Force	Generating A Non-Deterministic Finite Automata (NFA) Graph For R
MP12477	MP12477CN	CN	2014104315551	8/28/2014	ZL2014104315	1/4/2019	In Force	Generating a non-deterministic finite automata (NFA) graph for reg
MP12477	MP12477HK	HK	15108608.0	8/28/2014			Pending	Generating A Non-Deterministic Finite Automata (NFA) Graph For R
MP12477	MP12477KR	KR	1020140114469	8/29/2014		4/21/2016	In Force	GENERATING A NFA (Non-Deterministic finite automata) GRAPH FOR
MP12478	MP12478	US	14/186,913	2/21/2014	9507563	11/29/2016	In Force	System And Method To Traverse A Non-Deterministic Finite Automat
MP12478	MP12478CN	CN	2014104332025	8/28/2014	ZL2014104332	4/23/2019	In Force	System and method to traverse a non-deterministic finite automata (
MP12478	MP12478HK	HK	15112237.1	8/28/2014			Pending	SYSTEM AND METHOD TO TRAVERSE A NON-DETERMINISTIC FINITE
MP12479	MP12479	US	14/015,248	8/30/2013	9426165	8/23/2016	In Force	Method And Apparatus For Compilation Of Finite Automata
MP12479	MP12479CN	CN	2014104333210	8/28/2014	ZL2014104333	3/23/2018	In Force	Method And Apparatus For Compilation Of Finite Automata
MP12479	MP12479HK	HK	15108609.9	8/28/2014		7/5/2019	In Force	Method And Apparatus For Compilation Of Finite Automata
MP1248	MP1248	US	11/775,172	7/9/2007	8046775	10/25/2011	In Force	EVENT-BASED BANDWIDTH ALLOCATION MODE SWITCHING METHO
MP1248	MP1248C1	US	13/279,163	10/21/2011	8424021	4/16/2013	In Force	EVENT-BASED BANDWIDTH ALLOCATION MODE SWITCHING METHO
MP1248	MP1248C1C1	US	13/863,163	4/15/2013	8799929	8/5/2014	In Force	METHOD AND APPARATUS FOR BANDWIDTH ALLOCATION MODE SW
MP12480	MP12480	US	14/015,607	8/30/2013	9335784	5/10/2016	In Force	Clock Distribution Circuit With Distributed Delay Locked Loop
MP12480	MP12480CN	CN	201410438613.:	8/29/2014	ZL2014104386	1/23/2018	In Force	Distributed Delay Locked Loop
MP12480	MP12480HK	HK	15107380.6	8/29/2014			Pending	Distributed Delay Locked Loop
MP12481	MP12481	US	14/142,623	12/27/2013	9509585	11/29/2016	In Force	APPARATUS AND METHOD FOR TIME STAMPING PACKETS ACROSS S
MP12482	MP12482	US	14/142,631	12/27/2013	9438539	9/6/2016	In Force	APPARATUS AND METHOD FOR OPTIMIZING THE NUMBER OF ACCES
MP12483	MP12483	US	14/142,643	12/27/2013	9009364	4/14/2015	In Force	APPARATUS AND METHOD FOR ACCELERATED PAGE LINK LIST PROCE
MP12483	MP12483C1	US	14/676,791	4/1/2015	9262369	2/16/2016	In Force	APPARATUS AND METHOD FOR ACCELERATED PAGE LINK LIST PROCE
MP12484	MP12484	US	14/142,653	12/27/2013	9736069	8/15/2017	In Force	Method for Storing and Retrieving Packets in High Bandwidth and Lo
MP12485	MP12485	US	14/034,200	9/23/2013	9281953	3/8/2016	In Force	SYSTEMS AND METHODS FOR ROUTING MULTICAST PACKETS
MP12486	MP12486	US	14/034,006	9/23/2013	9256491	2/9/2016	In Force	METHOD AND SYSTEM FOR DATA INTEGRITY
MP12487	MP12487	US	14/036,709	9/25/2013	9568542	2/14/2017	In Force	Memory Interface With Integrated Tester

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MP12488	MP12488TW	TW	103132756	9/23/2014			Pending	SEMICONDUCTOR WITH VIRTUALIZED COMPUTATION AND SWITCH
MP12488	MP12488WO	WO	PCT/US2014/05	9/17/2014	DNA		Pending	SEMICONDUCTOR WITH VIRTUALIZED COMPUTATION AND SWITCH
MP12489	MP12489	US	14/038,156	9/26/2013	9639476	5/2/2017	In Force	Merged TLB Structure For Multiple Sequential Address Translations
MP12489	MP12489CN	CN	2014104984014	9/25/2014	ZL2014104984	2/6/2018	In Force	Merged TLB Structure For Multiple Sequential Address Translations
MP12489	MP12489HK	HK	15107954.2	8/18/2015			Pending	Merged TLB Structure For Multiple Sequential Address Translations
MP12489	MP12489KR	KR	1020140129272	9/26/2014		1/14/2016	In Force	Merged TLB Structure For Multiple Sequential Address Translations
MP1248W	MP1248WO	WO	PCT/US2007/01	8/14/2007	DNA		Pending	Event-Based Bandwidth Allocation Mode Switching Method and Ap
MP1249	MP1249	US	11/836,044	8/8/2007	8032737	10/4/2011	In Force	METHODS AND APPARATUS FOR HANDLING SWITCHING AMONG TI
MP1249	MP1249C1	US	13/247,030	9/28/2011	8478972	7/2/2013	In Force	METHODS AND APPARATUS FOR HANDLING SWITCHING AMONG TI
MP12490	MP12490	US	14/038,383	9/26/2013	9208103	12/8/2015	In Force	Translation Bypass In Multi-Stage Address Translation
MP12490	MP12490CN	CN	201410498013.(	9/25/2014	ZL2014104980	6/8/2018	In Force	Translation Bypass In Multi-Stage Address Translation
MP12490	MP12490HK	HK	15109895.0	9/25/2014		8/2/2019	In Force	Translation Bypass In Multi-Stage Address Translation
MP12490	MP12490KR	KR	10-2014-012926	9/26/2014		2/23/2016	In Force	Translation Bypass In Multi-Stage Address Translation
MP12491	MP12491	US	14/038,189	9/26/2013	9645941	5/9/2017	In Force	Collapsed Address Translation With Multiple Page Sizes
MP12491	MP12491C1	US	15/475,718	3/31/2017	10042778	8/7/2018	In Force	Collapsed Address Translation With Multiple Page Sizes
MP12491	MP12491DE	DE	102014014 076.	9/25/2014			Pending	Collapsed Address Translation With Multiple Page Sizes
MP12492	MP12492	US	14/038,225	9/26/2013	9268694	2/23/2016	In Force	Maintenance Of Cache And Tags In A Translation Lookaside Buffer
MP12494	MP12494	US	14/040,431	9/27/2013	9507369	11/29/2016	In Force	Dynamically Adjusting Supply Voltage Based On Monitored Chip Tem
MP12494	MP12494CN	CN	201410503442.(	9/26/2014			Pending	Dynamically Adjusting Supply Voltage Based On Monitored Chip Tem
MP12494	MP12494CND1	CN	201810444294.(	9/26/2014			Pending	Dynamically Adjusting Supply Voltage Based On Monitored Chip Tem
MP12494	MP12494DE	DE	102014014 494.	9/25/2014			Pending	Dynamically Adjusting Supply Voltage Based On Monitored Chip Tem
MP12494	MP12494HK	HK	15107953.3	9/26/2014			Pending	Dynamically Adjusting Supply Voltage Based On Monitored Chip Tem
MP12494	MP12494JP	JP	2014-192983	9/22/2014	JP6002728	9/9/2016	In Force	DYNAMIC ADJUSTMENT OF SUPPLY VOLTAGE BASED ON MONITORE
MP12495	MP12495	US	14/040,577	9/27/2013	9413568	8/9/2016	In Force	Method And Apparatus For Calibrating An Input Interface
MP12496	MP12496	US	14/040,563	9/27/2013	9087567	7/21/2015	In Force	Method And Apparatus For Amplifier Offset Calibration
MP12497	MP12497	US	14/040,532	9/27/2013	9496012	11/15/2016	In Force	Method and Apparatus For Reference Voltage Calibration In A Single-
MP12498	MP12498	US	14/039,846	9/27/2013	9490033	11/8/2016	In Force	Auto-Blow Memory Repair
MP12499	MP12499	US	14/041,961	9/30/2013	9041432	5/26/2015	In Force	Clock Multiplexing And Repeater Network
MP1249E	MP1249EP	EP	07016002.3	8/14/2007	1890225	4/4/2012	In Force	METHODS AND APPARATUS FOR HANDLING SWITCHING AMONG TH
MP1249E	MP1249EPDE	DE	07016002.3	8/14/2007		4/4/2012	In Force	METHOD, APPARATUS AND COMPUTER PROGRAM PRODUCT FOR H
MP1249E	MP1249EPFR	FR	07016002.3	8/14/2007	1890225	4/4/2012	In Force	METHOD, APPARATUS AND COMPUTER PROGRAM PRODUCT FOR H
MP1249E	MP1249EPGB	GB	07016002.3	8/14/2007	1890225	4/4/2012	In Force	METHOD, APPARATUS AND COMPUTER PROGRAM PRODUCT FOR H
MP1249H	MP1249HK	HK	HK08108651.5	8/5/2008		12/28/2012	In Force	METHODS AND APPARATUS FOR HANDLING SWITCHING AMONG TH
MP1249W	MP1249WO	WO	PCT/US2007/01	8/14/2007	DNA		Pending	Methods and Apparatus for Handling Switching Among Threads Withi

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MP12500	MP12500	US	14/042,111	9/30/2013	9264383	2/16/2016	In Force	SYSTEMS AND METHODS FOR QUALITY OF SERVICE FOR LINK AGGRE
MP12501	MP12501	US	14/042,403	9/30/2013	10110393	10/23/2018	In Force	Protocol Switching over Multi-Network Interface
MP12502	MP12502	US	14/045,674	10/3/2013	9390023	7/12/2016	In Force	Method And Apparatus For Conditional Storing Of Data Using A Cor
MP12502	MP12502KR	KR	10-2014-013448	10/6/2014	101587362	1/14/2016	In Force	Method And Apparatus For Conditional Storing Of Data Using A Cor
MP12503	MP12503	US	14/045,602	10/3/2013	9501243	11/22/2016	In Force	Method And Apparatus For Supporting Wide Operations Using Ator
MP12503	MP12503CN	CN	201410521310.!	9/30/2014	ZL2014105213	3/23/2018	In Force	Method And Apparatus For Supporting Wide Operations Using Ator
MP12503	MP12503HK	HK	15107951.5	9/30/2014	1207437	7/5/2019	In Force	Method And Apparatus For Supporting Wide Operations Using Ator
MP12503	MP12503TW	TW	103133879	9/30/2014	I670648	9/1/2019	In Force	Method And Apparatus For Supporting Wide Operations Using Ator
MP12504	MP12504	US	14/045,644	10/3/2013	9281034	3/8/2016	In Force	Data Strobe Generation
MP12505	MP12505	US	14/046,879	10/4/2013	9721627	8/1/2017	In Force	Method and Apparatus for Aligning Signals
MP12506	MP12506	US	14/048,849	10/8/2013	9170880	10/27/2015	In Force	METHOD AND SYSTEM FOR DATA INTEGRITY IN TCAMS
MP12507	MP12507	US	14/051,290	10/10/2013	9253120	2/2/2016	In Force	SYSTEMS AND METHODS FOR HIGH SPEED DATA PROCESSING AT A
MP12508	MP12508	US	14/067,815	10/30/2013	9465406	10/11/2016	In Force	TIMERS AND METHODS THEREOF FOR COMPUTING DEVICES
MP12509	MP12509	US	14/080,389	11/14/2013	9323715	4/26/2016	In Force	Method And Apparatus To Represent A Processor Context With Fewe
MP12509	MP12509CN	CN	2014106415662	11/13/2014	ZL2014106415	5/4/2018	In Force	Method And Apparatus To Represent A Processor Context With Fewe
MP12509	MP12509HK	HK	15107950.6	11/13/2014	1207436	7/12/2019	In Force	Method And Apparatus To Represent A Processor Context With Fewe
MP12509	MP12509KR	KR	1020140157977	11/13/2014	101694591000	1/3/2017	In Force	Method And Apparatus To Represent A Processor Context With Fewe
MP12509	MP12509TW	TW	103138879	11/10/2014	I541661	7/11/2016	In Force	Method And Apparatus To Represent A Processor Context With Fewe
MP1251	MP1251	US	11/893,163	8/14/2007	7751139	7/6/2010	In Force	CIRCUITS, SYSTEMS, AND METHODS FOR CALIBRATION OF BIAS ACR
MP12510	MP12510	US	14/083,894	11/19/2013	9363193	6/7/2016	In Force	VIRTUALIZED NETWORK INTERFACE FOR TCP REASSEMBLY BUFFER A
MP12510	MP12510DE	DE	102014012618.!	8/22/2014			Pending	Virtualized network interface for TCP recomposition buffer allocation
MP12511	MP12511	US	14/088,082	11/22/2013	9223518	12/29/2015	In Force	METHOD AND SYSTEM FOR REMOTE CACHE DIRECT DATA PLACEME
MP12512	MP12512	US	14/151,748	1/9/2014	9058463	6/16/2015	In Force	SYSTEMS AND METHODS FOR SPECIFYING, MODELING, IMPLEMENTI
MP12512	MP12512TW	TW	103141564	12/1/2014	I634445	9/1/2018	In Force	Systems and methods for specifying, modeling, implementing and ve
MP12513	MP12513	US	14/106,841	12/15/2013	9313029	4/12/2016	In Force	VIRTUALIZED NETWORK INTERFACE FOR REMOTE DIRECT MEMORY
MP12514	MP12514	US	14/106,827	12/15/2013			Pending	VIRTUALIZED NETWORK INTERFACE FOR LOCKDOWN AND OVERLAY
MP12515	MP12515	US	14/140,503	12/25/2013	9379992	6/28/2016	In Force	METHOD AND AN APPARATUS FOR VIRTUALIZATION OF A QUALITY-
MP12515	MP12515CN	CN	2014107420206	12/5/2014	ZL2014107420	7/28/2017	In Force	Method and an apparatus for virtualization of quality-of-service
MP12515	MP12515HK	HK	15108376	12/5/2014	1207719B	4/20/2018	In Force	A METHOD AND AN APPARATUS FOR VIRTUALIZATION OF A QUALITY
MP12515	MP12515KR	KR	1020140164241	11/24/2014	101698648000	1/16/2017	In Force	A METHOD AND AN APPARATUS FOR VIRTUALIZATION OF QUALITY-
MP12515	MP12515TW	TW	103137163	10/28/2014	I571076	2/11/2017	In Force	A METHOD AND AN APPARATUS FOR VIRTUALIZATION OF A QUALITY
MP12516	MP12516	US	14/140,488	12/25/2013	9306916	4/5/2016	In Force	SYSTEM AND A METHOD FOR A REMOTE DIRECT MEMORY ACCESS O
MP12517	MP12517	US	14/140,494	12/25/2013	9977737	5/22/2018	In Force	METHOD AND AN APPARATUS FOR MEMORY ADDRESS ALLIGNMENT
MP12517	MP12517CN	CN	201410756095X	12/10/2014	ZL2014107560	8/2/2019	In Force	Method and apparatus for memory address allignment

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MP12517	MP12517HK	HK	15109030.6	12/10/2014			Pending	A METHOD AND AN APPARATUS FOR MEMORY ADDRESS ALLIGNME
MP12517	MP12517TW	TW	103137161	10/28/2014	I627529	6/21/2018	In Force	A METHOD AND AN APPARATUS FOR MEMORY ADDRESS ALLIGNMI
MP12518	MP12518	US	14/140,585	12/26/2013	9665508	5/30/2017	In Force	METHOD AND AN APPARATUS FOR CONVERTING INTERRUPTS INTO
MP12519	MP12519	US	14/141,076	12/26/2013	9390209	7/12/2016	In Force	SYSTEM FOR AND METHOD OF COMBINING CMOS INVERTERS OF M
MP1252	MP1252	US	11/893,162	8/14/2007	7848043	12/7/2010	In Force	CIRCUITS, SYSTEMS, AND METHODS FOR LOW NOISE BIASING OF M
MP12520	MP12520	US	14/141,096	12/26/2013	9443053	9/13/2016	In Force	SYSTEM FOR AND METHOD OF PLACING CLOCK STATIONS USING VA
MP12521	MP12521	US	14/141,104	12/26/2013	9305129	4/5/2016	In Force	SYSTEM FOR AND METHOD OF TUNING CLOCK NETWORKS CONSTR
MP12521	MP12521HK	HK	15107952.4	8/18/2015			Pending	SYSTEM AND METHOD FOR TUNING CLOCK NETWORKS
MP12521	MP12521JP	JP	2014-263371	12/25/2014	JP6544923	6/28/2019	In Force	SYSTEM AND METHOD OF TUNING CLOCK NETWORKS CONSTRUCTI
MP12521	MP12521KR	KR	1020140191072	12/26/2014			Pending	SYSTEM FOR AND METHOD OF TUNING CLOCK NETWORKS CONSTR
MP12521	MP12521TW	TW	103145439	12/25/2014	I661325	6/1/2019	In Force	SYSTEM FOR AND METHOD OF TUNING CLOCK NETWORKS CONSTR
MP12522	MP12522	US	14/142,511	12/27/2013	9620213	4/11/2017	In Force	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS
MP12522	MP12522CN	CN	201410838433.4	12/29/2014	ZL2014108384	8/9/2019	In Force	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS
MP12522	MP12522D1	US	15/446,259	3/1/2017	9952799	4/24/2018	In Force	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522D2	US	15/446,297	3/1/2017	9952800	4/24/2018	In Force	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522D2C1	US	15/923,851	3/16/2018			Pending	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522HK	HK	15111512.9	12/29/2014			Pending	A MATRIX OF ON-CHIP ROUTERS INTERCONNECTING A PLURALITY OF
MP12522	MP12522JP	JP	2014-263373	12/25/2014			Pending	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522KR	KR	1020140191978	12/29/2014			Pending	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12522	MP12522TW	TW	103145450	12/25/2014	I659303	5/11/2019	In Force	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12523	MP12523	US	14/142,497	12/27/2013	9548945	1/17/2017	In Force	MATRIX OF ON-CHIP ROUTERS INTERCONNECTING A PLURALITY OF P
MP12523	MP12523CN	CN	201410838063.4	12/29/2014	ZL2014083806	8/9/2019	In Force	Matrix of on-chip routers interconnecting a plurality of processing en
MP12523	MP12523HK	HK	15111519.2	12/29/2014			Pending	METHOD AND SYSTEM FOR RECONFIGURABLE PARALLEL LOOKUPS U
MP12523	MP12523JP	JP	2014-263372	12/25/2014	JP6556450	7/19/2019	In Force	A MATRIX OF ON-CHIP ROUTERS INTERCONNECTING A PLURALITY OF
MP12523	MP12523KR	KR	1020140192064	12/29/2014			Pending	A MATRIX OF ON-CHIP ROUTERS INTERCONNECTING A PLURALITY OF
MP12523	MP12523TW	TW	103145445	12/25/2014	I661702	6/1/2019	In Force	A MATRIX OF ON-CHIP ROUTERS INTERCONNECTING A PLURALITY OF
MP12524	MP12524	US	14/141,987	12/27/2013	9491099	11/8/2016	In Force	LOOK-ASIDE PROCESSOR UNIT WITH INTERNAL AND EXTERNAL ACCE
MP12524	MP12524CN	CN	201410756800.6	12/10/2014	ZL2014107568	11/23/2018	In Force	LOOK-ASIDE PROCESSOR UNIT WITH INTERNAL AND EXTERNAL ACCE
MP12524	MP12524HK	HK	15108374.2	12/10/2014			Pending	A LOOK-ASIDE PROCESSOR UNIT WITH INTERNAL AND EXTERNAL AC
MP12524	MP12524KR	KR	1020140160344	11/17/2014			Pending	A LOOK-ASIDE PROCESSOR UNIT WITH INTERNAL AND EXTERNAL AC
MP12524	MP12524TW	TW	103137160	10/28/2014	I652623	3/1/2019	In Force	A LOOK-ASIDE PROCESSOR UNIT WITH INTERNAL AND EXTERNAL AC
MP12525	MP12525	US	14/141,681	12/27/2013	9460033	10/4/2016	In Force	Apparatus and Method for Interrupt Collecting and Reporting Status
MP12526	MP12526	US	14/143,586	12/30/2013	9419943	8/16/2016	In Force	Method And Apparatus For Processing Of Finite Automata
MP12526	MP12526CN	CN	201410432859X	8/28/2014	ZL2014104328	6/5/2018	In Force	Method And Apparatus For Processing Of Finite Automata

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MP12526	MP12526HK	HK	15112773.1	8/28/2014	1212119	8/2/2019	In Force	Method And Apparatus For Processing Of Finite Automata
MP12527	MP12527	US	14/144,260	12/30/2013	9880844	1/30/2018	In Force	METHOD AND APPARATUS FOR PARALLEL AND CONDITIONAL DATA
MP12527	MP12527CN	CN	2014108431994	12/30/2014	ZL2014108431	5/28/2019	In Force	Method and apparatus for parallel and conditional data manipulati
MP12527	MP12527HK	HK	15109646.2	12/30/2014			Pending	METHOD AND APPARATUS FOR PARALLEL AND CONDITIONAL DATA
MP12527	MP12527JP	JP	2014-267000	12/29/2014	6537823	6/14/2019	In Force	METHOD AND APPARATUS FOR PARALLEL AND CONDITIONAL DATA
MP12527	MP12527KR	KR	1020140194027	12/30/2014			Pending	METHOD AND APPARATUS FOR PARALLEL AND CONDITIONAL DATA
MP12528	MP12528	US	14/144,270	12/30/2013	9379963	6/28/2016	In Force	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKIN
MP12528	MP12528CN	CN	201410843934.:	12/30/2014			Pending	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKIN
MP12528	MP12528D1	US	15/167,704	5/27/2016	10009273	6/26/2018	In Force	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKIN
MP12528	MP12528D1D1	US	15/991,810	5/29/2018			Pending	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKIN
MP12528	MP12528HK	HK	15111520.9	12/30/2014			Pending	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKIN
MP12528	MP12528JP	JP	2014-267001	12/29/2014	6537824	6/14/2019	In Force	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKIN
MP12528	MP12528KR	KR	1020140194209	12/30/2014			Pending	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKIN
MP12528	MP12528TW	TW	103145662	12/26/2014	I661695	6/1/2019	In Force	APPARATUS AND METHOD OF GENERATING LOOKUPS AND MAKING
MP12529	MP12529	US	14/145,374	12/31/2013	9275336	3/1/2016	In Force	METHOD AND SYSTEM FOR SKIPPING OVER GROUP(S) OF RULES BAS
MP1253	MP1253	US	11/840,745	8/17/2007	7852168	12/14/2010	In Force	POWER-EFFICIENT BIASING CIRCUIT
MP12530	MP12530	US	14/145,918	12/31/2013	9544402	1/10/2017	In Force	MULTI-RULE APPROACH TO ENCODING A GROUP OF RULES
MP12534	MP12534	US	14/150,622	1/8/2014	10447823	10/15/2019	In Force	PACKET PARSING ENGINE
MP12534	MP12534WO	WO	PCT/US2015/10	1/6/2015			Pending	PACKET PARSING ENGINE
MP12535	MP12535	US	14/150,650	1/8/2014	9307057	4/5/2016	In Force	METHODS AND SYSTEMS FOR RESOURCE MANAGEMENT IN A SINGLE
MP12536	MP12536	US	14/150,550	1/8/2014	9268855	2/23/2016	In Force	PROCESSING REQUEST KEYS BASED ON A KEY SIZE SUPPORTED BY U
MP12537	MP12537	US	14/150,572	1/8/2014	9432284	8/30/2016	In Force	METHOD AND APPARATUS FOR COMPILING SEARCH TREES FOR PRO
MP12538	MP12538	US	14/150,761	1/8/2014	9667446	5/30/2017	In Force	CONDITION CODE APPROACH FOR COMPARING RULE AND PACKET D
MP12539	MP12539TW	TW	104100216	1/6/2015	I593256	7/21/2017	In Force	METHODS AND SYSTEMS FOR FLEXIBLE PACKET CLASSIFICATION
MP12539	MP12539WO	WO	PCT/US2015/01	1/6/2015			Pending	METHODS AND SYSTEMS FOR FLEXIBLE PACKET CLASSIFICATION
MP1254	MP1254C1	US	13/034,613	2/24/2011	8232841	7/31/2012	In Force	Amplifier With Output Filtering
MP12540	MP12540	US	14/150,664	1/8/2014	9513926	12/6/2016	In Force	FLOATING MASK GENERATION FOR NETWORK PACKET FLOW
MP12541	MP12541	US	14/150,635	1/8/2014	10284690	5/7/2019	In Force	METHODS AND SYSTEMS FOR DISTRIBUTION OF PACKETS AMONG P
MP12543	MP12543	US	14/152,817	1/10/2014	9647947	5/9/2017	In Force	BLOCK MASK REGISTER KEY PROCESSING BY COMPILING DATA STRU
MP12544	MP12544	US	14/165,100	1/27/2014	9454305	9/27/2016	In Force	METHOD AND SYSTEM FOR MANAGING STORAGE RESERVATION
MP12545	MP12545	US	14/166,082	1/28/2014	9378036	6/28/2016	In Force	METHOD AND SYSTEM FOR COMMUNICATION IN A VIRTUAL MACHI
MP12546	MP12546	US	14/169,830	1/31/2014	9602532	3/21/2017	In Force	Method And Apparatus For Optimizing Finite Automata Processing
MP12546	MP12546KR	KR	10-2014-011384	8/29/2014	101633649	6/21/2016	In Force	Method And Apparatus For Optimizing Finite Automata Processing
MP12547	MP12547	US	14/169,967	1/31/2014	9904630	2/27/2018	In Force	Finite Automata Processing Based On A Top Of Stack (TOS) Memory

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MP12547	MP12547CN	CN	2014104332877	8/28/2014	ZL2014104332	9/25/2018	In Force	Finite Automata Processing Based On A Top Of Stack (TOS) Memory
MP12547	MP12547HK	HK	16101361.1	8/28/2014			Pending	Finite Automata Processing Based On A Top Of Stack (TOS) Memory
MP12548	MP12548	US	14/170,955	2/3/2014			Pending	METHOD AND AN APPARATUS FOR WORK PACKET QUEUING, SCHE
MP12548	MP12548WO	WO	PCT/US2015/14	2/2/2015			Pending	METHOD AND AN APPARATUS FOR WORK PACKET QUEUING, SCHE
MP12549	MP12549	US	14/171,108	2/3/2014	9838471	12/5/2017	In Force	METHOD AND AN APPARATUS FOR WORK REQUEST ARBITRATION I
MP12549	MP12549WO	WO	PCT/US2015/01	2/2/2015			Pending	METHOD AND AN APPARATUS FOR WORK REQUEST ARBITRATION I
MP12550	MP12550	US	14/171,290	2/3/2014	9811467	11/7/2017	In Force	METHOD AND AN APPARATUS FOR PRE-FETCHING AND PROCESSIN
MP12550	MP12550WO	WO	PCT/US2015/01	2/2/2015			Pending	A METHOD AND AN APPARATUS FOR PRE-FETCHING AND PROCESSI
MP12550	MP12550WOCN	CN	201580003167.(	2/2/2015			Pending	A method and an apparatus for pre-fetching and processing work fo
MP12551	MP12551	US	14/186,743	2/21/2014	9692715	6/27/2017	In Force	Multiple Ethernet Ports and Port Types Using a Shared Data Path
MP12551	MP12551C1	US	15/601,493	5/22/2017	10404623	9/3/2019	In Force	Multiple Ethernet Ports And Port Types Using A Shared Data Path
MP12551	MP12551CN	CN	201510072689.:	2/11/2015	104869176B	4/12/2019	In Force	Multiple Ethernet Ports and Port Types Using a Shared Data Path
MP12551	MP12551HK	HK	15109893.2	2/11/2015			Pending	Multiple Ethernet Ports and Port Types Using a Shared Data Path
MP12551	MP12551KR	KR	10-2015-002345	2/16/2015	101664658	10/4/2016	In Force	Multiple Ethernet Ports and Port Types Using a Shared Data Path
MP12551	MP12551TW	TW	104103687	2/4/2015	I551101	9/21/2016	In Force	AN INTERFACE UNIT BASED ON MULTIPLE ETHERNET PORTS AND PO
MP12552	MP12552	US	14/630,554	2/24/2015	9729320	8/8/2017	In Force	APPARATUS AND METHOD FOR SOFTWARE ENABLED ACCESS TO PR
MP12552	MP12552CN	CN	201510172120.4	2/25/2015			Pending	APPARATUS AND METHOD FOR SOFTWARE ENABLED ACCESS TO PR
MP12552	MP12552HK	HK	15111508.5	2/25/2015			Pending	APPARATUS AND METHOD FOR SOFTWARE ENABLED ACCESS TO PR
MP12552	MP12552TW	TW	104105809	2/24/2015	I633458	8/21/2018	In Force	APPARATUS AND METHOD FOR SOFTWARE ENABLED ACCESS TO PR
MP12553	MP12553	US	14/191,163	2/26/2014	9431105	8/30/2016	In Force	Method And Apparatus For Memory Access Management
MP12554	MP12554	US	14/192,100	2/27/2014	9490968	11/8/2016	In Force	CDR Voter With Improved Frequency Offset Tolerance
MP12555	MP12555	US	14/194,049	2/28/2014	9432288	8/30/2016	In Force	System On Chip Link Layer Protocol
MP12556	MP12556	US	14/193,793	2/28/2014	9471416	10/18/2016	In Force	Partitioned Error Code Computation
MP12557	MP12557	US	14/193,933	2/28/2014	9397938	7/19/2016	In Force	Packet Scheduling In A Network Processor
MP12557	MP12557TW	TW	104105253	2/16/2015	I559706	11/21/2016	In Force	Packet Scheduling In A Network Processor
MP12557	MP12557WO	WO	PCT/US2014/07	12/30/2014			Pending	Packet Scheduling In A Network Processor
MP12557	MP12557WOCN	CN	201480076494.(	12/30/2014	ZL2014800764	4/20/2018	In Force	Packet Scheduling In A Network Processor
MP12558	MP12558	US	14/193,895	2/28/2014	9680742	6/13/2017	In Force	Packet Output Processing
MP12558	MP12558TW	TW	104104215	2/9/2015	I566551	1/11/2017	In Force	Packet Output Processing
MP1256	MP1256	US	11/857,081	9/18/2007	9516178	12/6/2016	In Force	Multiple Media Format Support For Printers
MP12560	MP12560	US	14/193,899	2/28/2014	9483100	11/1/2016	In Force	Method And Apparatus For Power Gating Hardware Components In
MP12560	MP12560CN	CN	201510064815.(	2/6/2015	ZL2015100648	5/4/2018	In Force	Method And Apparatus For Power Gating Hardware Components In
MP12560	MP12560HK	HK	15111507.6	6/2/2015	1210879	7/12/2019	In Force	Method And Apparatus For Power Gating Hardware Components In
MP12560	MP12560TW	TW	104105811	2/24/2015	I618355	3/11/2018	In Force	Method And Apparatus For Power Gating Hardware Components In

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MP12561	MP12561	US	14/194,038	2/28/2014	9559982	1/31/2017	In Force	Packet Shaping In A Network Processor
MP12561	MP12561TW	TW	104104959	2/13/2015	I668975	8/11/2019	In Force	Circuit and method for packet shaping in a network processor
MP12561	MP12561WO	WO	PCT/US2014/07	12/30/2014			Pending	Packet Shaping In A Network Processor
MP12561	MP12561WOCN	CN	201480076493.3	12/30/2014	ZL2014800764	7/13/2018	In Force	Packet Shaping In A Network Processor
MP12562	MP12562	US	14/193,658	2/28/2014	9588920	3/7/2017	In Force	METHOD AND SYSTEM FOR PORT TRUNKING
MP12564	MP12564	US	14/201,594	3/7/2014	9529532	12/27/2016	In Force	Method And Apparatus For Memory Allocation In A Multi-Node Sys
MP12564	MP12564HK	HK	17104455.1	12/30/2014			Pending	Method And Apparatus For Memory Allocation In A Multi-Node Sys
MP12564	MP12564TW	TW	104104217	2/9/2015	I519958	2/1/2016	In Force	Method And Apparatus For Memory Allocation In A Multi-Node Sys
MP12564	MP12564WO	WO	PCT/US2014/07	12/30/2014			Pending	Method And Apparatus For Memory Allocation In A Multi-Node Sys
MP12564	MP12564WOCN	CN	201480076878.:	12/30/2014	ZL2014800768	3/1/2019	In Force	Method and apparatus for memory allocation in multi-node system
MP12565	MP12565	US	14/201,513	3/7/2014	9372800	6/21/2016	In Force	Inter-Chip Interconnect Protocol For A Multi-Chip System
MP12565	MP12565TW	TW	104107158	3/6/2015	I541649	7/11/2016	In Force	System and method of inter-chip interconnect protocol for a multi-c
MP12566	MP12566	US	14/201,557	3/7/2014			Pending	Method And System For Ordering I/O Access In A Multi-Node Envirc
MP12566	MP12566TW	TW	104105256	2/16/2015	I547870	9/1/2016	In Force	Method And System For Ordering I/O Access In A Multi-Node Environ
MP12567	MP12567	US	14/201,541	3/7/2014	9411644	8/9/2016	In Force	Method And System For Work Scheduling In A Multi-Chip System
MP12567	MP12567D1	US	15/200,587	7/1/2016	10169080	1/1/2019	In Force	Method For Work Scheduling In A Multi-Chip System
MP12567	MP12567TW	TW	104105255	2/16/2015	I543073	7/21/2016	In Force	Method And System For Work Scheduling In A Multi-Chip System
MP12569	MP12569	US	14/230,768	3/31/2014	9384008	7/5/2016	In Force	METHOD AND SYSTEM FOR OPERATING SYSTEM RECOVERY FROM A
MP1256W	MP1256WO	WO	PCT/IB2007/002	9/19/2007			Pending	Multiple Media Format Support For Printers
MP1257	MP1257	US	11/775,605	7/10/2007	7974351	7/5/2011	In Force	METHOD FOR DETECTING A PERIODIC SIGNAL
MP1257	MP1257C1	US	13/175,476	7/1/2011	8208571	6/26/2012	In Force	METHOD FOR DETECTING A PERIODIC SIGNAL
MP12571	MP12571	US	14/252,390	4/14/2014	9438561	9/6/2016	In Force	Processing Of Finite Automata Based On A Node Cache
MP12572	MP12572	US	14/252,299	4/14/2014	10110558	10/23/2018	In Force	Processing Of Finite Automata Based On Memory Hierarchy
MP12573	MP12573	US	14/252,354	4/14/2014	9823895	11/21/2017	In Force	Memory Management For Finite Automata Processing
MP12574	MP12574	US	14/252,293	4/14/2014	10002326	6/19/2018	In Force	Compilation Of Finite Automata Based On Memory Hierarchy
MP12574	MP12574CN	CN	2014104334020	8/28/2014	ZL2014104334	11/13/2018	In Force	Compilation Of Finite Automata Based On Memory Hierarchy
MP12574	MP12574HK	HK	16102603.7	8/28/2014			Pending	Compilation Of Finite Automata Based On Memory Hierarchy
MP12575	MP12575	US	14/288,121	5/27/2014	9355206	5/31/2016	In Force	SYSTEM AND METHOD FOR AUTOMATED FUNCTIONAL COVERAGE G
MP12575	MP12575TW	TW	104108422	3/17/2015	I627547	6/21/2018	In Force	System and method for automated functional coverage generation a
MP12576	MP12576	US	14/264,927	4/29/2014	9483290	11/1/2016	In Force	METHOD AND SYSTEM FOR VIRTUAL MACHINE COMMUNICATION
MP12577	MP12577	US	14/264,957	4/29/2014	9229893	1/5/2016	In Force	SYSTEMS AND METHODS FOR MANAGING DIRECT MEMORY ACCESS
MP12578	MP12578	US	14/300,552	6/10/2014	9294567	3/22/2016	In Force	SYSTEMS AND METHODS FOR ENABLING ACCESS TO EXTENSIBLE STO
MP12578	MP12578-4	US	14/496,916	9/25/2014	9819739	11/14/2017	In Force	SYSTEMS AND METHODS FOR SUPPORTING HOT PLUGGING OF REM
MP12578	MP12578-5	US	14/537,758	11/10/2014	9430268	8/30/2016	In Force	SYSTEMS AND METHODS FOR SUPPORTING MIGRATION OF VIRTUAL

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MP12578	MP12578I1	US	15/041,892	2/11/2016	9529773	12/27/2016	In Force	SYSTEMS AND METHODS FOR ENABLING ACCESS TO EXTENSIBLE RE
MP12578	MP12578TW	TW	104106790	3/4/2015	I621023	4/11/2018	In Force	SYSTEMS AND METHODS FOR SUPPORTING HOT PLUGGING OF REM
MP12578	MP12578TW-3	TW	104106793	3/4/2015	I637613	10/1/2018	In Force	SYSTEMS AND METHODS FOR ENABLING ACCESS TO EXTENSIBLE ST
MP12578	MP12578TW-5	TW	104111571	4/10/2015	I647573	1/11/2019	In Force	Systems and methods for supporting migration of virtual machines
MP12579	MP12579	US	14/289,533	5/28/2014			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS
MP12579	MP12579CN	CN	201510257702.:	5/19/2015			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS
MP12579	MP12579HK	HK	16108885.3	5/19/2015			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS
MP12579	MP12579IN	IN	835/DEL/2015	3/25/2015			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS
MP12579	MP12579JP	JP	2015-107430	5/27/2015			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS
MP12579	MP12579KR	KR	1020150073195	5/26/2015			Pending	METHOD AND APPARATUS FOR FLEXIBLE AND EFFICIENT ANALYTICS
MP12580	MP12580	US	14/289,548	5/28/2014	9773036	9/26/2017	In Force	METHOD AND APPARATUS FOR TABLE AGING IN A NETWORK SWITC
MP12580	MP12580CN	CN	2015102546804	5/18/2015			Pending	METHOD AND APPARATUS FOR TABLE AGING IN A NETWORK SWITC
MP12580	MP12580D1	US	15/675,336	8/11/2017	10216780	2/26/2019	In Force	METHOD AND APPARATUS FOR TABLE AGING IN A NETWORK SWITC
MP12580	MP12580HK	HK	16108887.1	5/18/2015			Pending	METHOD AND APPARATUS FOR TABLE AGING IN A NETWORK SWITC
MP12581	MP12581-4	US	14/723,858	5/28/2015	9571279	2/14/2017	In Force	SYSTEMS AND METHODS FOR SECURED BACKUP OF HARDWARE SEC
MP12581	MP12581TW-5	TW	104119522	6/17/2015	I632797	8/11/2018	In Force	SYSTEMS AND METHODS FOR SECURED BACKUP OF HARDWARE SEC
MP12582	MP12582	US	14/302,351	6/11/2014	9413357	8/9/2016	In Force	HIERARCHICAL STATISTICALLY MULTIPLEXED COUNTERS AND A MET
MP12582	MP12582CN	CN	2015103201878	6/11/2015			Pending	HIERARCHICAL STATISTICALLY MULTIPLEXED COUNTERS AND A MET
MP12582	MP12582D1	US	15/202,428	7/5/2016	10038448	7/31/2018	In Force	HIERARCHICAL STATISICALLY MULTIPLEXED COUNTERS AND A METH
MP12582	MP12582D2	US	16/019,780	6/27/2018			Pending	HIERARCHICAL STATISICALLY MULTIPLEXED COUNTERS AND A METH
MP12582	MP12582HK	HK	16108891.5	6/11/2015			Pending	HIERARCHICAL STATISTICALLY MULTIPLEXED COUNTERS AND A MET
MP12582	MP12582TW	TW	104112176	4/16/2015	I656476	4/11/2019	In Force	HIERARCHICAL STATISTICALLY MULTIPLEXED COUNTERS AND ARCHIT
MP12583	MP12583CN	CN	201510253417.:	5/18/2015			Pending	COUNTER WITH OVERFLOW FIFO AND A METHOD THEREOF
MP12583	MP12583HK	HK	16108886.2	5/18/2015			Pending	COUNTER WITH OVERFLOW FIFO AND A METHOD THEREOF
MP12584	MP12584	US	14/302,706	6/12/2014	9423980	8/23/2016	In Force	METHODS AND SYSTEMS FOR AUTOMATICALLY ADDING INTELLIGEN
MP12585	MP12585	US	14/309,659	6/19/2014	9635146	4/25/2017	In Force	METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLLA
MP12585	MP12585C1	US	15/457,970	3/13/2017			Pending	METHOD OF USING BIT VECTORS TO ALLOW EXPASION AND COLLAP
MP12585	MP12585CN	CN	2015102724093	5/25/2015			Pending	A METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLL
MP12585	MP12585EP	EP	15172929.0	6/19/2015	2958287	6/19/2019	In Force	A METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLL
MP12585	MP12585EPDE	DE	15172929.0	6/19/2015	602015030226	6/19/2019	In Force	A METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLL
MP12585	MP12585HK	HK	16108884.4	5/25/2015			Pending	A METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLL
MP12585	MP12585KR	KR	1020150086042	6/17/2015			Pending	METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLLA
MP12585	MP12585TW	TW	104111755	4/13/2015	I664843	7/1/2019	In Force	A METHOD OF USING BIT VECTORS TO ALLOW EXPANSION AND COLL
MP12586	MP12586	US	14/309,726	6/19/2014	9516145	12/6/2016	In Force	METHOD OF EXTRACTING DATA FROM PACKETS AND AN APPARATUS

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MP12586	MP12586CN	CN	201510236939.:	5/11/2015			Pending	A METHOD OF EXTRACTING DATA FROM PACKETS AND AN APPARAT
MP12586	MP12586HK	HK	16108889.9	5/11/2015			Pending	A METHOD OF EXTRACTING DATA FROM PACKETS AND AN APPARA"
MP12586	MP12586JP	JP	2015-122563	6/18/2015			Pending	METHOD OF EXTRACTING DATA FROM PACKETS, AND APPARATUS '
MP12586	MP12586KR	KR	1020150087785	6/19/2015			Pending	A METHOD OF EXTRACTING DATA FROM PACKETS AND AN APPARA
MP12587	MP12587	US	14/309,802	6/19/2014	9628385	4/18/2017	In Force	METHOD OF IDENTIFYING INTERNAL DESTINATIONS OF NETWORK F
MP12587	MP12587C1	US	15/457,959	3/13/2017	10397113	8/27/2019	In Force	METHOD OF IDENTIFYING INTERNAL DESTINATIONS OF NETWORK F
MP12587	MP12587CN	CN	2015102296103	5/7/2015			Pending	METHOD OF IDENTIFYING INTERNAL DESTINATIONS OF NETWORK F
MP12587	MP12587HK	HK	16108883.5	5/7/2015			Pending	A METHOD OF IDENTIFYING INTERNAL DESTINATIONS OF NETWORI
MP12587	MP12587TW	TW	104110829	4/2/2015	I660609	5/21/2019	In Force	A method of identifying internal destinations of network packets an
MP12588	MP12588	US	14/309,789	6/19/2014	9742694	8/22/2017	In Force	METHOD OF DYNAMICALLY RENUMBERING PORTS AND AN APPARA
MP12588	MP12588C1	US	15/653,021	7/18/2017			Pending	METHOD OF DYNAMICALLY RENUMBERING PORTS AND AN APPARA
MP12589	MP12589	US	14/309,773	6/19/2014	10050833	8/14/2018	In Force	METHOD OF REDUCING LATENCY IN A FLEXIBLE PARSER AND AN AP
MP12589	MP12589CN	CN	2015102297708	5/7/2015			Pending	METHOD OF REDUCING LATENCY IN A FLEXIBLE PARSER AND AN AP
MP12589	MP12589HK	HK	16108892.4	5/7/2015			Pending	A METHOD OF REDUCING LATENCY IN A FLEXIBLE PARSER AND AN A
MP12590	MP12590	US	14/309,763	6/19/2014			Pending	METHOD OF HANDLING LARGE PROTOCOL LAYERS FOR CONFIGURAB
MP12590	MP12590CN	CN	2015102297799	5/7/2015			Pending	METHOD OF HANDLING LARGE PROTOCOL LAYERS FOR CONFIGURAB
MP12590	MP12590HK	HK	16108890.6	5/7/2015			Pending	A METHOD OF HANDLING LARGE PROTOCOL LAYERS FOR CONFIGUR
MP12591	MP12591	US	14/309,739	6/19/2014	9438703	9/6/2016	In Force	METHOD OF FORMING A HASH INPUT FROM PACKET CONTENTS AN
MP12591	MP12591CN	CN	2015102367984	5/11/2015			Pending	A METHOD OF FORMING A HASH INPUT FROM PACKET CONTENTS A
MP12591	MP12591HK	HK	16108888.0	5/11/2015			Pending	A METHOD OF FORMING A HASH INPUT FROM PACKET CONTENTS A
MP12591	MP12591JP	JP	2015-122564	6/18/2015			Pending	METHOD OF FORMING HASH INPUT FROM PACKET CONTENTS, AND
MP12591	MP12591KR	KR	1020150087800	6/19/2015			Pending	METHOD OF FORMING A HASH INPUT FROM PACKET CONTENTS AN
MP12592	MP12592	US	14/309,679	6/19/2014	9531849	12/27/2016	In Force	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592CN	CN	2015102764264	5/26/2015			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592EP	EP	15172935.7	6/19/2015			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592HK	HK	16108882.6	7/26/2016			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592IN	IN	841/DEL/2015	3/26/2015			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592JP	JP	2015-122562	6/18/2015			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12592	MP12592KR	KR	1020150086078	6/17/2015			Pending	METHOD OF SPLITTING A PACKET INTO INDIVIDUAL LAYERS FOR MO
MP12593	MP12593	US	14/309,650	6/19/2014	9531848	12/27/2016	In Force	METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO ENA
MP12593	MP12593CN	CN	2015102726703	5/25/2015			Pending	A METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN
MP12593	MP12593EP	EP	15172917.5	6/19/2015	2958286	5/1/2019	In Force	A METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN
MP12593	MP12593EPDE	DE	15172917.5	6/19/2015	602015029172	5/1/2019	In Force	A METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN
MP12593	MP12593HK	HK	16108894.2	5/25/2015			Pending	A METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN

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MP12593	MP12593IN	IN	847/DEL/2015	3/26/2015			Pending	A METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN
MP12593	MP12593JP	JP	2015-122561	6/18/2015			Pending	METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN/
MP12593	MP12593KR	KR	1020150084526	6/15/2015			Pending	METHOD OF USING GENERIC MODIFICATION INSTRUCTIONS TO EN/
MP12594	MP12594	US	14/309,633	6/19/2014	9497294	11/15/2016	In Force	METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY STR
MP12594	MP12594CN	CN	201510272163.)	5/25/2015			Pending	A METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY ST
MP12594	MP12594EP	EP	15172911.8	6/19/2015	2958296	3/27/2019	In Force	A METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY ST
MP12594	MP12594EPDE	DE	15172911.8	6/19/2015	602015027021	3/27/2019	In Force	A METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY ST
MP12594	MP12594HK	HK	16104736.3	4/26/2016			Pending	A METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY ST
MP12594	MP12594IN	IN	836/DEL/2015	3/25/2015			Pending	A METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY ST
MP12594	MP12594JP	JP	2015-122560	6/18/2015			Pending	METHOD AND DEVICE FOR IDENTIFYING STRUCTURE OF PACKET BY
MP12594	MP12594KR	KR	1020150083631	6/12/2015			Pending	METHOD OF USING A UNIQUE PACKET IDENTIFIER TO IDENTIFY STR
MP12595	MP12595	US	14/309,619	6/19/2014	9473601	10/18/2016	In Force	METHOD OF REPRESENTING A GENERIC FORMAT HEADER USING CC
MP12595	MP12595CN	CN	2015102724360	5/25/2015			Pending	A METHOD OF REPRESENTING A GENERIC FORMAT HEADER USING
MP12595	MP12595HK	HK	16107161.0	5/25/2015			Pending	A METHOD OF REPRESENTING A GENERIC FORMAT HEADER USING C
MP12596	MP12596	US	14/309,603	6/19/2014	9961167	5/1/2018	In Force	METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR ENAB
MP12596	MP12596CN	CN	2015102765888	5/26/2015			Pending	Method of modifying packets to a generic format for enabling progra
MP12596	MP12596EP	EP	15172942.3	6/19/2015	2958288	5/1/2019	In Force	A METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR EN
MP12596	MP12596EPDE	DE	15172942.3	6/19/2015	602015029173	5/1/2019	In Force	A METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR EN
MP12596	MP12596HK	HK	16108893.3	5/26/2015			Pending	A METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR EN
MP12596	MP12596IN	IN	834/DEL/2015	3/25/2015			Pending	A METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR EN
MP12596	MP12596JP	JP	2015-122559	6/18/2015			Pending	Method of modifying packets to a generic format for enabling progra
MP12596	MP12596KR	KR	1020150084520	6/15/2015			Pending	METHOD OF MODIFYING PACKETS TO A GENERIC FORMAT FOR ENAB
MP12598	MP12598	US	14/312,130	6/23/2014	9436654	9/6/2016	In Force	METHODS AND SYSTEMS FOR PROCESSING TASK MANAGEMENT FUN
MP1260	MP1260	US	11/840,460	8/17/2007	7863842	1/4/2011	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP1260	MP1260C1	US	12/965,259	12/10/2010	8314581	11/20/2012	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP1260	MP1260C1C1	US	13/663,341	10/29/2012	8497647	7/30/2013	In Force	MOTOR SPINDLE CONTROL SYSTEM AND METHOD
MP12600	MP12600	US	14/325,841	7/8/2014	9785403	10/10/2017	In Force	Engine Architecture For Processing Finite Automata
MP12600	MP12600C1	US	15/703,638	9/13/2017	10466964	11/5/2019	In force	Engine Architecture For Processing Finite Automata
MP12600	MP12600CN	CN	2014104323187	8/28/2014	ZL2014104323	3/1/2019	In Force	Engine Architecture For Processing Finite Automata
MP12600	MP12600HK	HK	15107653.6	8/28/2014			Pending	Engine Architecture For Processing Finite Automata
MP12601	MP12601	US	14/663,912	3/20/2015	9692560	6/27/2017	In Force	METHODS AND SYSTEMS FOR RELIABLE NETWORK COMMUNICATIO
MP12602	MP12602	US	14/328,951	7/11/2014			Pending	MANAGING INSTRUCTION ORDER IN A PROCESSOR PIPELINE
MP12602	MP12602TW	TW	104114685	5/8/2015	I658407	5/1/2019	In Force	MANAGING INSTRUCTION ORDER IN A PROCESSOR PIPELINE
MP12603	MP12603	US	14/328,923	7/11/2014			Pending	MANAGING INSTRUCTION ORDER IN A PROCESSOR PIPELINE

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MP12603	MP12603TW	TW	104110835	4/2/2015	I659357	5/11/2019	In Force	MANAGING INSTRUCTION ORDER IN A PROCESSOR PIPELINE
MP12603	MP12603TWD1	TW	108111505	4/2/2015			Pending	MANAGING INSTRUCTION ORDER IN A PROCESSOR PIPELINE
MP12604	MP12604	US	14/331,105	7/14/2014	10198389	2/5/2019	In Force	BASEBOARD INTERCONNECTION DEVICE, SYSTEM AND METHOD
MP12606	MP12606	US	14/339,086	7/23/2014	9477424	10/25/2016	In Force	METHODS AND SYSTEMS FOR USING AN INTELLIGENT STORAGE AD.
MP12607	MP12607	US	14/724,273	5/28/2015	9819515	11/14/2017	In Force	INTEGRATED FABRIC ADAPTER AND ASSOCIATED METHODS THEREC
MP12609	MP12609	US	14/447,732	7/31/2014	9678779	6/13/2017	In Force	METHOD AND AN APPARATUS FOR CO-PROCESSOR DATA PLANE VI
MP1261	MP1261C1	US	12/827,517	6/30/2010	7986114	7/26/2011	In Force	Fast Startup Supply For Chip Initialization
MP12610	MP12610	US	14/464,535	8/20/2014	9477414	10/25/2016	In Force	METHODS AND SYSTEMS FOR IMPROVED CACHING WITH DATA REC
MP12611	MP12611	US	14/470,309	8/27/2014	9311021	4/12/2016	In Force	METHODS AND SYSTEMS FOR PERFORMING A READ AHEAD OPERA
MP12612	MP12612	US	14/470,337	8/27/2014	9330010	5/3/2016	In Force	METHODS AND SYSTEMS FOR IMPROVED DATA CACHING IN A VIRT
MP12613	MP12613	US	14/473,524	8/29/2014	9936003	4/3/2018	In Force	METHOD AND SYSTEM FOR TRANSMITTING INFORMATION IN A NE
MP12614	MP12614	US	14/038,549	9/26/2013	9671844	6/6/2017	In Force	Method And Apparatus For Managing Global Chip Power On A Mult
MP12614	MP12614C1	US	15/499,531	4/27/2017	10152102	12/11/2018	In Force	Method And Apparatus For Managing Global Chip Power On A Mult
MP12614	MP12614C1C1	US	16/210,987	12/5/2018			Pending	Method And Apparatus For Managing Global Chip Power On A Multic
MP12614	MP12614CN	CN	2014104983609	9/25/2014	ZL2014104983	7/21/2017	In Force	Method and apparatus for managing global chip power on multicore
MP12614	MP12614CND1	CN	2017105321604	9/25/2014			Pending	Method And Apparatus For Managing Global Chip Power On A Multic
MP12614	MP12614DE	DE	10 2014 014 301	9/25/2014	10 2014 014 3(	8/16/2018	In Force	Method And Apparatus For Managing Global Chip Power On A Multic
MP12614	MP12614HK	HK	15109894.1	9/25/2014	1209210B	5/4/2018	In Force	Method And Apparatus For Managing Global Chip Power On A Multic
MP12614	MP12614JP	JP	2014-192982	9/22/2014	6169547	7/7/2017	In Force	METHOD AND APPARATUS FOR MANAGING GLOBAL CHIP POWER O
MP12614	MP12614KR	KR	20140128554	9/25/2014	101701004000	1/23/2017	In Force	Method And Apparatus For Managing Global Chip Power On A Multic
MP12615	MP12615	US	14/494,187	9/23/2014	9729338	8/8/2017	In Force	FAST HARDWARE SWITCHOVER IN A CONTROL PATH IN A NETWORK
MP12615	MP12615C1	US	15/642,141	7/5/2017	10341130	7/2/2019	In Force	FAST HARDWARE SWITCHOVER IN A CONTROL PATH IN A NETWORK
MP12616	MP12616	US	14/494,291	9/23/2014	9565136	2/7/2017	In Force	MULTICAST REPLICATION ENGINE OF A NETWORK ASIC AND METHO
MP12617	MP12617	US	14/494,168	9/23/2014	9813327	7/11/2017	In Force	HIERARCHICAL HARDWARE LINKED LIST APPROACH FOR MULTICAST
MP12617	MP12617C1	US	15/725,643	10/5/2017	10205649	2/12/2019	In Force	HIERARCHICAL HARDWARE LINKED LIST APPROACH FOR MULTICAST
MP12617	MP12617C1D1	US	16/232,882	12/26/2018			Pending	HIERARCHICAL HARDWARE LINKED LIST APPROACH FOR MULTICAST
MP12618	MP12618	US	14/493,865	9/23/2014	9882678	1/30/2018	In Force	Method and Apparatus for Improving Data Integrity Using Compresse
MP12619	MP12619	US	14/494,229	9/23/2014	9760418	9/12/2017	In Force	SESSION BASED PACKET MIRRORING IN A NETWORK ASIC
MP12619	MP12619C1	US	15/672,082	8/8/2017	10417067	9/17/2019	In Force	SESSION BASED PACKET MIRRORING IN A NETWORK ASIC
MP1262	MP1262C1	US	14/305,312	6/16/2014	9313825	4/12/2016	In Force	Multi-Mode Handheld Apparatus
MP12620	MP12620	US	14/493,897	9/23/2014	9503218	11/22/2016	In Force	Method and Apparatus for Quantizing Soft Information using Non-Lin
MP12620	MP12620-2	US	14/493,953	9/23/2014	9768912	9/19/2017	In Force	Method and Apparatus for Quantizing Soft Information using Linear
MP12621	MP12621	US	14/498,580	9/26/2014	9460017	10/4/2016	In Force	METHODS AND SYSTEMS FOR EFFICIENT CACHE MIRRORING
MP12622	MP12622	US	14/716,689	5/19/2015	10129162	11/13/2018	In Force	SYSTEMS AND METHODS FOR DEFINING STORAGE

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MP12623	MP12623	US	14/870,675	9/30/2015			Pending	SYSTEMS AND METHODS FOR ALLOWING FLEXIBLE CHIP CONFIGURA
MP12624	MP12624	US	14/514,708	10/15/2014	9690590	6/27/2017	In Force	FLEXIBLE INSTRUCTION EXECUTION IN A PROCESSOR PIPELINE
MP12624	MP12624TW	TW	103138148	11/4/2014	I613590	2/1/2018	In Force	FLEXIBLE INSTRUCTION EXECUTION IN A PROCESSOR PIPELINE
MP12625	MP12625	US	14/514,596	10/15/2014	9747109	8/29/2017	In Force	FLEXIBLE INSTRUCTION EXECUTION IN A PROCESSOR PIPELINE
MP12625	MP12625TW	TW	103138147	11/4/2014	I613589	2/1/2018	In Force	FLEXIBLE INSTRUCTION EXECUTION IN A PROCESSOR PIPELINE
MP12626	MP12626	US	14/521,333	10/22/2014			Pending	APPARATUS AND A METHOD OF DETECTING ERRORS ON REGISTERS
MP12627	MP12627	US	14/521,354	10/22/2014	9990324	6/5/2018	In Force	TWO MODES OF A CONFIGURATION INTERFACE OF A NETWORK AS
MP12627	MP12627D1	US	15/969,681	5/2/2018			Pending	METHOD OF IMPLEMENTING A NETWORK ASIC IN A NETWORK DEV
MP12628	MP12628	US	14/521,359	10/22/2014	9542342	1/10/2017	In Force	SMART HOLDING REGISTERS TO ENABLE MULTIPLE REGISTER ACCES
MP12629	MP12629	US	14/521,367	10/22/2014	10078605	9/18/2018	In Force	MULTIPLE-INTERRUPT PROPAGATION SCHEME IN A NETWORK ASIC
MP1263	MP1263	US	11/840,682	8/17/2007	8441751	5/14/2013	In Force	DIBIT PULSE EXTRACTION METHODS AND SYSTEMS
MP12630	MP12630	US	14/530,388	10/31/2014	9936021	4/3/2018	In Force	METHODS AND SYSTEMS FOR A MULTI-FUNCTION ADAPTER
MP12631	MP12631	US	14/667,485	3/24/2015	9934177	4/3/2018	In Force	METHODS AND SYSTEMS FOR ACCESSING STORAGE USING A NETW
MP12632	MP12632	US	14/536,937	11/10/2014	10116564	10/30/2018	In Force	HYBRID WILDCARD MATCH TABLE
MP12632	MP12632CN	CN	2015107573136	11/9/2015			Pending	HYBRID WILDCARD MATCH TABLE
MP12632	MP12632I1	US	14/932,791	11/4/2015			Pending	HYBRID WILDCARD MATCH TABLE
MP12632	MP12632TW	TW	104134823	10/23/2015	I667900	8/1/2019	In Force	HYBRID WILDCARD MATCH TABLE
MP12633	MP12633	US	14/538,484	11/11/2014	9645790	5/9/2017	In Force	Adder Decoder
MP12634	MP12634	US	14/540,414	11/13/2014	9858222	1/2/2018	In Force	Register Access Control Among Multiple Devices
MP12635	MP12635	US	14/540,175	11/13/2014	10013385	7/3/2018	In Force	Programmable Validation Of Transaction Requests
MP12636	MP12636	US	14/540,436	11/13/2014	10002099	6/19/2018	In Force	Arbitrated Access To Resources Among Multiple Devices
MP12637	MP12637	US	14/540,379	11/13/2014	9569362	2/14/2017	In Force	Programmable Ordering And Prefetch
MP12638	MP12638	US	14/540,656	11/13/2014			Pending	Independent Ordering of Independent Transactions
MP12639	MP12639	US	14/542,350	11/14/2014	10110515	10/23/2018	In Force	PACKET SCHEDULING USING HIERARCHICAL SCHEDULING PROCESS
MP12640	MP12640	US	14/542,039	11/14/2014	9417655	8/16/2016	In Force	FREQUENCY DIVISION CLOCK ALIGNMENT
MP12640	MP12640TW	TW	104112338	4/17/2015	I642277	11/21/2018	In Force	FREQUENCY DIVISION CLOCK ALIGNMENT
MP12641	MP12641	US	14/542,298	11/14/2014	10291540	5/14/2019	In Force	METHOD AND APPARATUS FOR PERFORMING A WEIGHTED QUEUE S
MP12642	MP12642	US	14/541,807	11/14/2014	9697137	7/4/2017	In Force	FILTERING TRANSLATION LOOKASIDE BUFFER INVALIDATIONS
MP12644	MP12644	US	14/541,902	11/14/2014	9665505	5/30/2017	In Force	MANAGING BUFFERED COMMUNICATION BETWEEN SOCKETS
MP12644	MP12644TW	TW	104110836	4/2/2015	I658362	5/1/2019	In Force	MANAGING BUFFERED COMMUNICATION BETWEEN SOCKETS
MP12645	MP12645	US	14/541,882	11/14/2014	10007524	6/26/2018	In Force	MANAGING HISTORY INFORMATION FOR BRANCH PREDICTION
MP12645	MP12645TW	TW	104112340	4/17/2015	I648624	1/21/2019	In Force	MANAGING HISTORY INFORMATION FOR BRANCH PREDICTION
MP12646	MP12646	US	14/541,977	11/14/2014	9470719	10/18/2016	In Force	TESTING SEMICONDUCTOR DEVICES
MP12647	MP12647	US	14/616,580	2/6/2015	9904511	2/27/2018	In Force	HIGH PERFORMANCE SHIFTER CIRCUIT

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MP12648	MP12648	US	14/542,060	11/14/2014	9601181	3/21/2017	In Force	CONTROLLED MULTI-STEP DE-ALIGNMENT OF CLOCKS
MP12649	MP12649	US	14/542,136	11/14/2014	9910776	3/6/2018	In Force	INSTRUCTION ORDERING FOR IN-PROGRESS OPERATIONS
MP12649	MP12649C1	US	15/890,921	2/7/2018	10339054	7/2/2019	In Force	INSTRUCTION ORDERING FOR IN-PROGRESS OPERATIONS
MP12649	MP12649TW	TW	104110197	3/30/2015	I664571	7/1/2019	In Force	INSTRUCTION ORDERING FOR IN-PROGRESS OPERATIONS
MP12649	MP12649TWD1	TW	108117952	5/24/2019			Pending	INSTRUCTION ORDERING FOR IN-PROGRESS OPERATIONS
MP12650	MP12650	US	14/542,118	11/14/2014	9870328	1/16/2018	In Force	MANAGING BUFFERED COMMUNICATION BETWEEN CORES
MP12651	MP12651	US	14/542,188	11/14/2014	9613679	4/4/2017	In Force	CONTROLLED DYNAMIC DE-ALIGNMENT OF CLOCKS
MP12652	MP12652	US	14/542,202	11/14/2014	9502099	11/22/2016	In Force	MANAGING SKEW IN DATA SIGNALS WITH MULTIPLE MODES
MP12652	MP12652I1	US	14/672,787	3/30/2015	9607672	3/28/2017	In Force	MANAGING SKEW IN DATA SIGNALS WITH ADJUSTABLE STROBE
MP12653	MP12653	US	14/541,952	11/14/2014	9570128	2/14/2017	In Force	MANAGING SKEW IN DATA SIGNALS
MP12654	MP12654	US	14/940,982	11/13/2015	10078601	9/18/2018	In Force	APPROACH FOR INTERFACING A PIPELINE WITH TWO OR MORE INT
MP12655	MP12655	US	14/541,841	11/14/2014	9703669	7/11/2017	In Force	APPARATUS AND METHOD FOR DISTRIBUTED INSTRUCTION TRACE
MP12656	MP12656	US	14/542,485	11/14/2014	10116772	10/30/2018	In Force	NETWORK SWITCHING WITH CO-RESIDENT DATA-PLANE AND NETW
MP12656	MP12656D1	US	16/132,424	9/16/2018			Pending	NETWORK SWITCHING WITH CO-RESIDENT DATA-PLANE AND NETW
MP12657	MP12657	US	14/542,383	11/14/2014			Pending	APPARATUS AND METHOD FOR A MULTI-ENTITY SECURE SOFTWARE
MP12658	MP12658	US	14/541,726	11/14/2014	9684606	6/20/2017	In Force	TRANSLATION LOOKASIDE BUFFER INVALIDATION SUPPRESSION
MP12658	MP12658TW	TW	104113187	4/24/2015	I646421	1/1/2019	In Force	TRANSLATION LOOKASIDE BUFFER INVALIDATION SUPPRESSION
MP12659	MP12659	US	14/541,616	11/14/2014	9405702	8/2/2016	In Force	CACHING TLB TRANSLATIONS USING A UNIFIED PAGE TABLE WALKER
MP12659	MP12659TW	TW	104110648	4/1/2015	I641946	11/21/2018	In Force	CACHING TLB TRANSLATIONS USING A UNIFIED PAGE TABLE WALKER
MP1266	MP1266	US	11/836,641	8/9/2007	8122331	2/21/2012	In Force	MEDIA DEFECT COMPENSATION SYSTEM AND METHOD
MP12660	MP12660	US	14/541,498	11/14/2014	9501425	11/22/2016	In Force	TRANSLATION LOOKASIDE BUFFER MANAGEMENT
MP12660	MP12660TW	TW	104110402	3/31/2015			Pending	TRANSLATION LOOKASIDE BUFFER MANAGEMENT
MP12661	MP12661	US	14/541,685	11/14/2014	10394730	8/27/2019	In Force	DISTRIBUTED INTERRUPT SCHEME IN A MULTI-PROCESSOR SYSTEM
MP12662	MP12662	US	14/541,971	11/14/2014	9404970	8/2/2016	In Force	DEBUG INTERFACE FOR MULTIPLE CPU CORES
MP12663	MP12663	US	14/542,528	11/14/2014	9813342	11/7/2017	In Force	METHOD AND SYSTEM FOR IMPROVED LOAD BALANCING OF RECEIV
MP12664	MP12664	US	14/941,182	11/13/2015	10303514	5/28/2019	In Force	SHARING RESOURCES IN A MULTI-CONTEXT COMPUTING SYSTEM
MP12665	MP12665	US	14/940,585	11/13/2015			Pending	IMPLEMENTING 128-BIT SIMD OPERATIONS ON A 64-BIT DATAPATH
MP12665	MP12665CN	CN	2015107789847	11/13/2015			Pending	IMPLEMENTING 128-BIT SIMD OPERATIONS ON A 64-BIT DATAPATH
MP12665	MP12665TW	TW	104137305	11/12/2015			Pending	IMPLEMENTING 128-BIT SIMD OPERATIONS ON A 64-BIT DATAPATH
MP12666	MP12666	US	14/542,509	11/14/2014	10050896	8/14/2018	In Force	MANAGEMENT OF AN OVER-SUBSCRIBED SHARED BUFFER
MP12667	MP12667	US	14/541,769	11/14/2014	9928193	3/27/2018	In Force	DISTRIBUTED TIMER SUBSYSTEM
MP12668	MP12668	US	14/940,981	11/13/2015	9933809	4/3/2018	In Force	Automatic Data Rate Matching
MP12669	MP12669	US	14/940,538	11/13/2015			Pending	CARRY CHAIN FOR SIMD OPERATIONS
MP1267	MP1267	US	11/894,179	8/20/2007	7873377	1/18/2011	In Force	Background Scan Process For Wireless Devices

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MP1267	MP1267C1	US	13/008,574	1/18/2011	8391907	3/5/2013	In Force	BACKGROUND SCAN PROCESS FOR WIRELESS DEVICES
MP1267	MP1267C1C1	US	13/785,825	3/5/2013	9020552	4/28/2015	In Force	BACKGROUND SCAN PROCESS FOR WIRELESS DEVICES
MP12670	MP12670	US	14/541,917	11/14/2014	9824058	11/21/2017	In Force	Bypass FIFO For Multiple Virtual Channels
MP12671	MP12671	US	14/941,082	11/13/2015	9678717	6/13/2017	In Force	DISTRIBUTING RESOURCE REQUESTS IN A COMPUTING SYSTEM
MP12672	MP12672	US	14/542,216	11/14/2014	9411361	8/9/2016	In Force	FREQUENCY DIVISION CLOCK ALIGNMENT USING PATTERN SELECTI
MP12673	MP12673	US	14/542,065	11/14/2014	9568944	2/14/2017	In Force	DISTRIBUTED TIMER SUBSYSTEM ACROSS MULTIPLE DEVICES
MP12674	MP12674	US	14/940,679	11/13/2015	9703722	7/11/2017	In Force	METHOD AND SYSTEM FOR COMPRESSING DATA FOR A TRANSLATI
MP12674	MP12674C1	US	15/598,719	5/18/2017	9772952	9/26/2017	In Force	METHOD AND SYSTEM FOR COMPRESSING DATA FOR A TRANSLATI
MP12675	MP12675	US	14/609,164	1/29/2015	9740807	8/22/2017	In Force	METHOD TO MEASURE EDGE-RATE TIMING PENALTY OF DIGITAL IN
MP12676	MP12676	US	14/548,040	11/19/2014	9674303	6/6/2017	In Force	METHODS AND SYSTEMS FOR EFFICIENT DATA TRANSMISSION IN A
MP12677	MP12677-1	US	14/681,978	4/8/2015	9571412	2/14/2017	In Force	SYSTEMS AND METHODS FOR HARDWARE ACCELERATED TIMER IM
MP12677	MP12677-2	US	14/939,982	11/12/2015	10084719	9/25/2018	In Force	SYSTEMS AND METHODS FOR HARDWARE ACCELERATED METERIN
MP12677	MP12677TW	TW	104118717	6/10/2015	I631841	8/1/2018	In Force	SYSTEMS AND METHODS FOR HARDWARE ACCELERATED TIMER IM
MP12678	MP12678	US	14/577,448	12/19/2014	9866657	1/9/2018	In Force	NETWORK SWITCHING WITH LAYER 2 SWITCH COUPLED CO-RESIDEN
MP12679	MP12679	US	14/585,761	12/30/2014	9396023	7/19/2016	In Force	METHODS AND SYSTEMS FOR PARALLEL DISTRIBUTED COMPUTATIO
MP12680	MP12680	US	14/593,898	1/9/2015	9483207	11/1/2016	In Force	METHODS AND SYSTEMS FOR EFFICIENT CACHING USING AN INTELL
MP12681	MP12681	US	14/597,030	1/14/2015	9952979	4/24/2018	In Force	METHODS AND SYSTEMS FOR DIRECT MEMORY ACCESS OPERATIONS
MP12682	MP12682	US	14/609,945	1/30/2015	9426084	8/23/2016	In Force	METHODS AND SYSTEMS FOR CONTROLLING TRANSMISSION RATES I
MP12683	MP12683	US	14/634,446	2/27/2015	9600614	3/21/2017	In Force	AUTOMATED FLIP-FLOP INSERTIONS IN PHYSICAL DESIGN WITHOUT
MP12684	MP12684	US	14/617,699	2/9/2015	9778315	10/3/2017	In Force	TESTBENCH BUILDER, SYSTEM, DEVICE AND METHOD HAVING AGEN
MP12685	MP12685	US	14/617,610	2/9/2015	9330227	5/3/2016	In Force	TESTBENCH BUILDER, SYSTEM, DEVICE AND METHOD INCLUDING A
MP12686	MP12686	US	14/617,546	2/9/2015	10082538	9/25/2018	In Force	TESTBENCH BUILDER, SYSTEM, DEVICE AND METHOD
MP12688	MP12688	US	14/617,645	2/9/2015	9506982	11/29/2016	In Force	TESTBENCH BUILDER, SYSTEM, DEVICE AND METHOD INCLUDING A
MP12689	MP12689	US	14/632,200	2/26/2015	9590897	3/7/2017	In Force	METHODS AND SYSTEMS FOR NETWORK DEVICES AND ASSOCIATED
MP1269	MP1269	US	11/893,543	8/16/2007	8122234	2/21/2012	In Force	Flash Memory Backup System For Network Devices
MP12691	MP12691	US	14/638,266	3/4/2015	9720773	8/1/2017	In Force	MANAGING REUSE INFORMATION IN CACHES
MP12691	MP12691TW	TW	104113188	4/24/2015	I641947	11/21/2018	In Force	MANAGING REUSE INFORMATION IN CACHES
MP12692	MP12692	US	14/638,194	3/4/2015	10013360	7/3/2018	In Force	MANAGING REUSE INFORMATION WITH MULTIPLE TRANSLATION ST
MP12694	MP12694	US	14/637,533	3/4/2015	9678159	6/13/2017	In Force	COMMUNICATION AND CONTROL TOPOLOGY FOR EFFICIENT TESTIN
MP12694	MP12694TW	TW	104108042	3/13/2015	I631355	8/1/2018	In Force	COMMUNICATION AND CONTROL TOPOLOGY FOR EFFICIENT TESTIN
MP12696	MP12696	US	15/065,799	3/9/2016	10002218	6/19/2018	In Force	DESIGN AND VERIFICATION OF A MULTICHIP COHERENCE PROTOCOL
MP12697	MP12697	US	14/662,405	3/19/2015	9471509	10/18/2016	In Force	MANAGING ADDRESS-INDEPENDENT PAGE ATTRIBUTES
MP12697	MP12697TW	TW	104113191	4/24/2015	I648625	1/21/2019	In Force	MANAGING ADDRESS-INDEPENDENT PAGE ATTRIBUTES
MP12698	MP12698	US	14/664,680	3/20/2015	9600620	3/21/2017	In Force	REPEATER INSERTIONS PROVIDING REDUCED ROUTING PERTURBATI

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