

PATENT ASSIGNMENT COVER SHEET

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Title of Invention: METHODS AND APPARATUS TO REDUCE THERMAL FLUCTUATIONS IN SEMICONDUCTOR PROCESSORS

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Foreign Applications filed/intended in (countries): JAPAN, CHINA, European Patent Organization

The invention identified above is a Subject Invention under **35 U.S.C. 200, et seq.**, and the Standard Patent Rights clause at **37 CFR 401.14, FAR 52.227-11** or **FAR 52.227-12** (if applicable) which are included among the terms of the above identified grant or contract award from the United States Government. This document is confirmatory of:

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Signed this 4 day of January, 20 23.

By Erin Sorensen
(Institutional Business Official)

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