

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT7971988

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
PRAMOD KOLAR	06/01/2021
STEPHEN EDWARD LILES	05/27/2021
GREGORY CHRISTOPHER BURDA	05/27/2021
RECEIVING PARTY DATA	
Name:	MICROSOFT TECHNOLOGY LICENSING, LLC
Street Address:	ONE MICROSOFT WAY
City:	REDMOND
State/Country:	WASHINGTON
Postal Code:	98052
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	18323250
CORRESPONDENCE DATA	
Fax Number:	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Email:	usdocket@microsoft.com
Correspondent Name:	MICROSOFT CORPORATION
Address Line 1:	ONE MICROSOFT WAY
Address Line 4:	REDMOND, WASHINGTON 98052
ATTORNEY DOCKET NUMBER:	409980-US-CNT
NAME OF SUBMITTER:	KATHERINE WILSON
SIGNATURE:	/Katherine Wilson/
DATE SIGNED:	05/24/2023
Total Attachments: 6	
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source=409980-US-CNT-Assignment-PK-Signed#page1.tif	
source=409980-US-CNT-Assignment-PK-Signed#page2.tif	
source=409980-US-CNT-Assignment-SL-Signed#page1.tif	

I Gregory Christopher BURDA ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled "MEMORY SYSTEM WITH BURST MODE HAVING LOGIC GATES AS SENSE ELEMENTS" ("APPLICATION"), which:

will be filed. ASSIGNOR hereby authorizes, and requests, ASSIGNEE'S legal representative, Microsoft Technology Licensing, LLC, One Microsoft Way, Redmond, Washington 98052 (ASSIGNEE'S LEGAL REPRESENTATIVES), to insert here in parentheses (filed on _____ and given Application No. _____ by the following Office _____) this APPLICATION's Application No., filing date, and Office, when known;

was filed on _____ and was given Application No _____ by the following Office " _____";

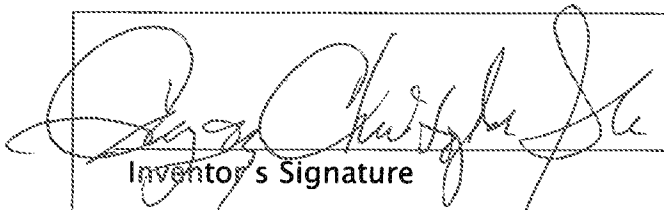
Microsoft Technology Licensing, LLC, a Washington limited liability company, on behalf of itself and its successors and assigns ("ASSIGNEE"), is entitled to, and is desirous of acquiring, the entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

ASSIGNOR hereby authorizes, and requests, ASSIGNEE'S LEGAL REPRESENTATIVES to insert the Application No., filing date, and Office into the following table for any APPLICATION DERIVATIVES after they are known;

Application No.	Filing Date	Office

For good and valuable consideration, the receipt of which is hereby acknowledged by the ASSIGNOR, the ASSIGNOR hereby sells, assigns and transfers to the ASSIGNEE, ASSIGNOR's entire and exclusive rights, title and interest, including the right of priority, in the INVENTION and the APPLICATION (and all other applications and patents derived therefrom, such as continuing applications, in whole or in part, in and for the United States, its territories, and all foreign countries ("APPLICATION DERIVATIVES"));

ASSIGNOR agrees to execute all instruments and documents required for the making and prosecution of the APPLICATION (and APPLICATION DERIVATIVES), for litigation regarding letters patent derived therefrom, and for the purpose of protecting and perfecting title to the APPLICATION (and APPLICATION DERIVATIVES).

 Inventor's Signature	5/27/2021 Date
Gregory Christopher BURDA Printed Name in English	 Printed Name in Native Language (if other than English)

I Pramod KOLAR (“ASSIGNOR”) am the original inventor or an original joint inventor of subject matter (“INVENTION”) disclosed and/or claimed in a patent application entitled “MEMORY SYSTEM WITH BURST MODE HAVING LOGIC GATES AS SENSE ELEMENTS” (“APPLICATION”), which:

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- was filed on May 28, 2021 and was given Application No 17/333,691 by the following Office “USPTO”;

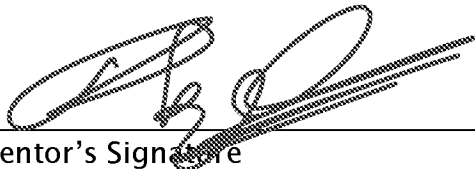
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 _____ Inventor's Signature	<u>1 June 2021</u> _____ Date
<u>Pramod KOLAR</u> _____ Printed Name in English	_____ Printed Name in Native Language (if other than English)

PATENT ASSIGNMENT

MS Docket No.: 409980-US-NP

I Stephen Edward LILES ("ASSIGNOR") am the original inventor or an original joint inventor of subject matter ("INVENTION") disclosed and/or claimed in a patent application entitled " MEMORY SYSTEM WITH BURST MODE HAVING LOGIC GATES AS SENSE ELEMENTS" ("APPLICATION"), which:

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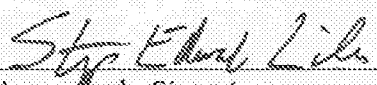
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 Inventor's Signature	<u>5-27-2021</u> Date
<u>Stephen Edward LILES</u> Printed Name in English	 Printed Name in Native Language (if other than English)