

PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
JEEVITH KUMAR NAGAMANGALA MUNINARAYANAPPA	02/09/2022
KAILAS NARAYANA IYER	02/10/2022
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Name:	Cypress Semiconductor Corporation
Street Address:	198 Champion Court
City:	San Jose
State/Country:	CALIFORNIA
Postal Code:	95134
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17669693
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ATTORNEY DOCKET NUMBER:	2021P05181 US
NAME OF SUBMITTER:	STOYCHO D. DRAGANOFF
SIGNATURE:	/StoychoDDraganoff#56181/
DATE SIGNED:	06/28/2023
Total Attachments: 4	
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ASSIGNMENT OF PATENT APPLICATION

Attorney Docket No.: 2021P05181 US

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, I/we, the undersigned,

Jeevith Kumar NAGAMANGALA MUNINARAYANAPPA
Kailas NARAYANA IYER

hereby sell, assign, and transfer to

Cypress Semiconductor Corporation

a corporation of Delaware.

(Type of Assignee: e.g., corporation, company, partnership, university, etc.)

having a principal place of business at 198 Champion Court, San Jose, California 95134-1709.

("Assignee"), and its successors, assigns, and legal representatives, the entire right, title, and interest for the United States and all foreign countries, in and to any and all inventions or improvements that are disclosed in the application (provisional or non-provisional) for the United States patent which may have a declaration executed by the undersigned prior hereto or concurrently herewith and is entitled

Bi-phase Mark Code Asynchronous Decoder using SPI Block,

said patent application also identified as follows (when known):

[I/we hereby authorize an attorney or agent for said Assignee to insert below the application number and filing date of said patent application when known.]

United States Patent Application Number 17/669,693 filed 02-11-2023,

and in and to said application (provisional or non-provisional) and all provisional applications, non-provisional applications, utility applications, design applications, divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications that have been or shall be filed in the United States and all foreign countries on any of said inventions or improvements; and in and to all original patents, reissued patents, reexamination certificates, and extensions, that have been or shall be issued in the United States and all foreign countries on said inventions or improvements; and in and to all rights of priority resulting from the filing of said United States application;

agree that said Assignee may apply for and receive a patent or patents for said inventions or improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns, and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all provisional applications, non-provisional applications, utility

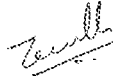
applications, design applications, divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissue applications, reexaminations, extensions, and all other patent applications on any and all said inventions or improvements; execute all rightful oaths, assignments, powers of attorney, and other papers; communicate to said Assignee, its successors, assigns, and representatives all facts known to the undersigned relating to said inventions or improvements and the history thereof; and generally assist said Assignee, its successors, assigns, or representatives in securing and maintaining proper patent protection for said inventions or improvements and for vesting title to said inventions or improvements, and all applications for patents and all patents on said inventions or improvements, in said Assignee, its successors, assigns, and legal representatives; and

covenant with said Assignee, its successors, assigns, and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Each Inventor: Please Sign and Date Below:

09-Feb-2022

Date



Jeevith Kumar NAGAMANGALA MUNINARAYANAPPA

Date

Kailas NARAYANA IYER

Date

Assignment Document Return Address:
CYPRESS SEMICONDUCTOR CORPORATION
198 Champion Court
San Jose, CA 95134

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Each Inventor: Please Sign and Date Below:

Date

Jeevith Kumar NAGAMANGALA MUNINARAYANAPPA

10-February-2022

Date



Kailas NARAYANA IYER

Date

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