### PATENT ASSIGNMENT COVER SHEET

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

#### **CONVEYING PARTY DATA**

Name	Execution Date
KOPIN CORPORATION	11/22/2019

### **RECEIVING PARTY DATA**

Name:	SOLOS TECHNOLOGY LIMITED
Street Address:	UNIT 211, 2/F., PHOTONICS CENTER, NO. 2 SCIENCE PARK EAST AVENUE
Internal Address:	HONG KONG SCIENCE PARK, SHATIN, N.T.
City:	HONG KONG
State/Country:	HONG KONG

### **PROPERTY NUMBERS Total: 1**

Property Type	Number
Application Number:	16736593

### CORRESPONDENCE DATA

**Fax Number:** (206)770-6562

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

**Phone:** 2062511699

Email: mark@peloquinlaw.com

Correspondent Name: PELOQUIN, PLLC
Address Line 1: 701 FIFTH AVE

Address Line 2: STE 4200

Address Line 4: SEATTLE, WASHINGTON 98104

ATTORNEY DOCKET NUMBER:	S120219.P002	
NAME OF SUBMITTER:	MARK S PELOQUIN	
SIGNATURE:	/MARK S PELOQUIN/	
DATE SIGNED:	07/19/2023	
	This document serves as an Oath/Declaration (37 CFR 1.63).	

### **Total Attachments: 4**

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PATENT REEL: 064318 FRAME: 0351 ASSIGNMENT No.: KSTL20191112

# ASSIGNMENT OF UNITED STATES PATENTS, UNITED STATES PATENT APPLICATIONS, AND INTERNATIONAL (PCT) & FOREIGN PATENT APPLICATIONS

Whereas, Kopin Corporation, a Delaware corporation, having a place of business at 125 North Drive, Westborough, Massachusetts 01581 (hereinafter "Assignor") is the sole owner of the entire right, title and interest in and to the United States Patents, United States Patent Applications, and International (PCT) and Foreign Patent Applications all of which are described in the IP Schedule attached hereto and made a part hereof (hereinafter "Patents"); and

Whereas, Solos Technology Limited, a Hong Kong corporation, having a place of business at Unit 211, 2/F., Photonics Centre, No. 2 Science Park East Avenue, Hong Kong Science Park, Shatin, N.T., Hong Kong (hereinafter "Assignee") is desirous of acquiring the entire right, title and interest in and to the Patents.

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, the Assignor has sold, assigned and transferred, and by these presents does hereby sell, assign and transfer Assignor's entire right, title and interest in and to the Patents and all reissues and extensions thereof, including all claims, if any, which may have arisen for infringement of the patents prior to the date of this assignment, all said rights to be held and enjoyed by the Assignee for its own use and for the use of its successors, assigns or other legal representatives, to the full end of the term for which the Patents have been or will be granted, extended or reissued, as fully and entirely as the same would have been held and enjoyed by the Assignor if this assignment and sale had not been made.

Assignor further agrees that Assignor will, without demanding any further consideration therefore, at the request but at the expense of Assignee, do all lawful and just acts, including the execution and acknowledgment of instruments, that may be or become necessary for obtaining, sustaining, or reissuing the Patents, and for maintaining and perfecting Assignee's right to the Patents.

Signature: Signature:	
Printed Name: Hong Cho. Printed Nam  Title: Chief Technology Officer Title: Ca  Date: Nov. 22, 2019 Date: 2	Kenny Cheung eneral Manages 2 Nov 2019

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## IP SCHEDULE UNITED STATES PATENTS

FILE NUMBER	PATENT NO.	ISSUE DATE	TITLE
41090.C001	7,386,135	June 10, 2008	Cardioid Beam with a Desired Null Based Acoustic Devices, Systems and Methods
41090.C011	8,885,850	November 11, 2014	Cardioid Beam with a Desired Null Based Acoustic Devices, Systems and Methods
41090.C022	9,456,275	September 27, 2016	Cardioid Beam with a Desired Null Based Acoustic Devices, Systems and Methods
41090.C033	9,491,544	November 8, 2016	Frequency Domain Noise Cancellation With A Desired Null Based Acoustic devices, Systems, and Methods
41090.C044	9,406,293	August 2, 2016	Apparatus and Methods to Detect and Obtain Desired Audio
K41090.P001	9,633,670	April 25, 2017	Dual Stage Noise Reduction Architecture For Desired Signal Extraction
K41090.P002	9,257,952	February 9, 2016	Apparatuses and Methods for Multi-Channel Signal Compression During Desired Voice Activity Detection
K41090.P2C1	9,792,927	October 17, 2017	Apparatuses and Methods for Multi-Channel Signal Compression During Desired Voice Activity Detection
K41090.P003	9,312,826	April 12, 2016	Apparatus and Methods for Acoustic Channel Auto- Balancing During Multi- Channel Signal Extraction
K41090.P005	10,306,389	May 28, 2019	Head Wearable Acoustic System with Noise Canceling Microphone Geometry Apparatuses and Methods
K41090.P008	9,941,895	April 10, 2018	Time Delay in Digitally Oversampled Sensor Systems, Apparatuses, and Methods
K41090.P8C1	10,298,253	May 21, 2019	Time Delay in Digitally Oversampled Sensor Systems, Apparatuses, and Methods
K41090.P2C2	10,339,952	July 2, 2019	Apparatuses and Systems for Acoustic Channel Auto- Balancing During Multi- Channel Signal Extraction

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## IP SCHEDULE (Continued)

## UNITED STATES PATENT APPLICATIONS

FILE NUMBER	SERIAL NO.	FILING DATE	TITLE
K41090.P004	14/886,054	October 18, 2015	Wrist Wearable Apparatuses and Methods with Desired Signal Extraction
K41090.P007	14/886,080	October 18, 2015	Apparatuses and Methods for Enhanced Speech Recognition in Variable Environments
K41090.P8C2	16/399,867	April 30, 2019	Beamforming Using Fractional Time Delay in Digitally Oversampled Sensor Systems, Apparatuses, and Methods
K41090.P013	16/171,254	October 25, 2018	Programmable Fractional Time Delay in Digitally Oversampled Microphone Systems, Circuits, and Methods
K41090.P014	16/171,266	October 25, 2018	Fractional Time Delay Structures in Digitally Oversampled Microphone Systems, Circuits, and Methods
K41090.P016	16/420,082	May 22, 2019	Microphone Configurations for Eyewear Devices Systems, Apparatus, and Methods
K41090.Z017	62/873,889	July 13, 2019	Wearable Devices Apparatuses, Systems, and Methods

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## IP SCHEDULE (Continued)

## INTERNATIONAL (PCT) & FOREIGN PATENT APPLICATIONS

FILE NUMBER	SERIAL NO.	FILING DATE	TITLE
K41090P08PCT-CN	201780047952.0 (China)	January 31, 2017	Time Delay in Digitally Oversampled Sensor Systems, Apparatuses, and Methods
K41090P08PCT-JP	2019-526185 (Japan)	January 30, 2019	Time Delay in Digitally Oversampled Sensor Systems, Apparatuses, and Methods
K41090P013PCT	PCT/US2018/057869	October 26, 2018	Programmable Fractional Time Delay in Digitally Oversampled Microphone Systems, Circuits, and Methods
K41090P014PCT	PCT/US2018/057879	October 27, 2019	Fractional Time Delay Structures in Digitally Oversampled Microphone Systems, Circuits, and Methods

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**RECORDED: 07/19/2023**