

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
Stylesheet Version v1.2

EPAS ID: PAT8164196

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
D-WAVE SYSTEMS INC.	08/03/2022
RECEIVING PARTY DATA	
Name:	1372929 B.C. LTD.
Street Address:	3033 BETA AVENUE
City:	BURNABY
State/Country:	CANADA
Postal Code:	V5G 4M9
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	17387654
CORRESPONDENCE DATA	
Fax Number:	(206)621-8783
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	(206) 340-1000
Email:	SClayton@cozen.com
Correspondent Name:	FRANK ABRAMONTE
Address Line 1:	999 THIRD AVENUE
Address Line 2:	SUITE 1900
Address Line 4:	SEATTLE, WASHINGTON 98104
ATTORNEY DOCKET NUMBER:	240105.592C1/00524542
NAME OF SUBMITTER:	FRANK ABRAMONTE
SIGNATURE:	/Frank Abramonte/
DATE SIGNED:	09/13/2023
Total Attachments: 43	
source=592C1_Assignment_Dwave_to_1372929#page1.tif	
source=592C1_Assignment_Dwave_to_1372929#page2.tif	
source=592C1_Assignment_Dwave_to_1372929#page3.tif	
source=592C1_Assignment_Dwave_to_1372929#page4.tif	
source=592C1_Assignment_Dwave_to_1372929#page5.tif	

source=592C1_Assignment_Dwave_to_1372929#page6.tif
source=592C1_Assignment_Dwave_to_1372929#page7.tif
source=592C1_Assignment_Dwave_to_1372929#page8.tif
source=592C1_Assignment_Dwave_to_1372929#page9.tif
source=592C1_Assignment_Dwave_to_1372929#page10.tif
source=592C1_Assignment_Dwave_to_1372929#page11.tif
source=592C1_Assignment_Dwave_to_1372929#page12.tif
source=592C1_Assignment_Dwave_to_1372929#page13.tif
source=592C1_Assignment_Dwave_to_1372929#page14.tif
source=592C1_Assignment_Dwave_to_1372929#page15.tif
source=592C1_Assignment_Dwave_to_1372929#page16.tif
source=592C1_Assignment_Dwave_to_1372929#page17.tif
source=592C1_Assignment_Dwave_to_1372929#page18.tif
source=592C1_Assignment_Dwave_to_1372929#page19.tif
source=592C1_Assignment_Dwave_to_1372929#page20.tif
source=592C1_Assignment_Dwave_to_1372929#page21.tif
source=592C1_Assignment_Dwave_to_1372929#page22.tif
source=592C1_Assignment_Dwave_to_1372929#page23.tif
source=592C1_Assignment_Dwave_to_1372929#page24.tif
source=592C1_Assignment_Dwave_to_1372929#page25.tif
source=592C1_Assignment_Dwave_to_1372929#page26.tif
source=592C1_Assignment_Dwave_to_1372929#page27.tif
source=592C1_Assignment_Dwave_to_1372929#page28.tif
source=592C1_Assignment_Dwave_to_1372929#page29.tif
source=592C1_Assignment_Dwave_to_1372929#page30.tif
source=592C1_Assignment_Dwave_to_1372929#page31.tif
source=592C1_Assignment_Dwave_to_1372929#page32.tif
source=592C1_Assignment_Dwave_to_1372929#page33.tif
source=592C1_Assignment_Dwave_to_1372929#page34.tif
source=592C1_Assignment_Dwave_to_1372929#page35.tif
source=592C1_Assignment_Dwave_to_1372929#page36.tif
source=592C1_Assignment_Dwave_to_1372929#page37.tif
source=592C1_Assignment_Dwave_to_1372929#page38.tif
source=592C1_Assignment_Dwave_to_1372929#page39.tif
source=592C1_Assignment_Dwave_to_1372929#page40.tif
source=592C1_Assignment_Dwave_to_1372929#page41.tif
source=592C1_Assignment_Dwave_to_1372929#page42.tif
source=592C1_Assignment_Dwave_to_1372929#page43.tif

INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT

This INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT (this "**IP Assignment Agreement**") dated as of August 3, 2022 is made by D-Wave Systems Inc. ("**D-Wave**") in favor of 1372929 B.C. LTD. (the "**Purchaser**"), the purchaser of certain assets of D-Wave pursuant to that certain Asset Purchase Agreement between D-Wave and the Purchaser dated as of the date hereof (the "**Purchase Agreement**").

RECITALS

A. Under the terms of the Purchase Agreement, D-Wave has conveyed, transferred, and assigned to the Purchaser, among other assets, certain intellectual property of D-Wave and has agreed to execute and deliver this IP Assignment Agreement, for recording with the Canadian Intellectual Property Office and corresponding entities or agencies in any applicable jurisdictions.

B. This IP Assignment Agreement is delivered pursuant to Section 3.2(a) and 3.3(a) of the Purchase Agreement.

NOW THEREFORE, for good and valuable consideration, the receipt and sufficiency of which are hereby acknowledged by each party, the parties agree as follows:

1. **Assignment.** D-Wave hereby irrevocably conveys, transfers, and assigns to the Purchaser, and the Purchaser hereby accepts, all of D-Wave's right, title, and interest in and to the following (the "**Assigned IP**"):

- (a) the patents set forth on Schedule 1 hereto and all issuances, divisions, continuations, continuations-in-part, reissues, extensions, reexaminations, and renewals thereof (the "**Patents**");
- (b) all rights of any kind whatsoever of D-Wave accruing under any of the foregoing provided by applicable law of any jurisdiction, by international treaties and conventions, and otherwise throughout the world;
- (c) any and all royalties, fees, income, payments, and other proceeds now or hereafter due or payable with respect to any and all of the foregoing; and
- (d) any and all claims and causes of action with respect to any of the foregoing, whether accruing before, on, or after the date hereof, including all rights to and claims for damages, restitution, and injunctive and other legal and equitable relief for past, present, and future infringement, dilution, misappropriation, violation, misuse, breach, or default, with the right but no obligation to sue for such legal and equitable relief and to collect, or otherwise recover, any such damages.

2. **Recordation and Further Actions.** D-Wave hereby authorizes the Registrar of Trademarks in the Canadian Intellectual Property Office, and the officials of corresponding entities or agencies in any applicable jurisdictions, to record and register this IP Assignment Agreement upon request by the Purchaser. Following the date hereof, D-Wave shall take such steps and actions, and provide such cooperation and assistance to the Purchaser and its successors, assigns, and legal representatives, including the execution and delivery of any affidavits, declarations, oaths, exhibits, assignments, powers of attorney, or other documents, as may be

reasonably necessary to effect, evidence, or perfect the assignment of the Assigned IP to the Purchaser, or any assignee or successor thereto.

3. **Terms of the Purchase Agreement.** The parties hereto acknowledge and agree that this IP Assignment Agreement is entered into pursuant to the Purchase Agreement, to which reference is made for a further statement of the rights and obligations of D-Wave and the Purchaser with respect to the Assigned IP. The representations, warranties, covenants, agreements, and indemnities contained in the Purchase Agreement shall not be superseded hereby but shall remain in full force and effect to the full extent provided therein. In the event of any conflict or inconsistency between the terms of the Purchase Agreement and the terms hereof, the terms of the Purchase Agreement shall govern.

4. **Counterparts.** This IP Assignment Agreement may be executed in counterparts, each of which shall be deemed an original, but all of which together shall be deemed one and the same agreement. A signed copy of this IP Assignment Agreement delivered by facsimile, e-mail, or other means of electronic transmission shall be deemed to have the same legal effect as delivery of an original signed copy of this IP Assignment Agreement.

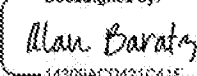
5. **Successors and Assigns.** This IP Assignment Agreement shall be binding upon and shall inure to the benefit of the parties hereto and their respective successors and assigns.

6. **Governing Law.** This IP Assignment Agreement and any claim, controversy, dispute, or cause of action (whether in contract, tort, or otherwise) based upon, arising out of, or relating to this IP Assignment Agreement and the transactions contemplated hereby shall be governed by, and construed in accordance with, the laws of Canada and the Province of British Columbia, without giving effect to any choice or conflict of law provision or rule (whether of the Province of British Columbia or any other jurisdiction).

[Signature Page Follows]

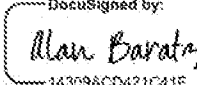
IN WITNESS WHEREOF, D-Wave has duly executed and delivered this IP Assignment Agreement as of the date first above written.

D-WAVE SYSTEMS INC.

DocuSigned by:

By: _____
Name: Alan Baratz
Title: President

AGREED TO AND ACCEPTED

1372929 B.C. LTD.

By: 
Name: Alan Baratz
Title: President

SCHEDULE 1
ASSIGNED PATENT REGISTRATIONS

Please see attached.

Patent Title	Country	Appl. No.	Issued Patent No.
Analog Processor Comprising Quantum Devices	AU	2005-318643	AU2005318643
Coupling Methods and Architectures for Information Processing	AU	2005-321780	AU2005321780
Systems, Methods and Apparatus for Local Programming of Quantum Processor Elements	AU	2007329156	AU2007329156
Superconducting Printed Circuit Board Related Systems, Methods, And Apparatus	CA	3045487	
Systems and Methods for Superconducting Flux Qubit Readout	CA	2814865	CA2814865
Systems and Methods for Superconducting Integrated Circuits	CA	2786281	CA2786281
Analog Processor Comprising Quantum Devices	CA	2592084	CA2592084
Analog Processor Comprising Quantum Devices	CA	2853583	CA2853583

Patent Title	Country	Appl. No.	Issued Patent No.
Systems, Methods and Apparatus for Measuring Magnetic Fields	CA	2738969	CA2738969
Qubit State Copying	CA	2606288	CA2606288
Systems and Methods For Fabrication of Superconducting Integrated Circuits	CA	2751897	CA2751897
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	CA	3077880	
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	CA	3029949	CA3029949
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	CA	2976901	CA2976901
Superconducting Shield for use with an Integrated Circuit for Quantum Computing	CA	2667640	CA2667640
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	CA	2736116	CA2736116
Systems, Methods and Apparatus for Local Programming of Quantum Processor Elements	CA	2669816	CA2669816

Patent Title	Country	Patent No.	Issued Patent No.
Systems, Devices and Methods for Analog Processing	CA	2719343	CA2719343
Systems, Methods and Apparatus for Calibrating, Controlling and Operating a Quantum Processor	CA	2724857	CA2724817
Physical Realization Of A Universal Adiabatic Quantum Computer	CA	2681138	CA2681138
Systems, Methods and Apparatus for Qubit State Readout	CA	2698132	CA2698132
Systems and Methods for Superconducting Flux Qubit Readout	CH	1183980.6	EP2638448
Systems and Methods For Fabrication of Superconducting Integrated Circuits	CH	16179636.6	EP3058866
Systems and Methods For Fabrication of Superconducting Integrated Circuits	CH	10746835.8	EP2401776
Systems, Methods and Apparatus for Calibrating, Controlling and Operating a Quantum Processor	CH	08751304.9	EP2326444
Systems And Methods For Improving Performance Of An Analog Processor	CN	201980166895	
Systems And Methods For Coupling Qubits In A Quantum Processor	CN	2018800897210	
Single Flux Quantum Source For Projective Measurements	CN	2015800460254	
Superconducting Printed Circuit Board Related Systems, Methods, And Apparatus	CN	2017800761139	

Patent Title	Country	Appl. No.	Issued Patent No.
Systems and Methods For Fabrication Of Superconducting Integrated Circuits	CN	20188021010X	
Systems And Methods For Degeneracy Mitigation In A Quantum Processor	CN	20188076369B	CN108475352B
Systems And Methods For Creating And Using Higher Degree Interactions Between Quantum Devices	CN	201680586867	
Systems And Methods For Quantum Processor Topology	CN	201780416714	
Quantum Flux Parametron Based Structures (E.G. Muxes, Demuxes, Shift Registers), Addressing Lines And Related Methods	CN	201880772357	
Systems And Methods For Superconducting Devices Used In Superconducting Circuits And Scalable Computing	CN	2017802268619	

Patent Title	Country	Patent No.	Issued Patent No.
Frequency Multiplexed Resonator Input And/or Output For A Superconducting Device	CN	201680263810	
Method Of Forming Superconducting Wiring Layers With Low Magnetic Noise	CN	2015800557384	CN107064755B
Systems and Methods for Cryogenic Refrigeration	CN	201480062478X	CN105765320
Methods of Adiabatic Quantum Computing	CN	200780003686X	CN101375902
Systems and Methods for Superconducting Integrated Circuits	CN	201180006131.5	CN102714272
Analog Processor Comprising Quantum Devices	CN	200580044348.X	CN100585629
Coupling Methods and Architectures for Information Processing	CN	200580045676.1	CN101095245
Qubit State Copying	CN	200680016776.8	CN101178610
Systems and Methods For Fabrication of Superconducting Integrated Circuits	CN	2018112611808	CN109626323

Patent Title	Country	Pub. No.	Pub. Date
Systems and Methods For Fabrication of Superconducting Integrated Circuits	CN	201610948381B	CN102914219
Systems and Methods For Fabrication of Superconducting Integrated Circuits	CN	201088009224.9	CN102334209
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	CN	200680141676.X	CN102187489
Systems, Methods and Apparatus for Local Programming of Quantum Processor Elements	CN	200782044687.7	CN101548288
Systems, Methods and Apparatus for Calibrating, Controlling and Operating a Quantum Processor	CN	200680117971.1	CN102037475
Systems, Methods and Apparatus for Qubit Readout	CN	200880117253.4	CN101868802
Systems and Methods For Fabrication Of Superconducting Integrated Circuits	DE	1874799B.5	EP3177700
Systems And Methods For Creating And Using Higher Degree Interactions Between Quantum Devices	DE	16933932.1	EP3335161

Patent Title	Country	Appl. No.	Patent No.
Frequency Multiplexed Resonator Input And/or Output For A Superconducting Device	DE	16793493.5	EP3266063
Method Of Forming Superconducting Wiring Layers With Low Magnetic Noise	DE	15832064.8	EP3155377
Systems and Methods for Cryogenic Refrigeration	DE	14645880.7	EP3048736
Systems and Methods for Error Correction in Quantum Computation	DE	14749223.5	EP2950416
Systems and Methods for Cryogenic Refrigeration	DE	13856325.8	EP2323165
Systems and Methods for Superconducting Flux Qubit Readout	DE	11839080.6	EP2638448
Analog Processor Comprising Quantum Devices	DE	5846198.6	EP1851693
Coupling Methods and Architectures for Information Processing	DE	05823448.5	EP1831935
Systems and Methods For Fabrication of Superconducting Integrated Circuits	DE	16179696.6	EP3098865
Qubit State Copying	DE	6741429.2	EP1875413

Patent Title	Country	Appl. No.	Issued Patent No.
Systems and Methods For Fabrication of Superconducting Integrated Circuits	DE	10746830.8	EP2403778
Systems, Methods, And Apparatus For Active Compensation Of Quantum Processor Elements	DE	08812240.1	EP2340572
Systems, Devices and Methods for Analog Processing	DE	09726326.9	EP2263165
Systems, Methods and Apparatus for Calibrating, Controlling and Operating a Quantum Processor	DE	09751384.9	EP2324444
Systems, Methods, And Apparatus For Qubit State Readout	DE	08800375	EP2208078
Single Flux Quantum Source For Projective Measurements	EP	18790186.1	
Systems and Methods For Fabrication Of Superconducting Integrated Circuits	EP	22163558.4	

Patent Title	Country	Patent No.	Issued Patent No.
Systems And Methods For Degeneracy Mitigation In A Quantum Processor	EP	10660808.1	
Systems And Methods For Creating And Using Higher Degree Interactions Between Quantum Devices	EP	21308673.4	
Systems And Methods For Quantum Processor Topology	EP	17610979.9	
Quantum Flux Parametron Based Structures (E.G. Muxes, Demuxes, Shift Registers), Addressing Lines And Related Methods	EP	15864813.3	
Systems and Methods for Cryogenic Refrigeration	FI	14845863.7	EP2046735
Systems and Methods for Cryogenic Refrigeration	FI	13859929.8	EP2923160
Systems, Devices and Methods for Analog Processing	FI	09726328.9	EP2263165
Systems and Methods For Fabrication Of Superconducting Integrated Circuits	FR	18747598.5	EP3577700
Systems And Methods For Creating And Using Higher Degree Interactions Between Quantum Devices	FR	16835532.3	EP3385161

Patent Title	Country	Pub. No.	Pub. Date (Yr.)
Frequency Multiplexed Resonator Input And/or Output For A Superconducting Device	FR	16793493.5	EP3266063
Method Of Forming Superconducting Wiring Layers With Low Magnetic Noise	FR	15832064.8	EP3155377
Systems and Methods for Cryogenic Refrigeration	FR	14645880.7	EP3048736
Systems and Methods for Error Correction in Quantum Computation	FR	14749223.5	EP2950416
Systems and Methods for Cryogenic Refrigeration	FR	13856325.8	EP2923366
Systems and Methods for Superconducting Flux Qubit Readout	FR	11839080.6	EP2638448
Analog Processor Comprising Quantum Devices	FR	5846198.6	EP1851693
Coupling Methods and Architectures for Information Processing	FR	05823448.5	EP1831935
Systems and Methods For Fabrication of Superconducting Integrated Circuits	FR	16179696.6	EP3098865
Qubit State Copying	FR	6741429.2	EP1875413

Patent Title	Country	Appl. No.	Issued Patent No.
Systems and Methods For Fabrication of Superconducting Integrated Circuits	FR	10746830.8	EP2403778
Systems, Methods, And Apparatus For Active Compensation Of Quantum Processor Elements	FR	08812240.1	EP2340572
Systems, Devices and Methods for Analog Processing	FR	09726326.9	EP2283168
Systems, Methods and Apparatus for Calibrating, Controlling and Operating a Quantum Processor	FR	09751394.9	EP2324444
Systems, Methods, And Apparatus For Qubit State Readout	FR	08800375	EP2208078
Systems and Methods For Fabrication Of Superconducting Integrated Circuits	GB	1874798.5	EP3577700
Systems And Methods For Creating And Using Higher Degree Interactions Between Quantum Devices	GB	18805032.1	EP3395161
Systems And Methods For Superconducting Devices Used in Superconducting Circuits And Scalable Computing	GB	1820299.4	GB2570989

Patent Title	Country	Appl. No.	Issued Patent No.
Systems And Methods For Superconducting Devices Used In Superconducting Circuits And Scalable Computing	GB	1820297.8	GB2570988
Systems And Methods For Superconducting Devices Used In Superconducting Circuits And Scalable Computing	GB	1819405.2	GB2566189
Frequency Multiplexed Resonator Input And/or Output For A Superconducting Device	GB	18793459.5	EP3268063
Method Of Forming Superconducting Wiring Layers With Low Magnetic Noise	GB	15832064.8	EP3195377
Systems and Methods for Cryogenic Refrigeration	GB	14845860.7	EP3049736
Systems and Methods for Error Correction In Quantum Computation	GB	14749223.5	EP2954415
Systems and Methods for Cryogenic Refrigeration	GB	13859929.8	EP2823160
Systems and Methods for Superconducting Flux Qubit Readout	GB	13899580.6	EP2838448

Patent Title	Country	Pub. No.	Patent No.
Analog Processor Comprising Quantum Devices	GB	5682195.6	EP1851693
Systems and Methods For Fabrication of Superconducting Integrated Circuits	GB	10179696.6	EP3096867
Coupling Methods and Architectures for Information Processing	GB	05823448.5	EP1831989
Systems and Methods For Fabrication of Superconducting Integrated Circuits	GB	10746830.6	EP2801776
Qubit State Copying	GB	6743429.2	EP1875419
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	GB	09812240.1	EP2340572
Systems, Devices and Methods for Analog Processing	GB	09726375.9	EP2263166
Systems, Methods and Apparatus for Calibrating, Controlling and Operating a Quantum Processor	GB	09751394.9	EP2326444
Systems, Methods, And Apparatus For Qubit State Readout	GB	08800375	EP2106078
Method Of Forming Superconducting Wiring Layers With Low Magnetic Noise	IE	15812064.8	EP3195377
Systems And Methods For Improving Performance Of An Analog Processor	JP	2020-540256	

Patent Title	Country	Appl. No.	Issued Patent No.
Single Flux Quantum Source For Projective Measurements	JP	2021-513362	
Systems and Methods For Fabrication Of Superconducting Integrated Circuits	JP	2019-062235	
Systems And Methods For Degeneracy Mitigation in A Quantum Processor	JP	2021-070787	
Systems And Methods For Degeneracy Mitigation in A Quantum Processor	JP	2018-521356	JP6873120
Systems And Methods For Creating And Using Higher Degree Interactions Between Quantum Devices	JP	2018-597016	JP6813817

Patent Title	Country	Pub. No.	Pub. Date
Systems And Methods For Quantum Processor Topology	JP	2018-562963	JP702277
Quantum Flux Parametron Based Structures (E.G. Muxes, Demuxes, Shift Registers), Addressing Lines And Related Methods	JP	2020-519439	
Systems And Methods For Superconducting Devices Used In Superconducting Circuits And Scalable Computing	JP	2021-149531	
Systems And Methods For Superconducting Devices Used In Superconducting Circuits And Scalable Computing	JP	2018-557925	JP6945653
Method Of Forming Superconducting Wiring Layers With Low Magnetic Noise	JP	2017-567750	JP6785218
Systems and Methods for Calibrating the Elements of a Quantum Processor	JP	2015-557134	JP6300839
Systems and Methods for Fabrication of Superconducting Integrated Circuits	JP	202007985-4	
Systems and Methods for Fabrication of Superconducting Integrated Circuits	JP	2014561117	JP6316379
Systems and Methods for Superconducting Integrated Circuits	JP	2012542129	JP5876648

Patent Title	Country	Pub. No.	Pub. Date
Systems, Methods and Apparatus for Measuring Magnetic Fields	JP	2011531176	JP5584691
Analog Processor Comprising Quantum Devices	JP	2007547127	JP5172603
Coupling Methods and Architectures for Information Processing	JP	2007-548651	JP5153189
Qubit State Copying	JP	2008908033	JP5039023
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	JP	2011-526207	JP5135507
Systems, Devices and Methods for Analog Processing	JP	2011-501013	JP5296189
Systems, Methods and Apparatus for Local Programming of Quantum Processor Elements	JP	2009539579	JP5139112
Systems, Methods and Apparatus for Calibrating, Controlling and Operating a Quantum Processor	JP	2011510651	JP5400872
Systems, Methods and Apparatus for Qubit Readout	JP	2010525175	JP5351893
Systems And Methods For Quantum Processor Topology	KR	20107006422	KR2009472
Methods of Adiabatic Quantum Computing	KR	2008-7020647	KR1309677

Patent Title	Country	Appl. No.	Issued Patent No.
Analog Processor Comprising Quantum Devices	KR	20077014632	KR1250514
Systems, Methods and Apparatus for Local Programming of Quantum Processor Elements	KR	20097011585	KR1446943
Systems and Methods for Cryogenic Refrigeration	NL	14645860.7	EP3049736
Systems and Methods for Cryogenic Refrigeration	NL	13856325.8	EP2923165
Systems and Methods for Superconducting Flux Qubit Readout	NL	11639080.6	EP2626448
Systems and Methods For Fabrication of Superconducting Integrated Circuits	NL	16179596.6	EP3198865
Systems and Methods For Fabrication of Superconducting Integrated Circuits	NL	10746830.8	EP2401776
Systems and Methods for Superconducting Flux Qubit Readout	SE	11639080.6	EP2626448
Analog Processor Comprising Quantum Devices	SG	250704685-7	SG133715

Patent Title	Country	Patent No.	Patent Ref. No.
Coupling Methods and Architectures for Information Processing	SG	200704709-5	SG133730
Qubit State Copying	SG	200717146-5	SG136711
Systems, Methods and Apparatus for Local Programming of Quantum Processor Elements	SG	200903790-4	SG152568
Systems, Methods and Apparatus for Calibrating, Controlling and Operating a Quantum Processor	SG	201008053-9	SG166290
Physical Realizations of a Universal Adiabatic Quantum Computer	SG	200904256-3	SG155592
Systems And Methods For Surface Code Based Quantum Computing	US	63/390180	
Systems And Methods For Scalable Control Of Superconducting Qubits	US	63/356663	
Systems And Methods For Tunable Parametric Amplification	US	63/265131	
Systems And Methods For Qubit Control	US	63/265605	
Topologically Protected Qubits, Processors With Topologically Protected Qubits, And Methods For Use Of Topologically Protected Qubits	US	63/240675	
Systems And Methods For Duty Cycle Compensation Of A Digital To Analog Converter (DAC)	US	63/275068	
Systems And Methods For Variable Bandwidth Annealing	US	17/148250	
Systems And Methods For Assembling Processor Systems	US	17/026740	
Systems And Methods For Tuning Capacitance in Quantum Devices	US	63/225022	
Tunable Superconducting Filters	US	63/354564	
Systems And Methods For Coupling A Superconducting Transmission Line To An Array Of Resonators	US	17/862905	
Systems And Methods For Improving Performance Of An Analog Processor	US	16/934790	

Patent Title	Country	Appl. No.	Issued Patent No.
Quantum Bit with a Multi-Terminal Junction and Loop with a Phase Shift	US	09/839636	US6687782
Quantum Bit with a Multi-Terminal Junction and Loop with a Phase Shift	US	09/839637	US6619679
Systems And Methods For Coupling Qubits In A Quantum Processor	US	16/935526	
Superconducting Quantum Processor And Method Of Operating Same	US	16/029026	US11295225
Single Flux Quantum Source For Projective Measurements	US	17/854284	
Systems And Devices For Filtering Electrical Signals	US	16/134592	US10897068
Systems And Methods For Magnetic Shielding For A Superconducting Computing System	US	16/218150	US11084637
Superconducting Printed Circuit Board Related Systems, Methods, And Apparatus	US	16/465765	

Patent Title	Country	Appl. No.	Issued Patent No.
Error Reduction And, Or, Correction In Analog Computing Including Quantum Processor-based Computing	US	17/387654	
Error Reduction And, Or, Correction In Analog Computing Including Quantum Processor-based Computing	US	16/275816	US1150418
Systems And Methods For Increasing Analog Processor Connectivity	US	15/418487	US10258622
Systems and Methods For Fabrication Of Superconducting Integrated Circuits	US	17/321819	

Patent Title	Country	Appl. No.	Issued Patent No.
Systems and Methods For Fabrication Of Superconducting Integrated Circuits	US	16/481788	US11038025
Systems And Methods For Degeneracy Mitigation In A Quantum Processor	US	17/378172	
Quantum Processing System for a Superconducting Qubit	US	09/871425	US6809529
Systems And Methods For Degeneracy Mitigation In A Quantum Processor	US	15/771606	US11100415
Superconducting Low Inductance Qubit	US	10/132136	US6979836
Systems And Methods For Creating And Using High-Degree Interactions Between Quantum Devices	US	15/752102	US10467545
Systems And Methods For Quantum Processor Topology	US	16/307382	
Method For Use With Superconducting Devices	US	16/930512	

Patent Title	Country	Appl. No.	Issued Patent No.
Finger Squid Qubit Device	US	10/625818	US6791109
Method Of Fabricating An Electrical Filter For Use With Superconducting-Based Computer Systems	US	11/382278	US10755190
Method Of Operation In A System Including Quantum Flux Parametron Based Structures	US	16/694693	US10744079
Quantum Flux Parametron Based Structures (E.G. Muxes, Demuxes, Shift Registers), Addressing Lines And Related Methods	US	15/726239	US10528886
Systems And Methods For Superconducting Devices Used In Superconducting Circuits And Scalable Computing	US	17/399375	

Patent Title	Country	Appl. No.	Issued Patent No.
Characterization And Measurement Of Superconducting Structures	US	10/321065	US7002174
Systems And Methods For Superconducting Devices Used In Superconducting Circuits And Scalable Computing	US	16/098801	US11127893
Frequency Multiplexed Resonator Input And/or Output For A Superconducting Device	US	17/158484	
Frequency Multiplexed Resonator Input And/or Output For A Superconducting Device	US	15/522731	US10938346
Method Of Forming Superconducting Wiring Layers With Low Magnetic Noise	US	15/503367	US10454015
Systems And Methods For Improving The Performance Of A Quantum Processor To Reduce Intrinsic/Control Errors	US	14/828342	US10352755
Multi-Junction Phase Qubit	US	10/921941	US6784451
Systems and Methods for Electrostatic Trapping of Contaminants in Cryogenic Refrigeration Systems	US	15/502165	US10978803

Patent Title	Country	Appl. No.	Issued Patent No.
Systems And Methods For Removing Unwanted Interactions In Quantum Devices	US	17/007395	
Systems And Methods For Removing Unwanted Interactions In Quantum Devices	US	16/673478	US10789329
Extra Substrate Control System	US	10/134665	US6911864
Systems And Methods For Removing Unwanted Interactions In Quantum Devices	US	15/934074	US10489477
Systems And Methods For Removing Unwanted Interactions In Quantum Devices	US	14/543180	US10002107
Sub-Flux Quantum Generator	US	10/445099	US6885325
Systems And Methods For Fabrication of Superconducting Circuits	US	14/660662	US9634224
Universal Adiabatic Quantum Computing With Superconducting Qubits	US	16/029040	US11348004

Patent Title	Country	Appl. No.	Issued Patent No.
Universal Adiabatic Quantum Computing With Superconducting Qubits	US	14/526139	US10037493
Resonant Controlled Qubit System	US	10/415024	US6906354
Resonant Controlled Qubit System	US	10/801335	US6897468
Resonant Controlled Qubit System	US	10/801340	US6906356
Resonant Controlled Qubit System	US	10/798737	US6990320
Resonant Controlled Qubit System	US	10/801336	US6960380
Quantum Processor with Instance Programmable Qubit Connectivity	US	16/258082	US10891554

Patent Title	Country	Patent No.	Issued Patent No.
Quantum Processor with Instance Programmable Qubit Connectivity	US	14/691268	US9710758
Systems and Devices for Quantum Processor Architectures	US	15/375910	US9875344
Systems and Devices for Quantum Processor Architectures	US	14/863045	US9547826
Systems and Devices for Quantum Processor Architectures	US	14/453883	US9183508
Quantum Logic Using Three Energy Levels	US	10/718925	US6943368
Systems And Methods For Improving The Performance Of A Quantum Processor By Reducing Errors	US	14/940303	US9495644
Systems and Methods for Error Correction in Quantum Computation	US	14/778478	US9870277
Systems and Methods for Error Correction in Quantum Computation	US	14/173301	US8361369
Systems And Methods For Increasing The Energy Scale Of A Quantum Processor	US	14/340291	US9129224
Systems And Methods For Operating A Quantum Processor To Determine Energy Eigenvalues Of A Hamiltonian	US	16/057800	US10552757

Patent Title	Country	Appl. No.	Issued Patent No.
Systems And Methods For Operating A Quantum Processor To Determine Energy Eigenvalues Of A Hamiltonian	US	14/896259	US10058180
Systems and Methods for Cryogenic Refrigeration	US	16/663723	
Conditional Rabi Oscillation Readout for Quantum Computing	US	10/845838	US7230266
Systems and Methods for Testing and Packaging a Superconducting Chip	US	14/105604	US9865648
A Quantum Processor Comprising A Second Set Of Inter-Cell Coupling Devices Where A Respective Pair Of Qubits In Proximity Adjacent Unit Cells Crossed One Another	US	14/050662	US9178154
Systems and Methods for Fabrication of Superconducting Integrated Circuits	US	16/870537	
Superconducting Phase-Charge Qubits	US	10/934049	US7335909
Systems and Methods for Fabrication of Superconducting Integrated Circuits	US	15/679663	US10700256
Systems and Methods for Fabrication of Superconducting Integrated Circuits	US	14/383637	US9768371
Systems, Methods and Apparatus for Planar Evaporator Shields	US	13/615075	US9192085

Patent Title	Country	Pub. No.	Patent No.
Systems and Methods for Superconducting Flux Qubit Readout	US	13/808000	US8854074
Adiabatic Quantum Computation with Superconducting Qubits	US	12/845352	US8503497
Adiabatic Quantum Computation with Superconducting Qubits	US	11/092963	US7418283
Adiabatic Quantum Computation with Superconducting Qubits	US	11/093205	US7135701
Systems and Methods for Magnetic Shielding	US	15/272260	US10325071
Methods of Adiabatic Quantum Computation comprising of Hamiltonian scaling	US	11/625702	US7788152
Systems and Methods for Magnetic Shielding	US	13/863962	US9465401
Systems and Methods for Magnetic Shielding	US	13/050742	US8441330
Systems and Devices for Electrical Filters	US	13/707210	US8670809
Systems and Devices for Electrical Filters	US	13/011667	US8346325

Patent Title	Country	Patent No.	Patent No. (US)
Methods For Quantum Processing	US	11/088650	US7613764
Bus Architectures For Quantum Processing	US	11/088653	US7613765
Systems and Methods for Superconducting Integrated Circuits	US	14/255561	US9355365
Systems and Methods for Superconducting Integrated Circuits	US	12/544518	US8738105
Systems, Methods and Apparatus for Measuring Magnetic Fields	US	14/461203	US9335385
Systems, Methods and Apparatus for Measuring Magnetic Fields	US	12/591893	US8811066
Superconducting Qubits Having a Plurality of Capacitive Couplings	US	11/267493	US7293854
A Superconducting Qubit With a Plurality of Capacitive Couplings	US	11/267478	US7268578
Systems and Methods for Fabrication of Superconducting Integrated Circuits	US	16/568221	US10891755

Patent Title	Country	Appl. No.	Issued Patent No.
Analog Processor Comprising Quantum Devices	US	15/685735	US10140248
Analog Processor Comprising Quantum Devices	US	16/173846	US10346349
Analog Processor Comprising Quantum Devices	US	16/421215	US10891833
Analog Processor Comprising Quantum Devices	US	16/859672	US11093490
Analog Processor Comprising Quantum Devices	US	17/355458	

Patent Title	Country	Appl. No.	Issued Patent No.
Systems and Methods for Fabrication of Superconducting Integrated Circuits	US	15/956404	US10453894
Analog Processor Comprising Quantum Devices	US	11/317838	US7533068
Analog Processor Comprising Quantum Devices	US	11/608214	US7824068
Analog Processor Comprising Quantum Devices	US	12/397999	US8008942
Analog Processor Comprising Quantum Devices	US	13/210369	US8283943
Systems and Methods for Fabrication of Superconducting Integrated Circuits	US	15/289782	US978609
Systems and Methods for Fabrication of Superconducting Integrated Circuits	US	14/589574	US9490296

Patent Title	Country	Patent No.	Issued Patent No.
Analog Processor Comprising Quantum Devices	US	14/727521	US8727527
Systems and Methods for Fabrication of Superconducting Integrated Circuits	US	12/992049	US8951806
Coupling Methods and Architectures for Information Processing	US	11/247857	US7619487
Coupling Methods and Architectures for Information Processing	US	12/575349	US7963806
Qubit State Copying	US	11/411051	US7639035
Systems, devices, and methods for controllably coupling qubits	US	12/618554	US7898282
Input/Output Systems And Devices For Use With Superconducting Devices	US	16/124767	US10468795
Input/Output Systems And Devices For Use With Superconducting Devices	US	15/672506	US10097151

Patent Title	Country	Appl. No.	Issued Patent No.
Input/Output Systems And Devices For Use With Superconducting Devices	US	14/958846	US9762200
Input/Output Systems And Devices For Use With Superconducting Devices	US	13/596801	US8231183
Input/Output Systems and Devices for use with superconducting Devices	US	12/503671	US8278022
Systems, Methods and Apparatus for Superconducting Demultiplexer Circuits	US	12/991289	US8611974
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	US	17/330037	
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	US	16/380751	US11031537
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	US	15/438296	US10290798

Patent Title	Country	Appl. No.	Issued Patent No.
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	US	14/846334	US9607270
Systems, Methods And Apparatus For Active Compensation Of Quantum Processor Elements	US	13/958238	US9152923
Systems, Methods and Apparatus for Active Compensation of Quantum Processor Elements	US	12/991891	US8538566
Systems, Methods and Apparatus for Quasi-Adiabatic Quantum Computation	US	11/777810	US7899852
Systems, Devices and Methods for Controllably Coupling Qubits	US	12/138147	US7880529
Quantum Processor	US	11/198532	US8779360
Systems, Devices And Methods For Analog Processing	US	14/868019	US9408028
Superconducting Shielding for use with an Integrated Circuit for Quantum Computing	US	11/948617	US7687038
Superconducting Shielding for use with an Integrated Circuit for Quantum Computing	US	12/703534	US8247389

Patent Title	Country	Appl. No.	Issued Patent No.
Superconducting Probe Card	US	14/273200	US9170178
Systems, Devices and Methods for Analog Processing	US	13/611672	US8772759
Quantum Processor	US	12/194282	US7932515
Systems, Devices and Methods for Analog Processing	US	12/934254	US8421053
Systems, Methods and Apparatus for Digital-to-Analog Conversion of Superconducting Magnetic Flux Signals	US	12/120354	US8098176
Systems, Methods and Apparatus for Digital-to-Analog Conversion of Superconducting Magnetic Flux Signals	US	13/325785	US8786476
Systems, Methods and Apparatus for Programming Quantum Processor Elements	US	11/950276	US7876248
Systems, Methods and Apparatus for Local Programming of Quantum Processor Elements	US	12/944509	US8355580
Systems, Methods and Apparatus for Local Programming of Quantum Processor Elements	US	13/226219	US8604944
Architecture for Local Programming of Quantum Processor Elements using Latching Qubits	US	12/109847	US7643209
Adiabatic Superconducting Qubit Logic Devices and Methods	US	12/909682	US8018244

Patent Title	Country	Patent No.	Patent Ref. No.
System, Devices and Methods for Coupling Qubits	US	12/822628	US8174305
Systems, Devices and Methods for Interconnected Processor Topology	US	12/013192	US8195586
Systems and Devices for Quantum Processor Architecture	US	12/483871	US8098857
Systems, Methods and Apparatus for Calibrating, Controlling and Operating a Quantum Processor	US	12/931808	US9015215
Input/Output System and Devices for use with Superconducting Devices	US	12/016801	US8441329
Electrical Filter Having A Dielectric Substrate With Wide And Narrow Regions For Supporting Capacitors And Conductive Windings	US	12/916709	US8061991
Systems, Devices and Methods for Controllably Coupling Qubits	US	12/742133	US8102185
Systems, Methods and Apparatus for Cryogenic Refrigeration	US	13/863218	US9134047
Systems, Methods and Apparatus for Cryogenic Refrigeration	US	12/811067	US8484543

Patent Title	Country	Patent No.	Issued Patent No.
Systems, Methods, And Apparatus For Combined Superconducting Magnetic Shielding And Radiation Shielding	US	12/362417	US8228688
Magnetic Vacuum Systems and Devices for use with Superconducting Based Computing Systems	US	12/805341	US8355765
Systems, Devices and Methods for Analog Processing	US	12/266378	US8190548
Systems, Methods, And Apparatus For Multilayer Superconducting Printed Circuit Boards	US	12/247275	US8315678
Method and Apparatus for Evolving a Quantum System Using a Mixed Initial Hamiltonian Comprising Both Diagonal and Off-Diagonal Terms	US	13/529664	US8560470
Systems, Devices, and Methods for Controllably Coupling Qubits	US	12/113753	US7801395
Systems, Methods And Apparatus for Adiabatic Quantum Computation and Quantum Annealing	US	12/473570	US8228663
Systems, Methods, And Apparatus For Qubit State Readout	US	12/236040	US8198231
Physical Realizations of a Universal Adiabatic Quantum Computer	US	12/098348	US8234303
Systems, Methods and Apparatus for Anti-Symmetric Qubit-Coupling	US	12/088347	US7605600
Physical Realizations of a Universal Adiabatic Quantum Computer	US	13/538039	US8162881

Patent Title	Country	Appl. No.	Issued Patent No.
Physical Realizations of a Universal Adiabatic Quantum Computer	US	14/367087	US9934333
Physical Realizations of a Universal Adiabatic Quantum Computer	US	15/967729	US10885459
Physical Realizations of a Universal Adiabatic Quantum Computer	US	17/113847	
Systems, Methods, And Apparatus For Electrical Filters And Input/Output Systems	US	13/416784	US8405468
Systems, Methods, And Apparatus For Electrical Filters And Input/Output Systems	US	17/258332	US8159313
Systems, Methods, And Apparatus For Superconducting Magnetic Shielding	US	12/256330	US7990862
Systems And Methods For Quantum Computing Using Fluxonium Qubits With Kinetic Inductors	WO	US2022/037457	
Systems And Methods For Controlling Quantum Components	WO	US2022/12090	
Kinetic Inductance Devices And Systems And Methods For Fabricating Kinetic Inductance Devices	WO	US2021/091373	