

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT8368340

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
PROMOS TECHNOLOGIES INC.	12/25/2023
RECEIVING PARTY DATA	
Name:	HASDRUBAL IP LLC
Street Address:	8105 RASOR BLVD STE 210
City:	PLANO
State/Country:	TEXAS
Postal Code:	75024
PROPERTY NUMBERS Total: 6	
Property Type	Number
Patent Number:	7446027
Patent Number:	7592219
Patent Number:	7781830
Patent Number:	7016235
Patent Number:	7218564
Patent Number:	7782080
CORRESPONDENCE DATA	
Fax Number:	
<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	8327259569
Email:	do@longhornip.com
Correspondent Name:	DO KIM
Address Line 1:	8105 RASOR BLVD STE 210
Address Line 4:	PLANO, TEXAS 75024
NAME OF SUBMITTER:	DO HYUNG KIM
SIGNATURE:	/Do Hyung Kim/
DATE SIGNED:	01/08/2024
This document serves as an Oath/Declaration (37 CFR 1.63).	
Total Attachments: 3	

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Patent Assignment

This patent assignment ("Assignment") is entered into as of December 25, 2023 (the "Effective Date"), by and between ProMOS Technologies Inc., with a principal place of business at 3A3, No. 1, Lixing 1st Rd., Hsinchu Science Park, Hsinchu 30078, Taiwan, R.O.C. ("Assignor"), and Hasdrubal IP LLC, a Texas Limited Liability Company, with principal place of business at 8105 Rasor Blvd. STE 210, Plano, TX 75024, USA ("Assignee").

For good and valuable consideration, the receipt of which is hereby acknowledged, Assignor hereby irrevocably assigns, sells, grants, transfers and conveys and agrees to assign, sell, grant, transfer, and convey to Assignee, and Assignee hereby accepts and receives, all right, title, and interest throughout the world in and to:

(a) the issued patents and pending patent applications identified on Schedule 1 attached hereto and any issued patent or patent application that directly or indirectly claims or is amended to claim priority to any of the above, in whole or in part (the "Patents");

(b) to the extent not included in (a), all issued patents, rights to inventions and pending and future applications for patents under U.S. law or regulation or of any foreign country with respect to the patentable inventions from which such Patents arise, including without limitation utility patents, utility models, design patents, invention certificates, provisionals, continuations, divisionals, continuations-in-part, reexaminations, reissues, extensions and renewals, in all countries of the world, as well as any patents and patent applications to which any of the Patents or any of the foregoing directly or indirectly claim priority, in whole or in part (subcategories (a) and (b) collectively, the "Patents");

(c) all causes of action (whether known or unknown or whether currently pending, filed or otherwise) and other enforcement rights under or on account of the Patents, including without limitation all causes of action and other enforcement rights for damages, injunctive relief, and any other remedies of any kind for past, current and future infringement;

(d) all rights to collect royalties or other payments under or on account of the Patents and the foregoing subcategory (c).

Assignor agrees upon request (and at the expense) of Assignee to, and if Assignor is unable or unwilling to do so authorizes Assignee to act in Assignor's name to: execute all oaths, assignments, powers, and any other papers necessary to perform Assignor's obligations hereunder, testify in any proceeding, and otherwise take any action, and fully cooperate with Assignee to perform Assignor's obligations hereunder, in each case, related to securing and enforcing Assignee's rights related to this Assignment.

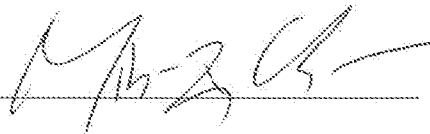
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The terms and conditions of this Assignment will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF, the parties hereto have caused this Assignment to be executed as of the Effective Date. The individuals signing for the parties represent and warrant that he or she has authority to sign for and enter into this Assignment on behalf of the respective parties.

Assignor:

ProMOS Technologies Inc.

By: 

Name: Min-Liang Chen

Title: Chairman & President

Date: Dec. 25, 2023

Assignee:

Hasdrubal IP LLC

By: Khaled Fekih Romdhane

Name: Khaled Fekih-Romdhane

Title: Manager

Date: December 25, 2023

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SCHEDULE I

FAMILY NO.	PATENT PUB NO.	COUNTRY	TITLE
1	7446027	US	METHOD FOR FORMING GATE STRUCTURE WITH LOCAL PULLED-BACK CONDUCTIVE LAYER AND ITS USE
	200830423A (Withdrawn/Lapsed)	TW	METHOD FOR FORMING GATE STRUCTURE WITH LOCAL PULLED-BACK CONDUCTIVE LAYER AND ITS USE
2	7592219	US	METHOD OF FABRICATING CAPACITOR OVER BIT LINE AND BOTTOM ELECTRODE THEREOF
	TWI317545B	TW	METHOD OF FABRICATING CAPACITOR OVER BIT LINE AND BOTTOM ELECTRODE THEREOF
3	7781830	US	RECESSED CHANNEL TRANSISTOR AND METHOD FOR PREPARING THE SAME
	TWI374543B	TW	RECESSED CHANNEL TRANSISTOR AND METHOD FOR PREPARING THE SAME
4	7016235	US	DATA SORTING IN MEMORIES
	TWI272619B	TW	DATA SORTING IN MEMORIES
	CN100541644C (Lapsed)	CN	INTEGRATE CIRCUIT
5	7218564	US	DUAL EQUALIZATION DEVICES FOR LONG DATA LINE PAIRS
	TWI291285B	TW	DUAL EQUALIZATION DEVICES FOR LONG DATA LINE PAIRS
	CN100472652C	CN	DUAL EQUALIZATION DEVICES FOR LONG DATA LINE PAIRS, EQUALIZATION METHOD AND INTEGRATED CIRCUIT STORAGE MEMORIZER
6	7782080	US	HIGH CAPACITIVE LOAD AND NOISE TOLERANT SYSTEM AND METHOD FOR CONTROLLING THE DRIVE STRENGTH OF OUTPUT DRIVERS IN INTEGRATED CIRCUIT DEVICES